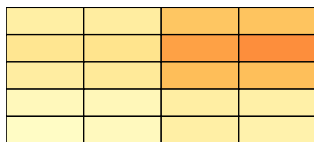
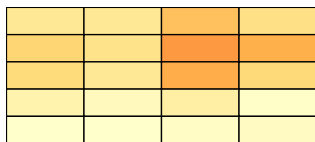


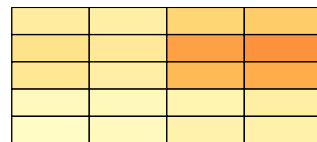
overall



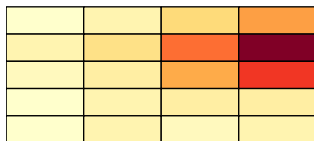
Europe:France



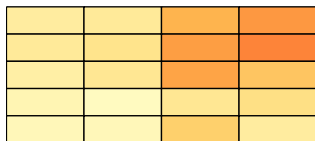
Europe:Germany



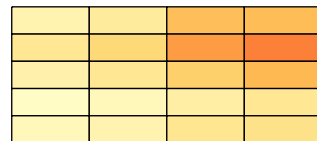
Europe:Italy



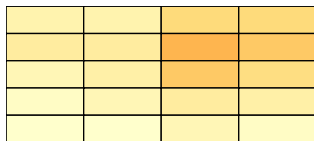
Europe:UK



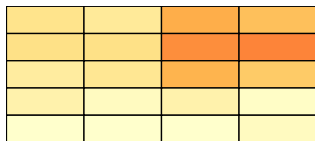
Europe:others



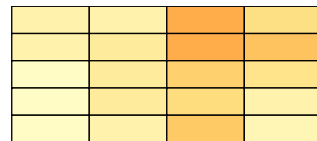
Japan



Russia



USA



Q12\*:MPL Implementation

	One-sided	Topologies	Dynamic process	No
MPICH	0	0.96	1.9	2.9
OMPI	3.8	4.8	5.8	6.7
Intel	7.7	8.7	9.6	11
MVA	12	12	13	14
Cray	15	16	17	18

One-sided

Topologies

Dynamic process

No

Q27\*:Unuseful Features