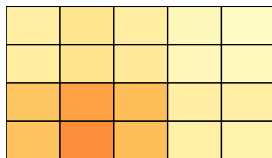
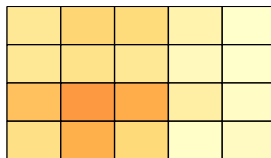


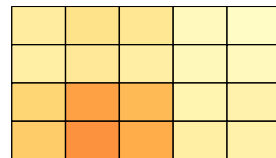
overall



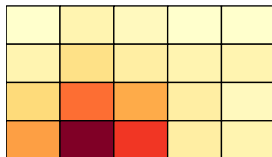
Europe:France



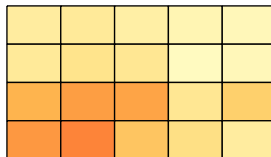
Europe:Germany



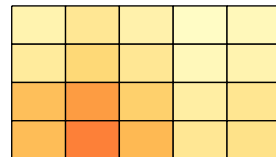
Europe:Italy



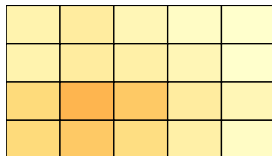
Europe:UK



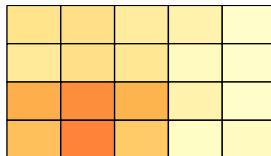
Europe:others



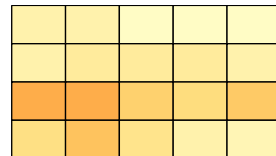
Japan



Russia



USA



Q27*:Useless Features

One-sided	0	0.96	1.9	2.9	3.8
Topologies	4.8	5.8	6.7	7.7	8.7
Dyn. process	9.6	11	12	12	13
No	14	15	16	17	18
	MPICH	OMPI	Intel	MVA	Cray

Q12*:MPI Implementation