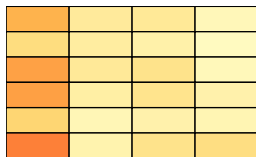
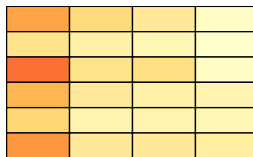


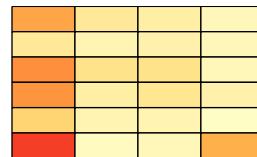
overall



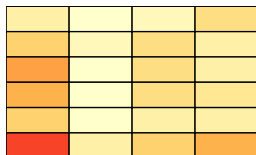
Europe:France



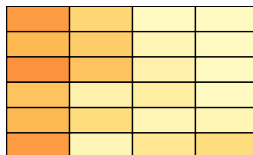
Europe:Germany



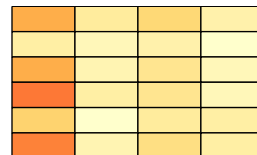
Europe:Italy



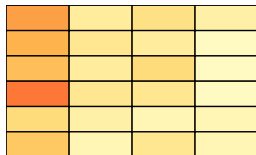
Europe:UK



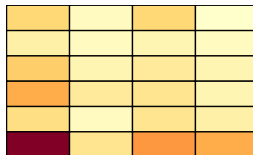
Europe:others



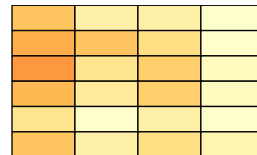
Japan



Russia



USA



Q26*:Missing Semantics

	OMP	Pthread	CUDA	No
Latency hiding	0	0.79	1.6	2.4
End-points	3.2	4	4.8	5.6
Resilience	6.3	7.1	7.9	8.7
Additional opt	9.5	10	11	12
Another API	13	13	14	15
MPI provides all	16	17	17	18
	OMP	Pthread	CUDA	No
	Q22*:MPI+X			