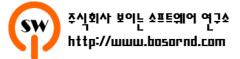
- 기능 명세 -Boot Loader

조 용 진

(drajin.cho@bosornd.com)





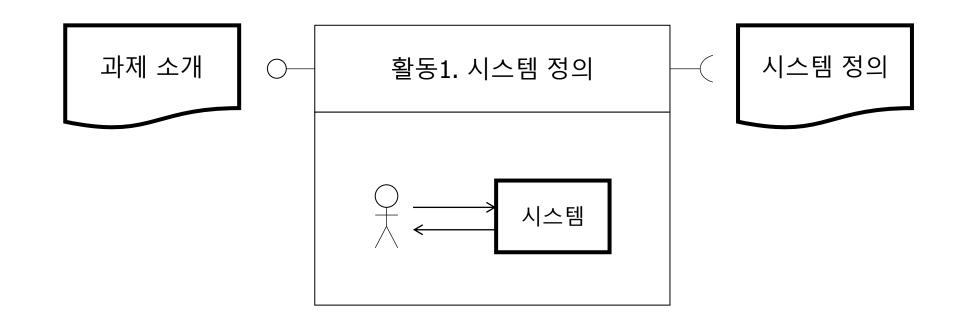
목적

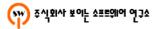
개발하고자 하는 시스템의 경계를 정의한다.

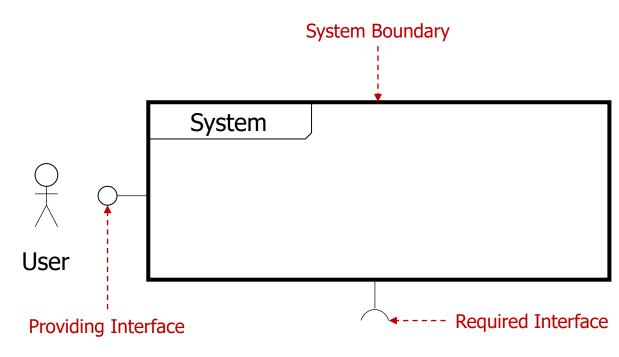
시스템과 상호 작용하는 외부 시스템(이해관계자 포함)을 정의한다.

시스템과 외부 시스템과의 관계를 파악한다.

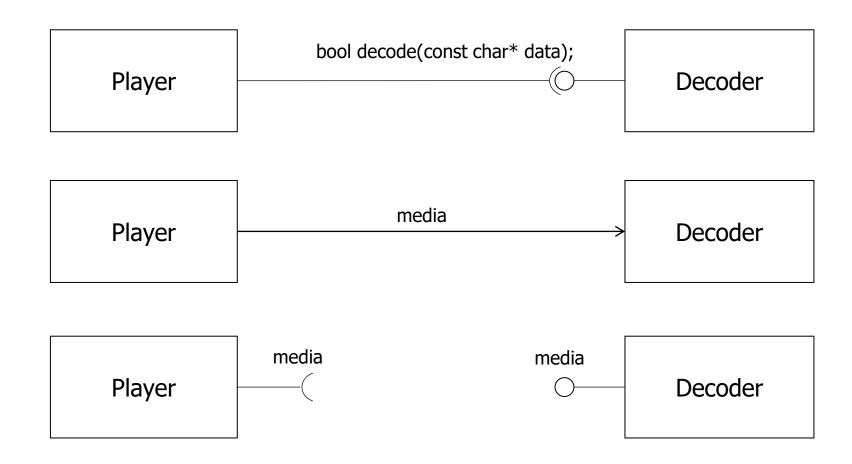
시스템의 동작 및 사업 환경을 파악한다.

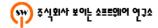


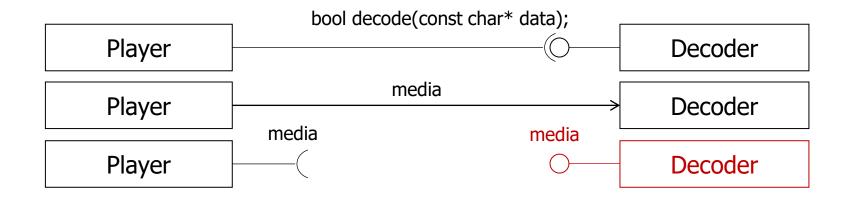


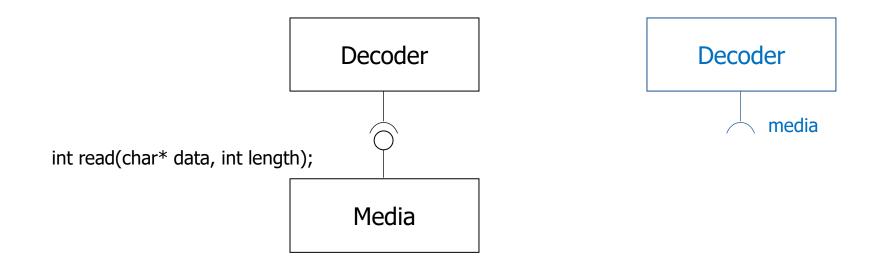


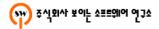
External System











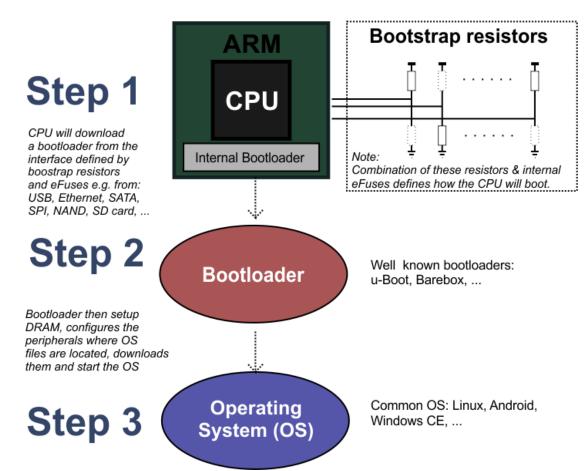
Providing interface: system will determine the interface and the external using it will be developed later.

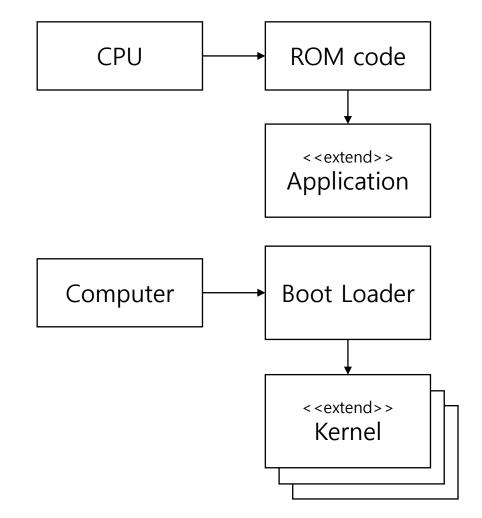
Required interface: the external system already exists and provides interface.

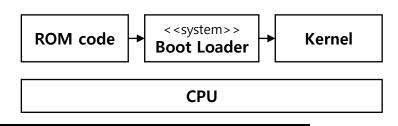
System uses it. The external system will not be changed later.

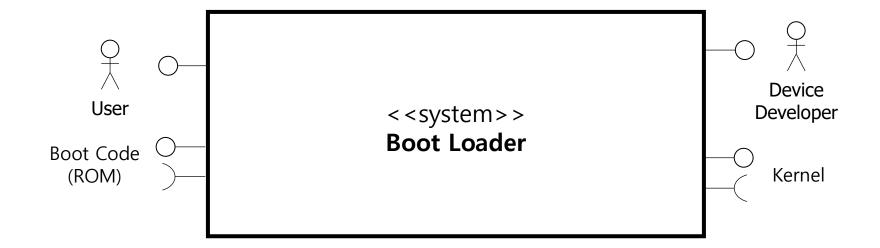


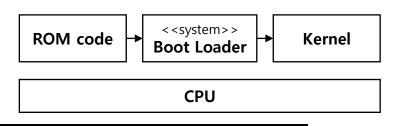
How does ARM Boot?

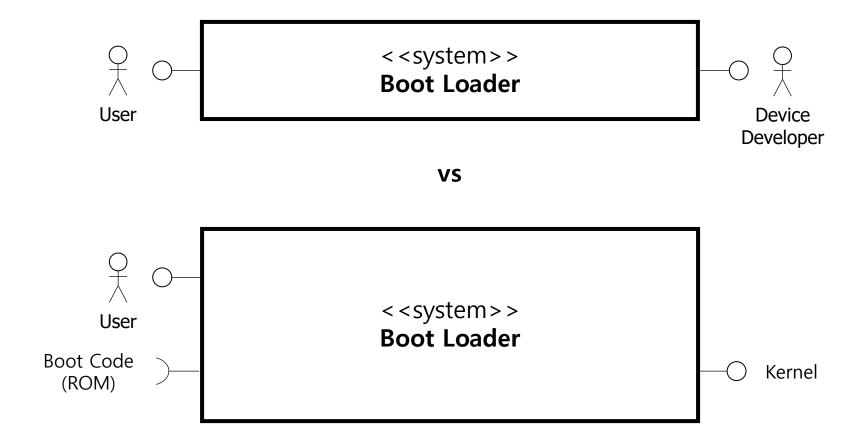


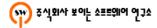


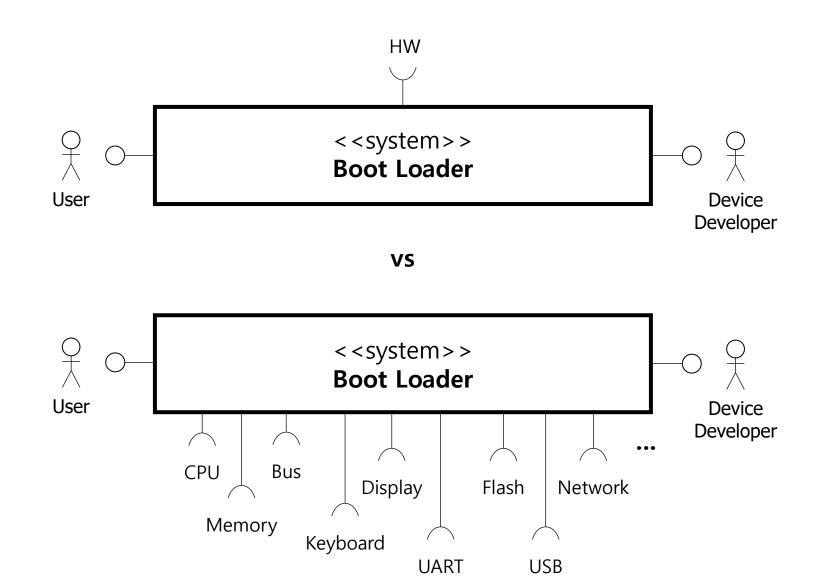


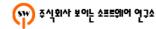


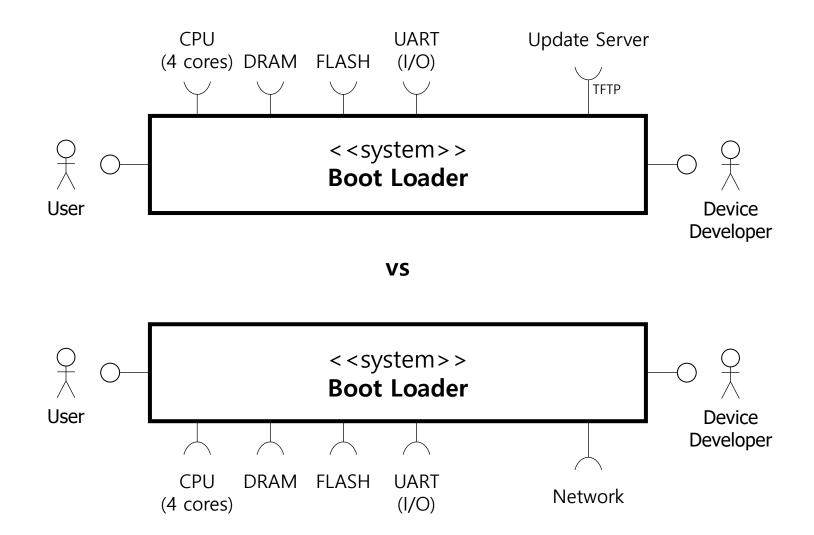


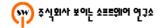


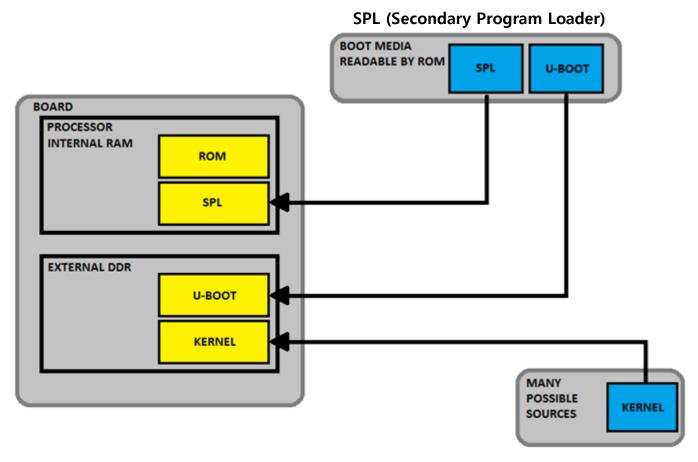






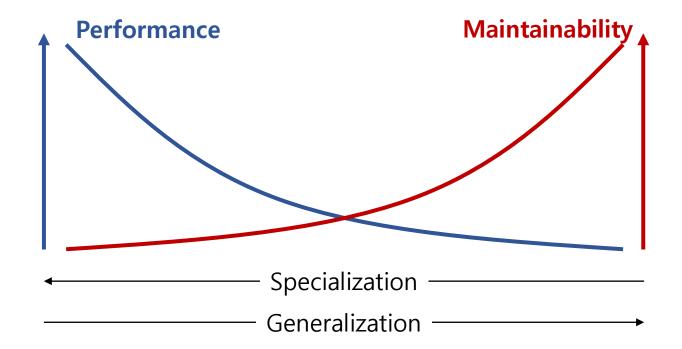


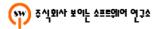




Size of internal RAM or boot media can be a technical constraint.

Business Driver

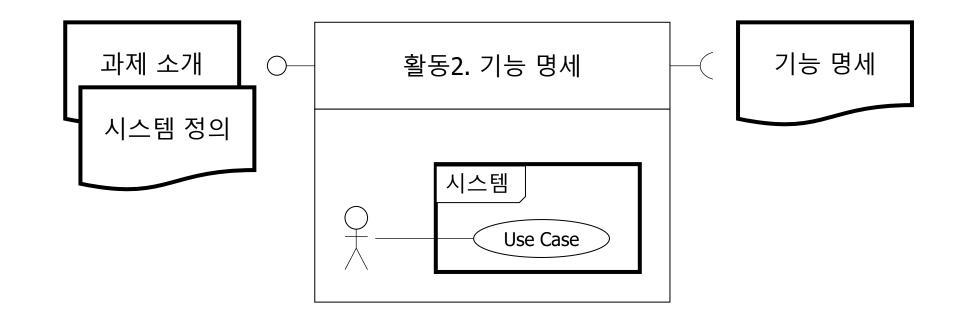


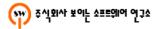


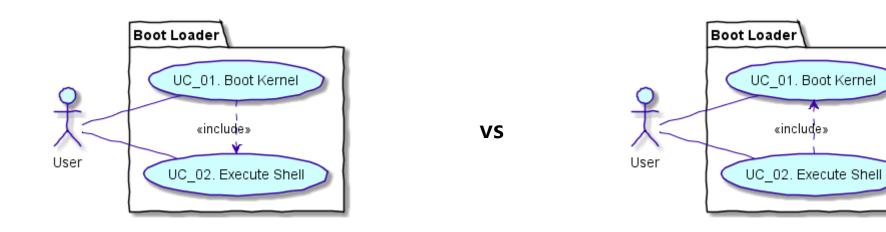
목적

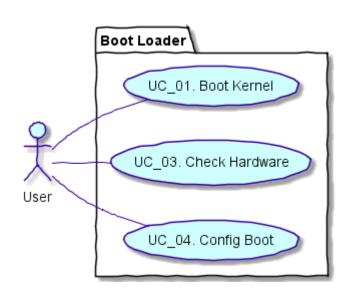
시스템의 기능을 명세한다.

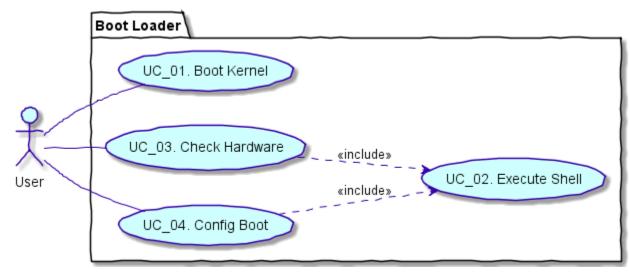
외부 시스템의 요청에 대한 시스템의 반응을 명세한다.



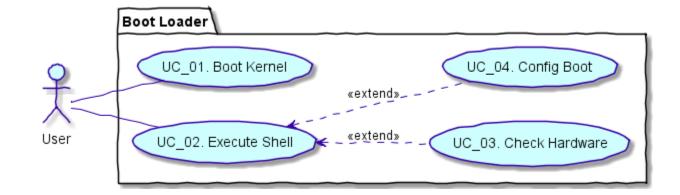


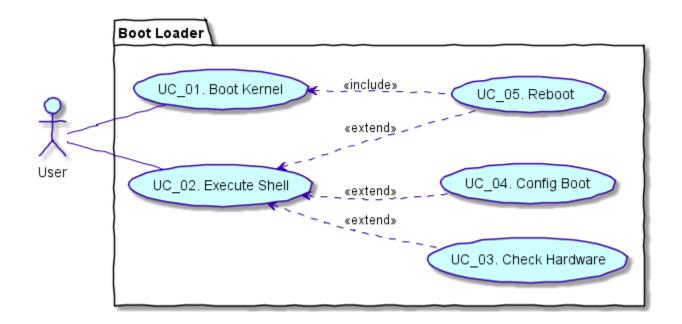


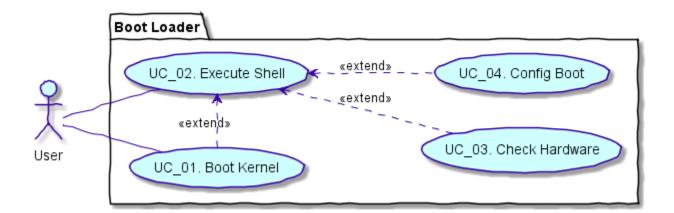


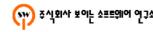


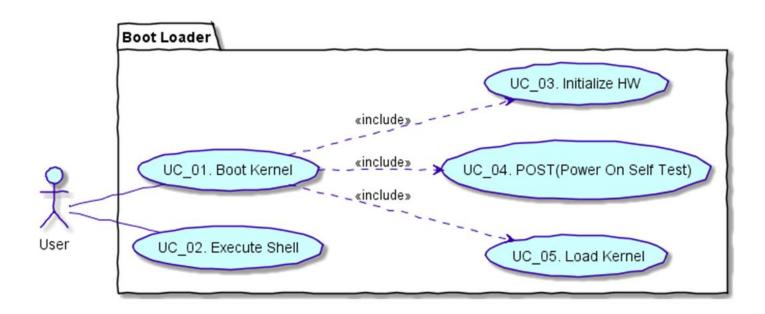
VS









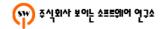


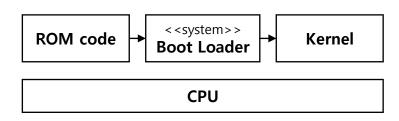
A use case is a metaphor to specify requirements.

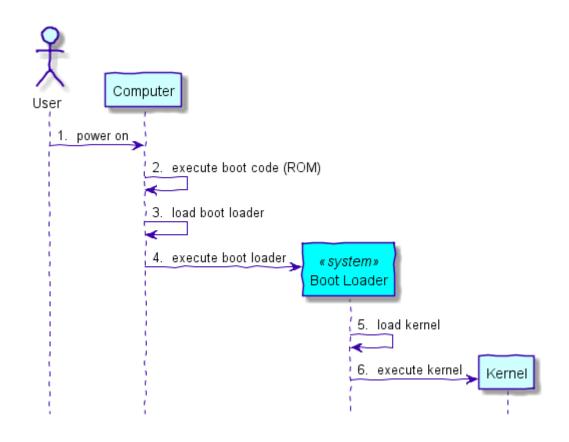
It is better identified by the user's intent,

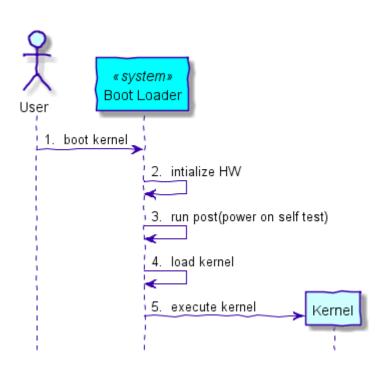
rather than internal functionalities or steps.

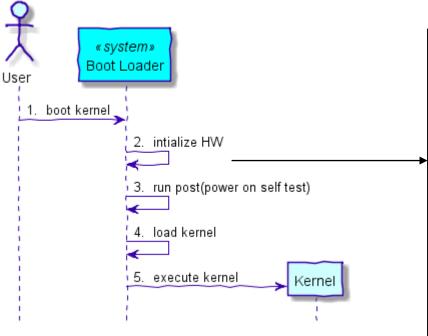
Don't design or organize use cases based on internal functionality.







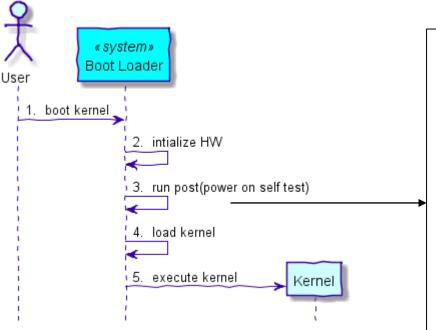




Initialization Flow – common/board_f.c, board_r.c

```
static const init_fnc_t init_sequence_f[] = {
    setup_mon_len,
#ifdef CONFIG_OF_CONTROL
    fdtdec_setup,
#endif
#ifdef CONFIG_TRACE_EARLY
    trace_early_init,
#endif
    initf_malloc,
    log_init,
    initf_bootstage,
#ifdef CONFIG_BLOBLIST
    bloblist_init,
#endif
    clear_bss.
    NULL,
void board_init_f(ulong boot_flags)
   gd->flags = boot_flags;
   gd->have_console = 0;
    if (initcall_run_list(init_sequence_f)) hang();
```

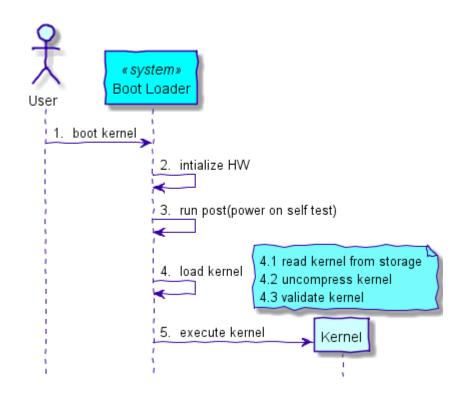
```
static const init_fnc_t init_sequence_r[] = {
    initr_trace,
    initr_reloc.
#if defined(CONFIG_ARM) || defined(CONFIG_RISCV)
    initr_caches,
#endif
    initr_reloc_global_data,
#if defined(CONFIG_SYS_INIT_RAM_LOCK) && ₩
     defined(CONFIG_E500)
    initr_unlock_ram_in_cache,
#endif
    initr_barrier,
    initr_malloc,
    run_main_loop,
void board_init_r(gd_t *new_gd, ulong dest_addr)
    gd = new_gd;
   gd->flags &= ~GD_FLG_LOG_READY;
    if (initcall_run_list(init_sequence_r)) hang();
   /* NOTREACHED - run_main_loop() does not return */
    hang();
```

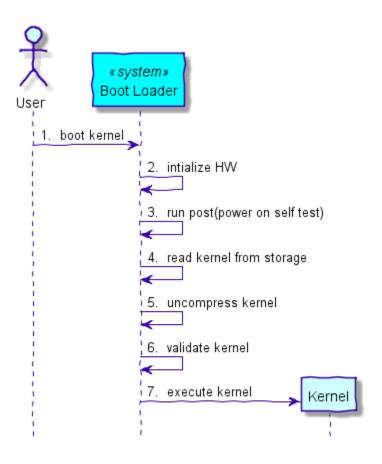


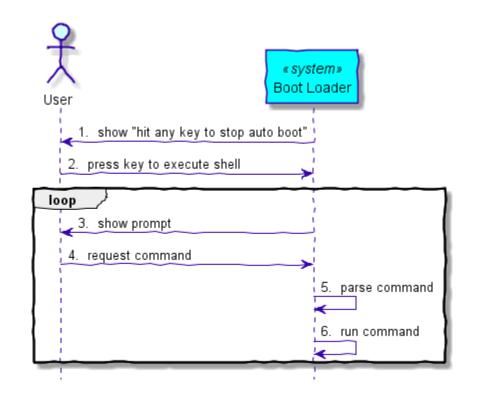
Initialization Flow – post/post.c, tests.c

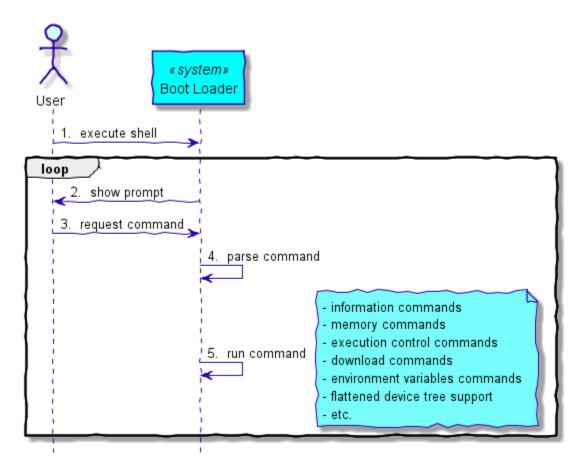
```
struct post_test post_list[] =
#if CONFIG_POST & CONFIG_SYS_POST_OCM
        "OCM test",
        "ocm".
        "This test checks on chip memory (OCM).",
        POST_ROM | POST_ALWAYS | POST_PREREL
        POST_CRITICAL | POST_STOP,
        &ocm_post_test.
        NULL,
        NULL.
        CONFIG_SYS_POST_OCM
   }.
#endif
#if CONFIG_POST & CONFIG_SYS_POST_CACHE
        "Cache test",
        "cache",
        "This test verifies the CPU cache operation.",
        POST_RAM | POST_ALWAYS,
        &cache_post_test,
        NULL,
        NULL,
        CONFIG SYS POST CACHE
#endif
```

```
int post_run(char *name, int flags)
   unsigned int i, last;
   int test_flags[POST_MAX_NUMBER];
   post_get_flags(test_flags);
   if (post_bootmode_get(&last) & POST_POWERTEST) {
       if (last & POST_FAIL_SAVE) {
       last &= ~POST_FAIL_SAVE;
       gd->flags |= GD_FLG_POSTFAIL;
   if (last < post_list_size &&</pre>
       (flags & test_flags[last] & POST_ALWAYS) &&
      (flags & test_flags[last] & POST_MEM)) {
       post_run_single(post_list + last,
           test_flags[last], flags | POST_REBOOT,
           last);
       for (i = last + 1; i < post_list_size; i++) {
           if (gd->flags & GD_FLG_POSTSTOP) break;
           post_run_single(post_list + i.
                test_flags[i], flags, i);
```

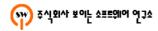




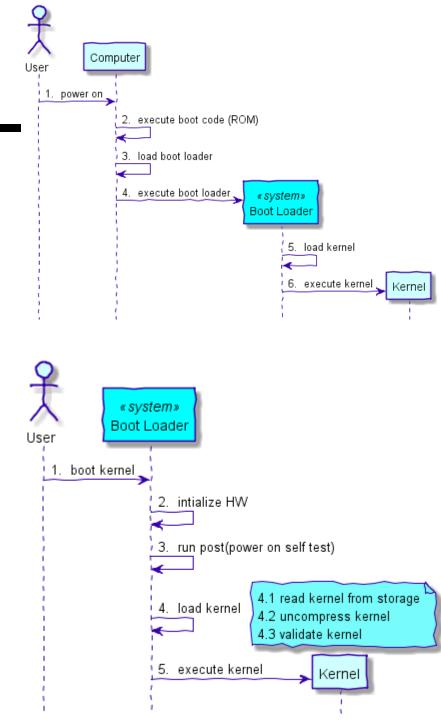


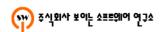


What the system needs to do for actor's request is specified in more detail, rather than user interface.

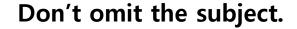


UC_01	Boot kernel		
Description	System loads and executes kernel.		
Actor	User		
Pre-condition	[DEVICE_OFF] Device is powered off.		
Post-condition	[KERNEL] Kernel is executed.		
Basic Flow	 User requests system (boot loader) to boot kernel. 1.1 User powers on device. 1.2 CPU executes boot code (ROM). 1.3 Boot code loads boot loader. 1.4 Boot code executes boot loader. 2. System initializes HW. 3. System runs POST (Power On Self Test). 4. System loads kernel. 4.1 System reads kernel from storage. 4.2 System decompresses kernel. 4.3 System validates kernel. 5. System executes kernel. 		
Additional Flow	2. If HW initialization fails, system halts with a notification.		
Additional Flow	4.3 If kernel validation fails, system loads the previously successful kernel.		

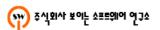




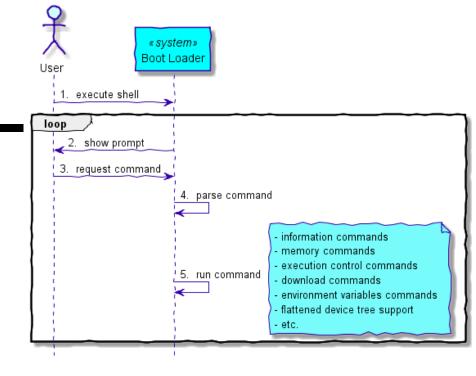
UC_02	Execute Shell	
Description	System provides CLI (Command Line Interface) to user.	
Actor	User	
Pre-condition	[DEVICE_OFF] Device is powered off.	
Post-condition	[SHELL] System provides CLI to user.	
Basic Flow	1. User requests system (boot loader) to provide CLI.	
	1.1 System shows "press any key to stop autoboot" with timeout.	
	1.2 User presses any key to stop autoboot within timeout.	[
	2. System shows prompt.	
	3. User requests system to execute command.	
	4. System parses command.	Ī
	5. System runs command.	
	6. System goes to 2 (loop).	
Additional Flow	1.2 Otherwise, system continues to boot kernel (UC_01).	
Additional Flow	3. System provides many commands, extended by UC_03, UC_04 and UC_01, etc.	



Don't use the passive voice.



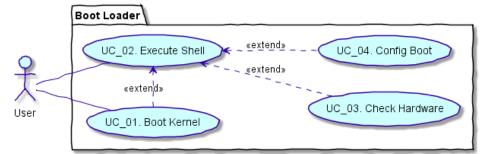
3. System gets command from user.

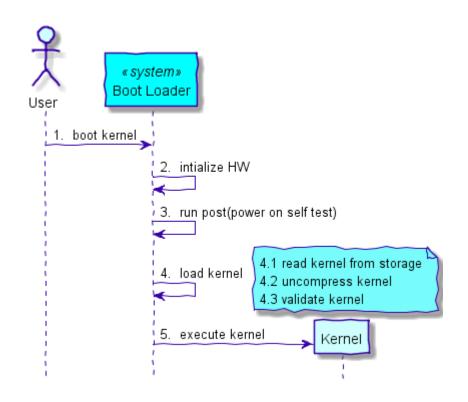


No need to specify user interface in detail.

UI can be easily changed → UI modifiability.

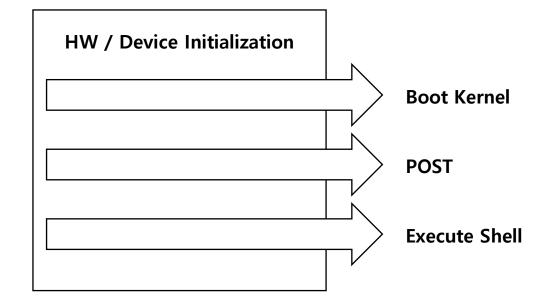
Good to specify extension point. → CLI modifiability.

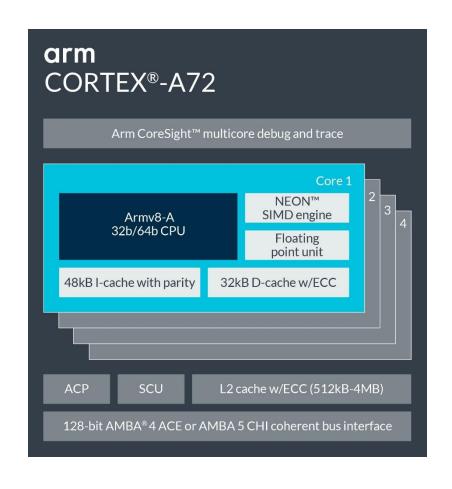




Architectural Concern

- Concurrency -

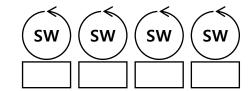




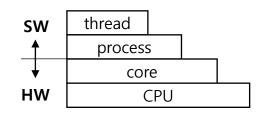
Architectural Concern

- Concurrency -

Concurrent Executions



Deployment Unit



Service		
Container		
Virtual Machin	ie	

Device Cloud

