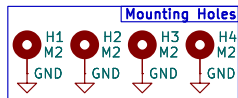
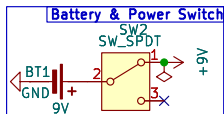
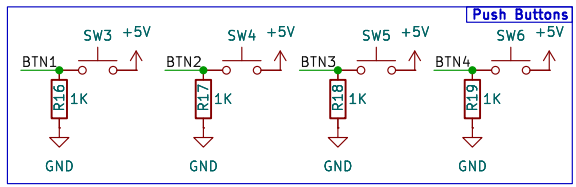


ATmega328	Peripherals
A2	PC2 Btn1
A3	PC3 Btn2
D2	PD2 Btn3
D3	PD3 Btn4
D4	PD4 A
D5	PD5 B
D6	PD6 C
D7	PD7 D
D8	PB0 E
D9	PB1 F
D10	PB2 G
D11	PB3 DP
D12	PB4 dig1
D13	PB5 dig2
A0	PC0 dig3
A1	PC1 dig4



Designed by:

Bostang Palaguna

Bandung Institute of Technology (ITB)

Sheet: /

File: DigitalClock_ATMega328.kicad_sch

Title: Digital Clock

Size: User

Date: 2024-12-08

Rev: 2.0

KiCad E.D.A. 8.0.7

Id: 1/2

