LSR Internet-Draft Intended status: Standards Track Expires: 18 August 2025 R. Chen
D. Zhao
ZTE Corporation
P. Psenak
K. Talaulikar
Cisco Systems
L. Gong
China mobile
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Prefix Flag Extension for OSPFv2 and OSPFv3 draft-ietf-lsr-ospf-prefix-extended-flags-06

Abstract

Each OSPF prefix can be advertised with an 8-bit field to indicate specific properties of that prefix. However, all the OSPFv3 Prefix Options bits have already been assigned and only a few bits remain unassigned in the flags field of the OSPFv2 Extended Prefix TLV.

This document solves th<u>ise</u> problem of insufficient prefix options bits by defining variable-length Prefix Attribute Flags Subsub-TLV for OSPF. This sub-TLV is applicable to OSPFv2 and OSPFv3.

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Commenté [MB1]: Should we have configuration parameter to control the use of the flags (e.g., rfc8362#appendix-A)?

Commenté [MB2]: Consistent with the use in rfc8362.

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1. Introduction

Each OSPF prefix can be advertised with an 8-bit field to indicate specific properties of that prefix. This is done using the OSPFv3 Prefix Options (Appendix A.4.1.1 of [RFC5340]) and the flags Flags field in the OSPFv2 Extended

Prefix TLV (Section 2.1 of [RFC7684]). The rest of this document refers to these

 $8\mbox{-bit fields}$ in both OSPFv2 and OSPFv3 as the "existing fixed-size prefix attribute flags".

However, all the OSPFv3 Prefix Options bits have already been assigned (see "OSPFv3 Prefix Options (8 bits)" OSPFv3 Prefix Options

IANA registries registry [IANA-OSPFv3-P0].

and Also, only 5 bits remain unassigned (at the time of publication of

document) in the $\underline{\texttt{F}}$ -lags field of the OSPFv2 Extended Prefix TLV (see "OSPFv2 Extended Prefix TLV Flags" IANA registries registry [IANA-OSPFv2-EPF]).

This document solves the problem of insufficient flag bits for the signaling of prefix properties in OSPF by defining variable-length Prefix Attribute Flags Sub-TLVs for OSPFv2 and OSPFv3.

1.1. Requirements Language

The key words "MUST", "MUST NOT", "REQUIRED", "SHALL", "SHALL NOT", "SHOULD", "SHOULD NOT", "RECOMMENDED", "NOT RECOMMENDED", "MAY", and "OPTIONAL" in this document are to be interpreted as described in BCP 14 [RFC2119] [RFC8174] when, and only when, they appear in all capitals, as shown here.

2. Variable-Length Prefix Attribute Flags Sub-TLVs

This document defines variable-Length Prefix Attribute Flags $\underline{\operatorname{Sub}}\underline{\operatorname{Sub}}$ - $\operatorname{TLV}_{\overline{\mathbf{S}}}$

Commenté [MB3]: Double check. This is to help reader to find where to look.

Commenté [MB4]: Double check. This is to help reader to find where to look.

Commenté [MB5]: Be consistent with the IANA registry name

Commenté [MB6]: Split the long sentence

Commenté [MB7]: Should we have a recommendation whether the remaining flags are assigned first vs. use of the sub-TLV?

for OSPFv2 and OSPFv3. These Such Sub-TLVs specifies the variable-flag fields to advertise additional attributes associated with OSPF prefixes._i.e., the The advertisement and processing of the existing fixedsize prefix attribute flags remains unchanged. The format of OSPFv2/OSPFv3 Prefix Attribute Flags sSub-TLVs is shown in Figure 1.÷ Ω 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6 7 8 9 0 1 Type Length Prefix Attribute Flags (Variable) Figure 1: Format of OSPFv2/OSPFv3 Prefix Attribute Flags Sub-TLV where: Type (2 octets): 11 for OSPFv2 and 37 for OSPFv3. Length (2 octets): Variable, dependent on the included Prefix Attribute Flags. This indicates the length of the value portion in bytes. The length MUST be a multiple of 4 octets. If the length is not a multiple of 4 octets, the Link State Advertisement (LSA) MUST be considered malformed. Prefix Attribute Flags (:- Variable):- The extended flag field. This field contains a variable number of 32-bit flags. Currently, no bits defined in this document. Unassigned bits MUST be set to zero on transmission and MUST be ignored on receipt. An implementation MUST limit the length of the sub-TLV so as to

Commenté [MB8]: Exapnd

Commenté [MB9]: While still being multiple of 4.

Commenté [MB10]: What is meant here?

Commenté [MB11]: Should we indicate/remind the behavior when supplied in other TLVs?

of the OSPFv2 Extended Prefix TLV as defined in [RFC7684].

 ${\tt OSPFv2}$ Prefix Attribute Flags ${\tt Sub}{\tt Sub}{\tt TLV}$ is advertised as a ${\tt Sub}{\tt Sub}{\tt TLV}$

signal the bits that are set to 1. Defined prefix flags that are not

transmitted due to being beyond the transmitted length ${\tt MUST}$ be

treated as being set to 0. If any trailing 32-bit block(s) are received without any bit being set in it, then the LSA MUST be

OSPFv3 Prefix Attribute Flags ${\color{red} \mathtt{Sub}}\mathtt{\underline{Sub}}\mathtt{\underline{-TLV}}$ is advertised as a ${\color{red} \mathtt{Sub}}\mathtt{\underline{sub}}\mathtt{\underline{-TLV}}$ of

the following OSPFv3 TLVs:

considered malformed.

* Inter-Area-Prefix TLV (Section 3.4 of [RFC8362]).

- * Intra-Area-Prefix TLV (Section 3.7 of [RFC8362]).
- * External-Prefix TLV (Section 3.6 of [RFC8362]).
 - * SRv6 Locator TLV [RFC9513].

When multiple instances of an_the OSPFv2/OSPFv3 Prefix Attribute Flags soub-TLVs are received within the same TLV, an implementation MUST use only the first occurrence of the soub-TLV and MUST ignore all subsequent instances of the soub-TLV.

3. Backward Compatibility

The Prefix Attribute Flags $\underline{Sub}\underline{sub}\text{-TLV} \underline{s}$ defined in this document does not

introduce any backward compatibility issues. An implementation that does not recognize the OSPFv2/OSPFv3 Prefix Attribute Flags <u>Subsub</u>-TLV <u>MUST</u> silently ignore the <u>Subsub</u>-TLV.

4. Acknowledgements

The authors thank Shraddha Hegde, Changwang Lin, Tom $\mathsf{Petch}_{\underline{I}}$ and many others for their suggestions and comments.

The authors would like to thank Acee Lindem for aligning the terminology with existing OSPF documents and for editorial improvements.

5. IANA Considerations

This document requests allocation for the following $\frac{\text{registry}\text{registries}}{\text{5.1 OSPFv2}}.$

5.1.1 OSPFv2 Prefix Attribute Flags Sub-TLV Registry

This document requests IANA to make permanent the early allocation of the following codepoint for the "OSPFv2 Prefix Attribute Flags" in the "OSPFv2 Extended Prefix TLV Sub-TLVs" registry to be made permanent:

Value	Description	Reference			
11	OSPFv2 Prefix Attribute Flags	This document			

 $5.1.\frac{12}{2}$. OSPFv2 Prefix Extended Flags Field Registry

This document requests the creation of "OSPFv2 Prefix Extended Flag Field" Registry under "Open Shortest Path First v2 (OSPFv2)
Parameters" registry group. The registry defines the bits in the Prefix Attribute

Flags field in the OSPFv2 Prefix Attribute Flags $\underline{\text{Sub}}\underline{\text{sub}}\text{-TLV}$ as specified

in <u>section Section 2</u>. The bits are to be allocated via IETF Review [RFC8126]. Each bit definition will include:

Commenté [MB12]: May be list them in the same 8362

Commenté [MB13]: Should this be logged?

Commenté [MB14]: «Unrecognized TLVs and sub-TLVs are ignored » is already stated in rfc8362#section-6

- * Bit number (counting from bit 0 as the most significant bit)
 - * Description
 - * Reference

No bits are currently defined. Bits 0-31 are to be initially marked as "Unassigned". IANA is requested to add subsequent blocks of 32 bits upon exhaustion of the preceding 32-bit block.

5.2 OSPFv3

5.2.1. OSPFv3 Prefix Attribute Flags Sub-TLV Registry

This document requests IANA to make permanent the early allocation of the following codepoint for the "OSPFv3 Prefix Attribute Flags" in the "OSPFv3 Extended-LSA Sub-TLVs" registry:

Value	Description	Reference	
37	OSPFv3 Prefix Attribute Flags	This document	

5.2.\frac{1}{2}. OSPFv3 Prefix Extended Flags Field Registry

This document requests the creation of "OSPFv3 Prefix Extended Flag Field" registry under "Open Shortest Path First v3 (OSPFv3)"

Parameters registry group. The registry defines the bits in the Prefix Attribute

Flags field in the OSPFv2 Prefix Attribute Flags $\frac{\text{Sub}}{\text{Sub}}$ -TLV as specified

in <u>S</u>ection 2. The bits are to be allocated via IETF Review [RFC8126]. <u>Each bit definition will include:</u>

- * Bit number (counting from bit 0 as the most significant bit)
- * Description
- * Reference

No bits are currently defined. Bits 0-31 are to be initially marked as "Unassigned". IANA is requested to add subsequent blocks of 32 bits upon exhaustion of the preceding 32-bit block.

6. Security Considerations

Procedures and protocol extensions defined in this document do not affect the OSPFv2 or OSPFv3 security models. See the "Security Considerations" Section of [RFC7684] for a discussion of OSPFv2 TLV-encoding considerations, and the "Security Considerations" Section of [RFC8362] for a discussion of OSPFv3 security.

- 7. References
- 7.1. Normative References

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Commenté [MB15]: Not sure if this is assumed, but should we be explicit that groups of bits (2 bits) may be allocated for one single purpose?

Commenté [MB16]: Group the request per version

Commenté [MB17]: Idem as previous comment

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7.2. Informative References

[IANA-OSPFv2-EPF]

"OSPFv2 Extended Prefix TLV Flags",

[IANA-OSPFv3-PO]

"OSPFv3 Prefix Options (8 bits)",

Authors' Addresses

Ran Chen
ZTE Corporation
Nanjing
China
Email: chen.ran@zte.com.cn

Detao Zhao ZTE Corporation Nanjing China

Email: zhao.detao@zte.com.cn

Peter Psenak Cisco Systems Slovakia Email: ppsenak@cisco.com

Ketan Talaulikar Cisco Systems
India
Email: ketant.ietf@gmail.com

Liyan Gong China mobile China Email: gongliyan@chinamobile.com