

reg_delay_sel_o_clk

reg_delay_sel_o_clk_n

31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16
15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0

reg_delay_sel_o_ceb

reg_delay_sel_o_dqs_oen0