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|------|------|------|------|------|------|------------------|------------------|------------------|-------------------|-----------------|--------------------|------|------|------|-----------|
| RSVD | RSVD | RSVD | RSVD | RSVD | RSVD | RSVD | RSVD | RSVD | RSVD | RSVD | RSVD | RSVD | RSVD | RSVD | RSVD |
| 31 | 30 | 29 | 28 | 27 | 26 | 25 | 24 | 23 | 22 | 21 | 20 | 19 | 18 | 17 | 16 |
| 15 | 14 | 13 | 12 | 11 | 10 | 9 | 8 | 7 | 6 | 5 | 4 | 3 | 2 | 1 | 0 |
| RSVD | RSVD | RSVD | RSVD | RSVD | RSVD | reg_int_vcnt_clr | reg_int_hcnt_clr | reg_int_fifo_clr | reg_int_frame_clr | reg_int_mem_clr | reg_int_normal_clr | RSVD | RSVD | RSVD | rfifo_pop |