MSB							LSB	MSB							LSB	MSB							LSB	MSB							LSB
ACR0								ACR1								ACR2								ACR3							
7	6	5	4	3	2	1	0	7	6	5	4	3	2	1	0	7	6	5	4	3	2	1	0	7	6	5	4	3	2	1	0

unused

AMR0								AMR1								AMR2								AMR3							
7	6	5	4	3	2	1	0	7	6	5	4	3	2	1	0	7	6	5	4	3	2	1	0	7	6	5	4	3	2	1	0

unused

ID.28 ID.27 ID.28 ID.25 ID.24 ID.23 ID.22 ID.21   ID.20 ID.19 ID.18 ID.17 ID.16 ID.15 ID.14 ID.13   ID.12 ID.11 ID.10 ID.9 ID.8 ID.7 ID.6 ID.5   ID.4 ID.3 ID.2 ID.1 ID.0 RTR

message bit
acceptance code bit ──→ = 1
acceptance mask bit ──→ ≥ 1 ──→ & ──→ logic 1 = accepted
logic 0 = not accepted

ACR = Acceptance Code Register
AMR = Acceptance Mask Register

Single filter configuration, receiving extended frame messages