Memory Address

SRC ADDR START: Address 33

SRC XINCR: 0

SRC YINCR: 0

SRC XCOUNT: 3

SRC YCOUNT: 5

Addr-

30 l

60

90

120

150

180

210

DEST ADDR START: Address 79

DEST XINCR: 1

DEST XCOUNT: 3

DEST YINCR: 28

→ Addr

29

59

89

119

149

179

209