

UART\_CLK

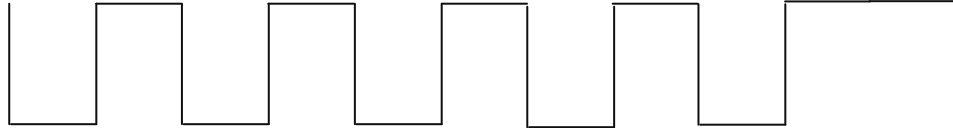


1001

1002

1001

Stop Bit



Start Bit  
1000

1005

1003

1002

0x55