Register bit field layout:

se_trng_0_rosc_en	RSVD	RSVD	RSVD	RSVD	se_trng_0_ht_od_en		se_trng_0_ht_apt_c							
31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16
15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0

se_trng_0_ht_rct_c

se_trng_0_cp_ratio