MSB ACR0 LSB | MSB ACR1 | LSB ACR1 | LSB ACR3

filter 1

ACR0	ACR1	ACR1	ACR3
7 6 5 4 3 2 1 0	7 6 5 4	3 2 1 0	3 2 1 0

AMR0	AMR1	AMR1	AMR3
7 6 5 4 3 2 1 0	7 6 5 4	3 2 1 0	3 2 1 0

message

ID.28 ID.27 ID.28 ID.25 ID.24 ID.23 ID.22 ID.21	ID.20 ID.19 ID.18 RTR	DB1.7 DB1.6 DB1.5 DB1.4	DB1.3 DB1.2 DB1.1 DB1.0

AMR2	AMR3
7 6 5 4 3 2 1 0	7 6 5 4

filter 2

ACR2	ACR3
7 6 5 4 3 2 1 0	7 6 5 4

MSB ACR2 LSB | MSB ACR3

ACR = Acceptance Code Register
AMR = Acceptance Mask Register

acceptance mask bit
acceptance code bit

= 1

≥ 1

&

message bit

= 1

≥ 1

&

acceptance code bit
acceptance mask bit

≥ 1

logic 1 = accepted
logic 0 = not accepted

DBX.Y means data byte X, bit Y.

Dual filter configuration, receiving standard frame messages