

RSVD					RSVD					RSVD					RSVD					RSVD					fclk_sw_state					RSVD					RSVD					RSVD					RSVD					RSVD					RSVD				
31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16																																												
15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0																																												
RSVD					RSVD					RSVD					RSVD					RSVD					RSVD					reg_bclk_sw_done_cnt					RSVD					sts_bclk_prot_done					reg_bclk_div_bypass					reg_bclk_div_act_pulse									