

## Memory Address

Addr →

0	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	1	0	0	-	-	-	-	-	29
30	-	-	-	1	1	1	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	1	0	0	-	-	-	-	-	59
60	-	-	-	1	0	0	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	1	1	1	-	-	-	-	-	89
90	-	-	-	1	1	1	-	-	-	-	-	-	-	1	1	1	-	-	-	-	-	1	0	0	-	-	-	-	-	119
120	-	-	-	1	0	0	-	-	-	-	-	-	-	0	0	1	-	-	-	-	-	1	1	1	-	-	-	-	-	149
150	-	-	-	1	0	0	-	-	-	-	-	-	-	1	1	1	-	-	-	-	-	-	-	-	-	-	-	-	-	179
180	-	-	-	-	-	-	-	-	-	-	-	-	-	0	0	1	-	-	-	-	-	-	-	-	-	-	-	-	-	209
210	-	-	-	-	-	-	-	-	-	-	-	-	-	0	0	1	-	-	-	-	-	-	-	-	-	-	-	-	-	239

The diagram illustrates a memory transfer operation. The source address range is from 33 to 47 (SRC ADDR START: 33, SRC YINCR: 28, SRC YCOUNT: 5). The destination address range is from 105 to 155 (DEST ADDR START: 105, DEST YINCR: -32, DEST YCOUNT: 3). The data is transferred in three segments: 33-35 to 105-107, 36-38 to 142-144, and 39-41 to 145-147. The remaining source addresses (42-47) are not transferred.

SRC ADDR START : Address 33  
SRC XINCR : 1  
SRC XCOUNT : 3  
SRC YINCR : 28  
SRC YCOUNT : 5

DEST ADDR START : Address 105  
DEST XINCR : -1  
DEST XCOUNT : 3  
DEST YINCR : 32

DEST ADDR START : Address 142  
DEST XINCR : 1  
DEST XCOUNT : 3  
DEST YINCR : -32