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| RSVD | RSVD | RSVD | RSVD | RSVD | RSVD | RSVD | RSVD | RSVD | RSVD | RSVD | RSVD | RSVD | RSVD | RSVD | RSVD | | | | | | | | | | | | | |
| 31 | 30 | 29 | 28 | 27 | 26 | 25 | 24 | 23 | 22 | 21 | 20 | 19 | 18 | 17 | 16 | | | | | | | | | | | | | |
| 15 | 14 | 13 | 12 | 11 | 10 | 9 | 8 | 7 | 6 | 5 | 4 | 3 | 2 | 1 | 0 | | | | | | | | | | | | | |
| Vendor Specific Error Status Enable | | | | RSVD | Tuning Error Status Enable | | ADMA Error Status Enable | | Auto CMD Error Status Enable | | Current Limit Error Status Enable | | Data End Bit Error Status Enable | | Data CRC Error Status Enable | | Command Timeout Error Status Enable | | Command End Bit Error Status Enable | | Command Index Error Status Enable | | Command End Bit Error Status Enable | | Command CRC Error Status Enable | | Command Timeout Error Status Enable | |