Memory Address 0 0 SRC ADDR START: Address 33 DEST ADDR START: Address 81 SRC XINCR: 1 DEST XINCR: 1 SRC XCOUNT: 3 **DEST XCOUNT: 3**

Addr-

30

60

90

150

180

210

SRC YINCR: 28

SRC YCOUNT: 5

- -

DEST YINCR: 28

→ Addr

59

89

119

149

179

209

239