

Standard Register Set

0F-00h

SD Command Generation

1F-10h

Response

23-20h

Buffer Data Port

Buffer

2F-24h

Host Control 1 and Others

3D-30h

Interrupt Controls

3F-3Eh

Host Control 2

4F-40h

Capabilities

53-50h

Force Event

5F-54h

ADMA

6F-60h

Preset Value

Reserved

E3-E0h

Shared Bus

Reserved

FF-F0h

Common Area