Bit pattern diagrams

Jean-Marc Bourguet jm@bourguet.org

December 6, 2015

Abstract

The bitpattern package is designed to typeset bit patterns as they may appear in description of data format, hardware or protocols. It covers thus more or less the same application domain as the package register and is somewhat related to bytefield.

Comparared to register the formating is more compact, the syntax less verbose and bitpattern allows big endian and little endian bit numbering. But bitpattern is less well adapted to the use of long names for the fields and has no provision for a reset value.

1 Examples

Op

 \overrightarrow{AC}

Ι

We'll first use the instruction formats of the PDP-10 to describe the features of bit patterns. The PDP-10 was a word-adressable 36-bit computer and the reason for which I've chosen it is that it was the computer on which T_EX was first implemented.

The PDP-10 has two instruction formats, one used for most of the instructions: $_{9}^{}$ $_{13}^{}$ $_{14}^{}$ $_{18}^{}$

| Op | AC | I | X | . | ' | | | ' | ' | | | Ý | - ' | | | ' | | ' | | | |
|----------------------|--------------|--------|--------|------|-----|----|------|------|----|----|----|----|-----|----|----|----|----|----|----|----|----|
| and the other used | for IO inst | truct | tions: | | | | | | | | | | | | | | | | | | |
| 111 Dev | IOP | I | X | | ' | | | | | | | Ý | - ' | | | | | | | | |
| 2 | , | 13 | | 17 | | | | | | | | | | | | | | | | | 35 |
| bitpattern allo | ows to nun | ıber | all t | he l | bit | s: | | | | | | | | | | | | | | | |
| 0 1 2 3 4 5 6 7 | 8 9 10 11 12 | 13 14 | 15 16 | 17 | 18 | 19 | 20 2 | . 22 | 23 | 24 | 25 | 26 | 27 | 28 | 29 | 30 | 31 | 32 | 33 | 34 | 35 |
| Op | AC | I | X | | , | | | ' | ' | | | Y | - ' | | | | ' | ' | | | |
| | | | C 11 | | | _ | | | 1 | | | | _ | | | | | _ | | | ш |
| or only at the start | and end c | ot a t | held: | | | | | | | | | | | | | | | | | | |
| 111 Dev | IOP | I | X | | · · | | | | | | | Ý | - ' | | | | | | | | |
| 0 2 3 | 9 10 12 | 13 1 | 4 | 17 | 18 | | | | _ | | | | | | | _ | | _ | | _ | 35 |
| Having a long fie | eld like Y | here | may | tal | ke | m | ore | pla | ce | th | ar | w | an | te | d. | b: | it | pa | tt | er | n |
| allows to reduce the | at: | | | | | | | | | | | | | | | | | | | | |

The PDP-10 numbered its bits in the big endian way. That's not the case of the 8080. The 8080 was an 8-bit computer. Trying to pack instructions in 8-bit forces to use a lot of formats. One of them was used for the move instruction,

Y

and let's take that opportunity to start and compact the layout of the bit field by changing the format of the bit numbers and the field descriptions:

After that it is possible to still be more compact by removing the numbers and reducing the height of the ticks. When that's done, you can put a bit pattern in a paragraph without disturbing it too much, like this of production. With some text afterwards to see how the next line is handled. The result seems quite readable.

2 Interface

2.1 \bitpattern

\bitpattern

The \bitpattern macro is the macro which formats the patterns.

The optional argument allowing to control how the formatting is made:

littleEndian indicates that bit numbering is little endian, that is the leftward bit has the biggest number;

bigEndian indicates that bit numbering is big endian, that is the leftward bit has the lowest number;

numberBitsAbove indicates that the bit numbers should be put above the fields; numberBitsBelow indicates that the bit numbers should be put below the fields; noBitNumbers indicates that there should be no bit numbers;

numberOncePerField indicates that the fields should have only one bit number; numberTwicePerField indicates that the fields should have two bit numbers; numberAllBits indicates that the fields shouldn't have a bit number indication; startBit=X indicates that the number for the leftmost bit should be X;

 ${\tt tickHeight=dimen}$ indicates that the size taken by the small ticks marking the bits should be ${\it dimen}.$

bitWidth=dimen indicates that the size taken by a bit should be dimen;

After the optional arguments comes the description of the fields. They are composed of the field names (which does not have to be included in braces if it takes one character) followed by two optional arguments giving the size of the field (1 if omitted) and the width it should take (the same as its size).

Field descriptions are ended by a /.

2.2 Package options

The package bitpattern accept some options:

littleEndian indicates that the default for bit numbering is little endian, that is the leftward bit has the biggest number;

bigEndian indicates that the default for bit numbering is big endian, that is the leftward bit has the lowest number;

numberBitsAbove indicates that by default the bit numbers should be put above the fields;

numberBitsBelow indicates that by default the bit numbers should be put below the fields;

noBitNumbers indicates that by default there should be no bit numbers;

numberOncePerField indicates that by default the fields should have only one bit number:

numberTwicePerField indicates that by default the fields should have two bit numbers;

number AllBits indicates that by default the fields shouldn't have a bit number indication.

2.3 Commands controlling the format

\bpLittleEndian \bpBigEndian \bpNumberBitsAbove **\bpLittleEndian** changes the default bit numbering to little endian.

\bpBigEndian changes the default bit numbering to little endian.

 $\verb|\bpNumberBitsAbove| changes the default to having the numbering above the fields.$

\bpNumberBitsBelow

 $\verb|\bpNumberBitsBelow| changes the default to having the numbering below the fields.$

\bpNoBitNumbers \bpNumberFieldOnce

\bpNoBitNumbers changes the default to having no numbering.

 $\verb|\bpNumberFieldOnce| changes the default to having the numbering done once per field.$

\bpNumberFieldTwice

\bpNumberFieldTwice changes the default to having the numbering done twice per field.

\bpNumberAllBits

\bpNumberAllBits changes the default to having the numbering done for all bits of a field.

 $\begin{tabular}{l} \begin{tabular}{l} \begin{tabu$

\bpStartAtBit

\bpStartAtBit gives the default bit number of the leftmost bit.

\bpSetBitWidth $\{\langle length \rangle\}$

\bpSetBitWidth

\bpSetBitWidth gives the default bit width

 $\begin{tabular}{ll} \begin{tabular}{ll} \beg$

\bpSetTickHeight

\bpSetTickHeight gives the default height for the ticks marking the bits in a multi-bit field.

\bpFormatField

\bpFormatField is a one argument macro used fo format the field. It can be replaced. Care should be taken to format all the fields with the same height, so putting a \strut in the replacement is probably in order.

\bpFormatBitNumber

\bpFormatBitNumber is a one argument macro used fo format the bit numbers. It can be replaced. Care should be taken to format all the bit numbers with the same height, so putting a \strut in the replacement is perhaps in order.

3 Examples revisited

bitpattern was loaded by:

\RequirePackage[numberOncePerField,bigEndian,numberBitsAbove]{bitpattern}



\bitpattern{Op}[9]{AC}[4]IX[4]Y[18]/



\bitpattern[numberBitsBelow] {111} [3] {Dev} [7] {IOP} [3] IX[4] Y[18] /



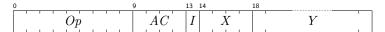
\bitpattern[numberAllBits]{Op}[9]{AC}[4]IX[4]Y[18]/



\bitpattern[numberBitsBelow,numberTwicePerField]% {111}[3]{Dev}[7]{I0P}[3]IX[4]Y[18]/



\bitpattern[numberBitsBelow,numberTwicePerField]% 111{Dev}[7]{IOP}[3]IX[4]Y[18]/



\bitpattern{Op}[9]{AC}[4]IX[4]Y[18][9]/

| | 01 | D | | S | |
|---|----|---|-----|---|---|
| _ | | | - 2 | | _ |

| 01 | D | S |
|----|---|---|

It is to be noted that usually one does not change the formatting for every pattern, so setting the optional argument of **\bitpattern** is rarely used.