ECE 2504: Introduction to Computer Engineering

Homework Assignment 6 (50 points)

Problem 1 (10 points)

If we know enough about the characteristic information for a flip-flop, we can derive the excitation information for that flip-flop.

Here is the characteristic information for the SR flip-flop:

| S | R | Function |
|---|---|---------------------------|
| 0 | 0 | No change Q+ = Q |
| 0 | 1 | Reset Q+ = 0 |
| 1 | 0 | Set Q ⁺ = 1 |
| 1 | 1 | Invalid Q+ = × |

To derive the excitation for the SR flip-flop, we can make the following observations:

- If Q = 0 and $Q^+ = 0$, we can make SR = 00 (no change, since $Q^+ = Q$) or SR = 01 (reset, since $Q^+ = 0$).
- If Q = 0 and $Q^+ = 1$, we can make SR = 10 (set, since $Q^+ = 1$).
- If Q = 1 and $Q^+ = 0$, we can make SR = 01 (reset, since $Q^+ = 0$).
- If Q = 1 and $Q^+ = 1$, we can make SR = 00 (no change, since $Q^+ = Q$) or SR = 10 (set, since $Q^+ = 1$).

Here is the excitation table that results:

| _ | Q | Q⁺ | S | R | Functions? |
|---|---|----|---|---|------------------|
| _ | 0 | 0 | 0 | × | No change, Reset |
| | 0 | 1 | 1 | 0 | Set |
| | 1 | 0 | 0 | 1 | Reset |
| _ | 1 | 1 | × | 0 | No change, Set |

The " \times " represents a don't care; in the case of the first instance of excitation, to change a present state of 0 into a next state of 0, S must be 0 and R does not matter.

Problem 1 (continued)

a. First, complete the characteristic table for the JK flip-flop.

Use the characteristic information to complete the excitation table for the JK flip-flop. Remember to consider all possible cases of characteristic information that cause a particular transition. Use the "Functions?" table to indicate which characteristic function or functions of the JK flip-flop are capable of causing the specified instance of excitation.

| J | K | Function |
|---|---|-------------------|
| 0 | 0 | No change Q+=Q |
| 0 | 1 | Reset Q+=0 |
| 1 | 0 | Set Q+=1 |
| 1 | 1 | Toggle Q+=Q' |

| Q | Q⁺ | J | K | Functions? |
|---|----|---|---|--------------------|
| 0 | 0 | 0 | Х | No Change or Reset |
| 0 | 1 | 1 | Х | Set or Toggle |
| 1 | 0 | Х | 1 | Reset or Toggle |
| 1 | 1 | Х | 0 | No Change or Set |

b. First, complete the characteristic table for the T flip-flop.

Use the characteristic information to complete the excitation table for the T flip-flop. Remember to consider all possible cases of characteristic information that cause a particular transition. Use the "Functions?" table to indicate which characteristic function or functions of the T flip-flop are capable of causing the specified instance of excitation.

| T | Function |
|---|--------------------|
| 0 | No change Q+= Q |
| 1 | Q+=Q' |

| Q | Q⁺ | Т | Functions? |
|---|----|---|------------|
| 0 | 0 | 0 | No change |
| 0 | 1 | 1 | Toggle |
| 1 | 0 | 1 | Toggle |
| 1 | 1 | 0 | No Change |

Download and review the "Counter Design" documents in Section 5 before you do Problems 2 and 3:

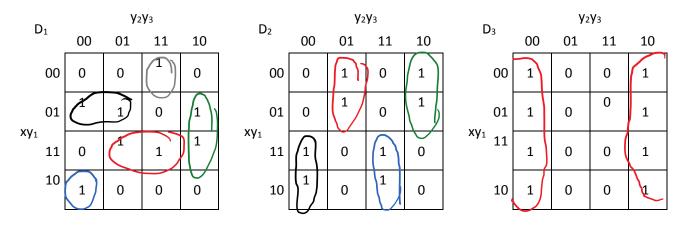
Problem 2 (20 points)

Design a three-bit counter using D flip-flops that has the following characteristics: When the value of an input x is 0, the counter counts "up" in standard order. When the value of x is 1, the counter counts down in standard order.

a. First, complete the state table shown below.

| Present State | | | ! | Next State | | | Excitation | | | |
|---------------|-----------------------|-----------------------|------------|-------------------------|-------------------------|-------------------------|------------|----------------|----------------|----|
| Х | y ₁ | y ₂ | y 3 | y 1 ⁺ | y 2 ⁺ | y 3 ⁺ | D_1 | D ₂ | D ₃ | |
| 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 1 | 0 |
| 0 | 0 | 0 | 1 | 0 | 1 | 0 | 0 | 1 | 0 | 1 |
| 0 | 0 | 1 | 0 | 0 | 1 | 1 | 0 | 1 | 1 | 2 |
| 0 | 0 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 3 |
| 0 | 1 | 0 | 0 | 1 | 0 | 1 | 1 | 0 | 1 | 4 |
| 0 | 1 | 0 | 1 | 1 | 1 | 0 | 1 | 1 | 0 | 5 |
| 0 | 1 | 1 | 0 | 1 | 1 | 1 | 1 | 1 | 1 | 6 |
| 0 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 7 |
| 1 | 0 | 0 | 0 | 1 | 1 | 1 | 1 | 1 | 1 | 8 |
| 1 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 9 |
| 1 | 0 | 1 | 0 | 0 | 0 | 1 | 0 | 0 | 1 | 10 |
| 1 | 0 | 1 | 1 | 0 | 1 | 0 | 0 | 1 | 0 | 11 |
| 1 | 1 | 0 | 0 | 0 | 1 | 1 | 0 | 1 | 1 | 12 |
| 1 | 1 | 0 | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 13 |
| 1 | 1 | 1 | 0 | 1 | 0 | 1 | 1 | 0 | 1 | 14 |
| 1 | 1 | 1 | 1 | 1 | 1 | 0 | 1 | 1 | 0 | 15 |

b. Next, derive the logic equations using the Karnaugh maps shown below.

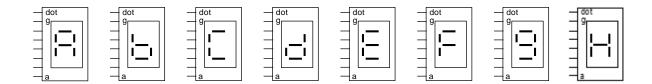


D1 = X' Y1 Y2' + X Y1 Y3 + X Y1 Y2' Y3' + X' Y1' Y2 Y3 + Y1 Y2 Y3' D2 = X Y2' Y3' + X' Y2' Y3 + X Y2 Y3 +X' Y2 Y3'

D3 = Y3'

Problem 3 (20 points)

The purpose of the counter is to drive a seven-segment LED display like the one you used in Project 1. Specifically, you want the counter to cause the LED display to cycle through the characters:

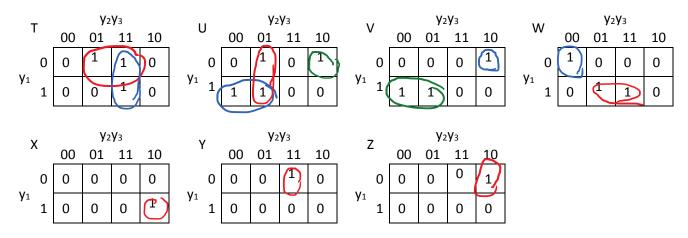


Of course, when the counter is counting up, the characters will appear in regular order, and when the counter is counting down, the characters will appear in reverse order. Since each character is associated with a specific present state, we don't actually have to include x as an input in the equations.

a. First, complete the truth table shown below. Remember that a lit segment requires a value of 0, while an unlit segment requires a value of 1.

| y 1 | y ₂ | y 3 | Character | T | U | V | W | Χ | Υ | Z |
|------------|-----------------------|------------|-----------|---|---|---|---|---|---|---|
| 0 | 0 | 0 | Α | 0 | 0 | 0 | 1 | 0 | 0 | 0 |
| 0 | 0 | 1 | В | 1 | 1 | 0 | 0 | 0 | 0 | 0 |
| 0 | 1 | 0 | С | 0 | 1 | 1 | 0 | 0 | 0 | 1 |
| 0 | 1 | 1 | d | 1 | 0 | 0 | 0 | 0 | 1 | 0 |
| 1 | 0 | 0 | E | 0 | 1 | 1 | 0 | 0 | 0 | 0 |
| 1 | 0 | 1 | F | 0 | 1 | 1 | 1 | 0 | 0 | 0 |
| 1 | 1 | 0 | g | 0 | 0 | 0 | 0 | 1 | 0 | 0 |
| 1 | 1 | 1 | Н | 1 | 0 | 0 | 1 | 0 | 0 | 0 |

b. Next, derive the logic equations using the Karnaugh maps shown below.



T: Y1' Y3 + Y2 Y3

U: Y1 Y2' + Y2' Y3 + Y1' Y2 Y3'

V: Y1 Y2' + Y1' Y2 Y3'

W: Y1' Y2' Y3' + Y1 Y3

X: Y1 Y2 Y3' Y: Y1' Y2 Y3 Z: Y1' Y2 Y3'