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Design an Adder

Follow the steps below to design an unsigned adder.

1. Complete the following addition problems directly in binary. In other words, work through the addition using the algorithm you learned in 1st grade. Don't convert them to base 10.

$$\begin{array}{r} 0010 \\ + 0101 \\ \hline \end{array}$$

$$\begin{array}{r} 0101 \\ + 0011 \\ \hline \end{array}$$

$$\begin{array}{r} 0011 \\ + 0011 \\ \hline \end{array}$$

2. Complete the truth table below for a one-bit adder. (Notice how this circuit can also handle the right-most column of the above addition problems.)

A	B	Carry	Out
0	0		
0	1		
1	0		
1	1		

3. Draw the circuit for this truth table.

The circuit you just designed and drew is called a "Half Adder"

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4. Make a truth table for a circuit that can handle the 2nd column of the above addition problems.
Hint: It should have three inputs and two outputs: (Don't forget to label the inputs and outputs.)

0	0	0		
0	0	1		
0	1	0		
0	1	1		
1	0	0		
1	0	1		
1	1	0		
1	1	1		

5. Draw the circuit for the above truth table. This circuit is called a “Full Adder”

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6. Show how you can combine 4 full adders into a single device that can add two 4-bit numbers. (Just use a box with the inputs and outputs labeled to represent each full adder. You need not draw the gates inside each full adder.)
7. What is the big-O running time for your adder? Why? (If you are not familiar with big-O, then do this: Draw a graph with the number of input bits on the x -axis and time on the y -axis. What is the shape of your graph? Why?)
8. What is the big-O size of your adder? Why? (If you are not familiar with big-O, then do this: Draw a graph with the number input bits on the x -axis and the total number of gates on the y -axis. What is the shape of your graph? Why?)