



**Assume we are working with the direct-mapped cache setup above.**

1. Where is the address 0x0001309C mapped?
2. Give two addresses that will conflict with the address from (1).
3. Assume the cache is initially empty, but it updates after each read. Are each of the following memory lookups hits or misses?

Memory address	Hit/miss?
0x00001308	
0x00001308	
0x00001318	
0x00001308	
0x00001278	
0x00001318	
0x00001278	
0x00001308	
0x00001368	
0x00001308	