## Legend

F = page fault
B = process blocked

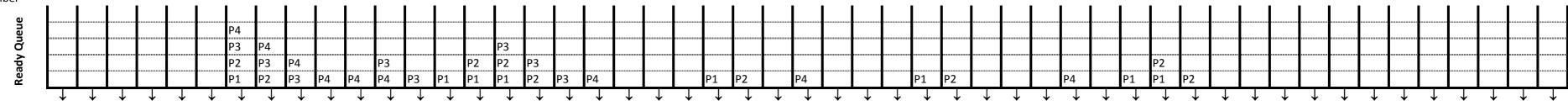
R = process ready

Pa(b) = processed page b of process a

f= frame number

Indicator

Р3 P4



tim	е	0 1	2	3	4	5	6 7	8	9	10	11	12 13	14	15 1	6 17	7 18	3 19	20	21	22	23	24	25 2	6 27	28	29	30	31	32	33 3	34 3	5 36	37	38	39	40	41	42	43	44	45	46	47	48	49	50
	P1F				P	P1(1) P1F	=						P1(2) P:	LF					P1(3)	P1F					P1(4)	P1F						P1(5	5)													
CDLL		P2F					P2(5	5) P2F	=					P2	(4) P2	F				F	2(3)	P2F					P2(2)	P2F						P2(1)	)											
CPU ev	/ents	P3F						P3(1	L) P3(1	L) P3(1)		P3(1) P3(1	) P3(1)		P3(:	1)																														
		P4F									P4(1)	P4F				P4(	2) P4F						P4(3) P4	(2) P4(1	) P4F					P4	1(4) P4	(4) P(4	1)													
		P1B					P1R P1E	3				P1R		P:	.B					P1R	P1B					P1R	P1B					P1	R													
Duo anno atat		P2B					P2R	P2B	3				P2R		P2I	В					P2R	P2B					P2R	P2B					P2R	R												
Process stat	lus events	P3B					P3R																																							
		P4B					P4R					P4B				P4	R P4B						P4R		P4B					Р	4R															
	P1																																							•	•					
Blocked	P2																																													

F											-																													
	f1				1	1	1	1	1	1	1	1	1	1	1	1	1	1	1 :	1 1	1	1	1	1	1	1	1	1	1 1	1	1	1	1	1						
- P	f2											2	2	2	2	2	2	2	2 2	2 2	2 2	2	2	2	2	2	2	2	2 2	2	2	2	2	2						
to to	f3																			3	3	3	3	3	3	3	3	3	3	3	3	3	3	3						
tec	f4																										4	4	4 4	4	4	4	4	4						
eoo	f5																																5	5						
₽	f6																																							
	f7																																							
	f8				5	5	5	5	5	5	5	5	5	5	5	5	5	5	5 5	5 5	5	5	5	5	5	5	5	5	5 5	5	5	5	5	5	5					
P2	f9												4	4	4	4	4	4	4 4	1 4	1 4	4	4	4	4	4	4	4	4 4	4	4	4	4	4	4					
<b>9</b>	f10																				3	3	3	3	3	3	3	3	3 3	3	3	3	3	3	3					
ed	f11																											2	2 2	2	2	2	2	2	2					
cat	f12																																	1	1					
Allc	f13																																							
	f14																																							
ľ	f15				1	1	1	1	1	1	1	1	1	1	1	1																								
Р3	f16				_			_	_	_																														
2	f17																																							
eq	f18																																							
cat	f19																	+		1								+						+						
Allo	f20																																							
1	f21																																							
ŀ	f22				1	1	1	1	1	1	1	1	1	1	1	1	1	1	1 '	1 1	1	1	1	1	1	1	1	1	1 1	1	1	1	1							
4	f23											$\dashv$	$\dashv$	$\dashv$		$\dashv$	2	2	2 2	2 2	2 2	2	2	2	2	2	2	2	2 2	2	2	2	2							
9	f24																-	-	-	-	1 -		2	3	3	3	3	3	2 2	3	3	3	3							
be 1	f25																											1	<del>-</del>	3	1	1	1							
cate	f26																														4	4								
Ŏ	f27																																							
⋖																																								
N - :	f28																																							
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<u>Summary</u>

PID	Turnaround time	Faults	Fault times
P1	38	5	0, 7, 16, 23, 30
P2	39	5	0, 8, 17, 24, 31
P3	18	1	0
P4	37	4	0, 12, 19, 28

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