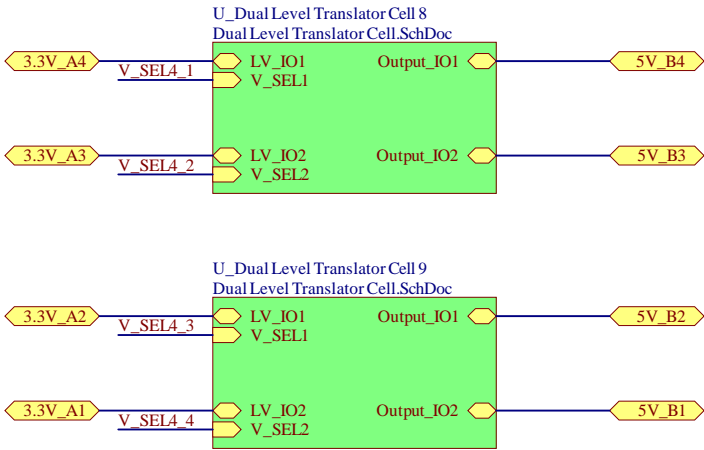
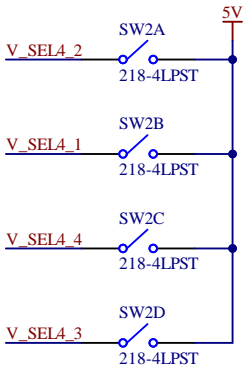
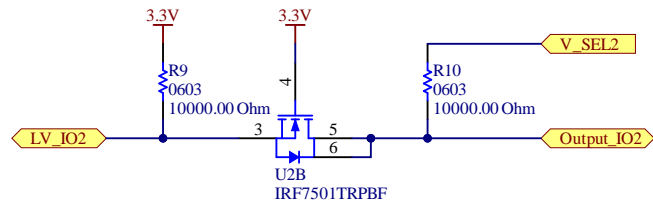
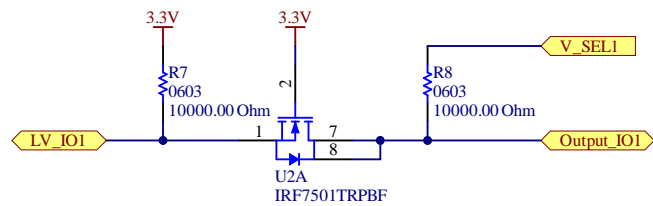


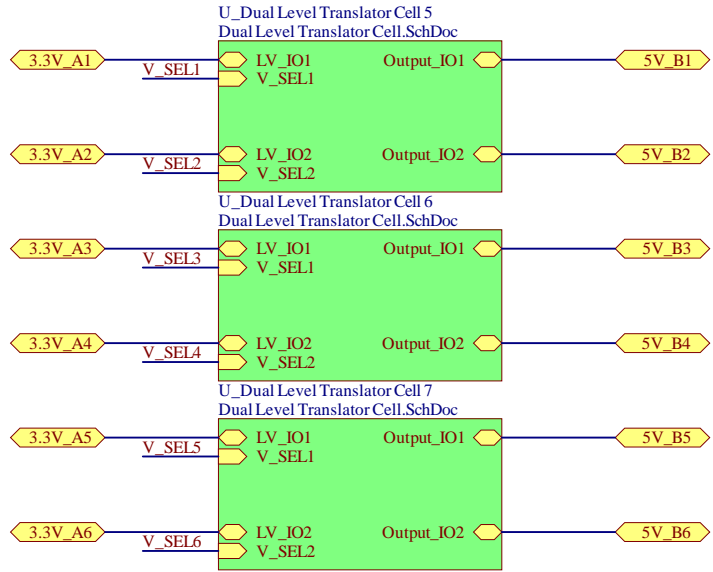
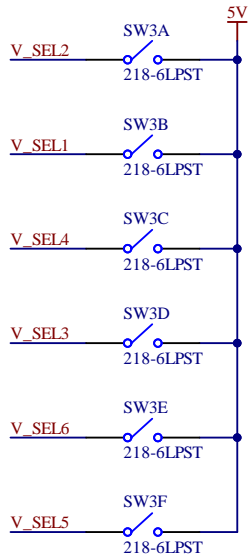
Title: CAN		
Project: PIC32 Board		
Print: 5/1/2023		
Drawn By: Zachary Kilburn		
Part Number: Z4		
Version: 0		
File: CAN.SchDoc	Size: A - Letter	Sheet 2 of 8



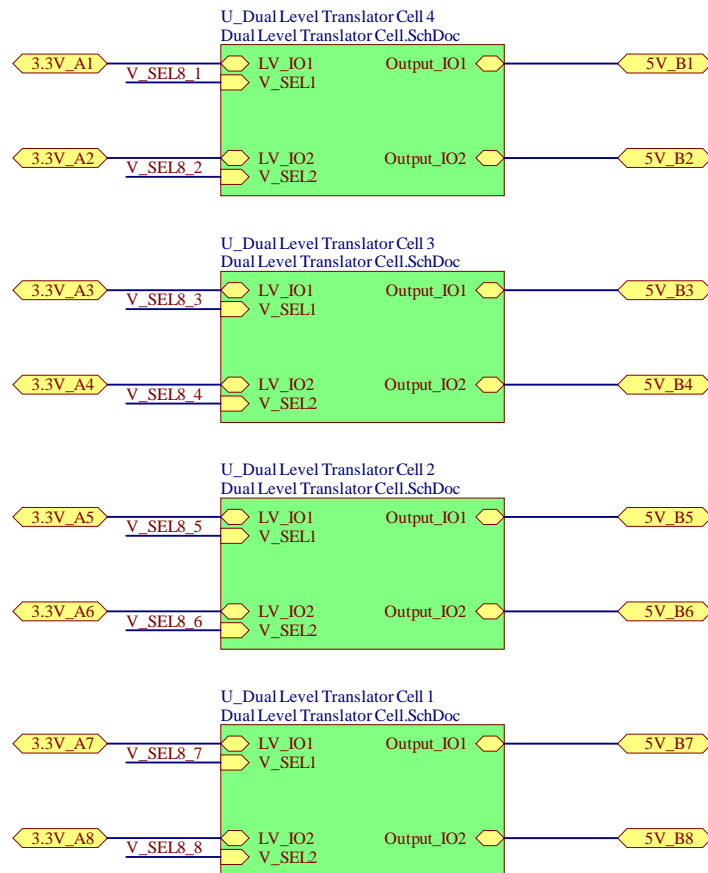
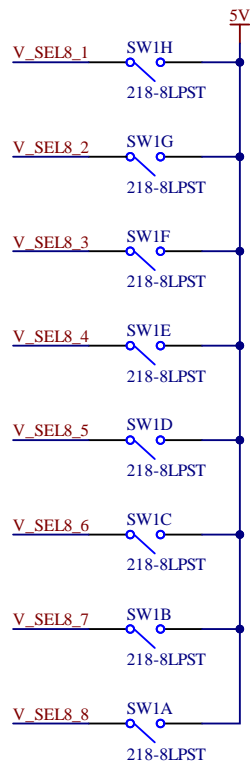
Title: <i>Level Translator 4</i>		
Project: PIC32 Board		
Print: 5/1/2023		
Drawn By: Zachary Kilburn		
Part Number: Z4		
Version: 0		
File: Level Translator 4.SchDoc	Size: A - Letter	Sheet 3 of 8



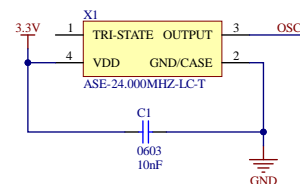
Title: Dual Level Translator Cell		
Project: PIC32 Board		
Print: 5/1/2023		
Drawn By: Zachary Kilburn		
Part Number: Z4		
Version: 0		
File: Dual Level Translator Cell.SchDoc	Size: A - Letter	Sheet 4 of 8



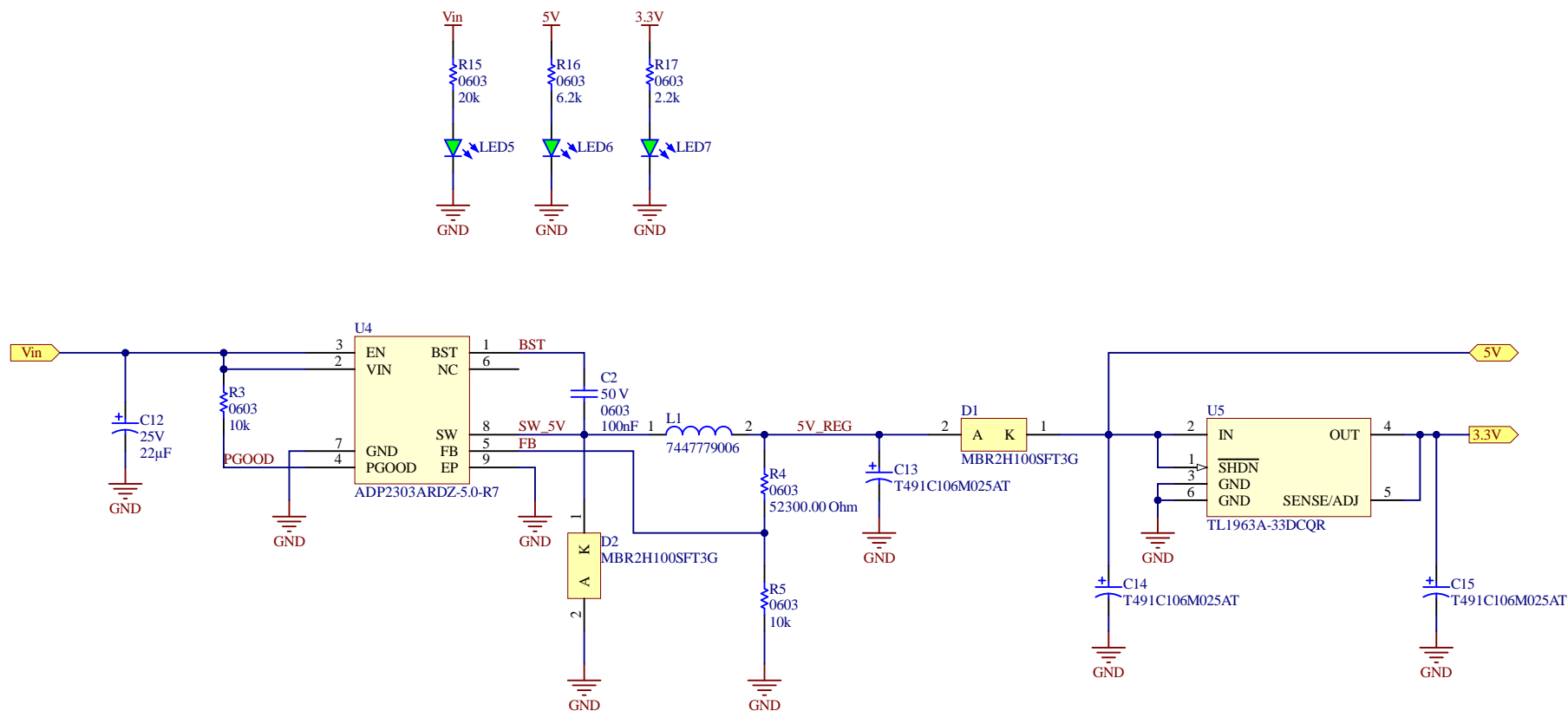
Title: Level Translator 6			
Project: PIC32 Board			
Print: 5/1/2023			
Drawn By: Zachary Kilburn			
Part Number: Z4			
Version: 0			
File: Level Translator 6.SchDoc		Size: A - Letter	Sheet 5 of 8



Title: Level Translator 8		
Project: PIC32 Board		
Print: 5/1/2023		
Drawn By: Zachary Kilburn		
Part Number: Z4		
Version: 0		
File: Level Translator 8.SchDoc	Size: A - Letter	Sheet 6 of 8



Title:	<i>Microcontroller</i>		
Project:	PIC32 Board		
Print:	5/1/2023		
Drawn By:	Zachary Kilburn		
Part Number:	Z4		
Version:	0		
File:	Microcontroller.SchDoc	Size:	A - Letter
		Sheet	7 of 8



Title: Power Management		
Project: PIC32 Board		
Print: 5/1/2023		
Drawn By: Zachary Kilburn		
Part Number: Z4		
Version: 0		
File: Power Management.SchDoc	Size: A - Letter	Sheet 8 of 8

