Mediump support in Mesa

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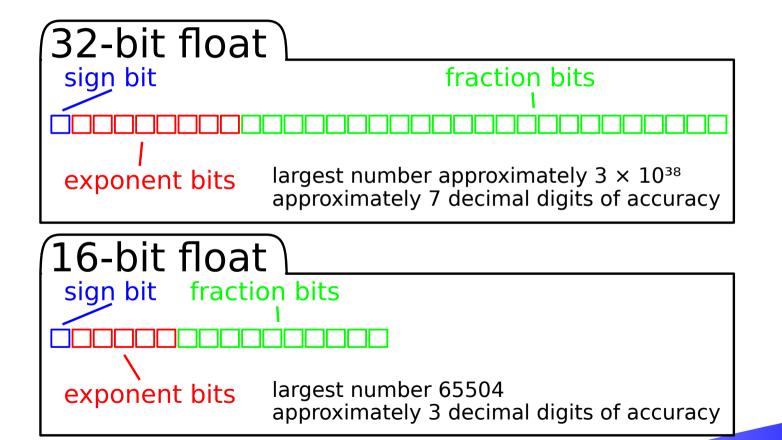
Overview

- Introduction
- History
- Examples
- Current status
- Future
- Questions

What is mediump?

- Only in GLSL ES
- Available since the first version of OpenGL ES.
- Used to tell the driver an operation in a shader can be done with lower precision.
- Some hardware can take advantage of this to trade off precision for speed.

 For example, an operation can be done with a 16bit float:



- GLSL ES has three available precisions:
 - lowp, mediump and highp
 - The spec specifies a minimum precision for each of these.
 - highp needs 16-bit fractional part.
 - It will probably end up being a singleprecision float.
 - mediump needs 10-bit fractional part.
 - This can be represented as a half float.
 - lowp has enough precision to store 8-bit colour channels.

- The precision does not affect the visible storage of a variable.
 - For example a mediump float will still be stored as 32-bit in a UBO.
 - Only operations are affected.
- The precision requirements are only a minimum.
 - Therefore a valid implementation could be to just ignore the precision and do every operation at highp.
 - This is effectively what Mesa currently does.

 The precision for a variable can be specified directly:

```
uniform mediump vec3 rect_color;
```

 Or it can be specified as a global default for each type:

```
precision mediump float;
uniform vec3 rect_color;
```

- The compiler specifies global defaults for most types except floats in the fragment shader.
- In GLSL ES 1.00 high precision support in fragment shaders is optional.

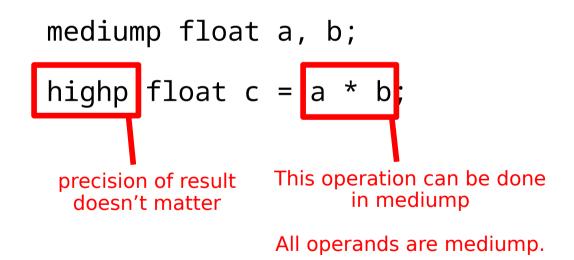
- The precision of operands to an operation determine the precision of the operation.
- Almost works like automatic float to double promotion in C.

```
mediump float a, b;
highp float c = a * b;
```

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All operands are mediump.

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Another example

```
mediump float a, b;
highp float c;
mediump float r = c * (a * b);
```

Another example

```
mediump float a, b;
highp float c;

mediump float r = c * (a * b);

This operation can still be done in mediump
```

Another example

- Corner case
 - Some things don't have a precision, eg constants.

```
mediump float diameter;
float circ = diameter * 3.141592;
```

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Constants have no precision
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 - Some things don't have a precision, eg constants.

```
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float circ = diameter * 3.141592;
```

Precision of multiplication is mediump anyway because one of the arguments has a precision

Constants have no precision

- Extreme corner case
 - Sometimes none of the operands have a precision.

```
uniform bool should_pi;
mediump float result =
    float(should_pi) * 3.141592;
```

- Extreme corner case
 - Sometimes none of the operands have a precision.

```
uniform bool should_pi;
mediump float result =
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Neither operand has a precision
```

- Extreme corner case
 - Sometimes none of the operands have a precision.

```
uniform bool should_pi;

mediump float result = float(should_pi) * 3.141592;

Precision of operation can come from outer expression, even the Ivalue of an assignment

Neither operand has a precision
```

What does Mesa currently do?

- Mesa already has code to parse the precision qualiers and store them in the IR tree.
- These currently aren't used for anything except to check for compile-time errors.
 - For example redeclaring a variable with a different precision.
- In desktop GL, the precision is always set to NONE.

- The precision usually doesn't form part of the glsl_type.
- Instead it is stored out-of-band as part of the ir_variable.

```
enum {
   GLSL_PRECISION_NONE = 0,
   GLSL_PRECISION_HIGH,
   GLSL_PRECISION_MEDIUM,
   GLSL_PRECISION_LOW
};
```

```
class ir variable : public ir instruction {
   /* ... */
public:
   struct ir variable data {
      /* ... */
      /**
       * Precision qualifier.
       * In desktop GLSL we do not care about precision qualifiers at
       * all, in fact, the spec says that precision qualifiers are
       * ignored.
       * To make things easy, we make it so that this field is always
       * GLSL PRECISION NONE on desktop shaders. This way all the
       * variables have the same precision value and the checks we add
       * in the compiler for this field will never break a desktop
       * shader compile.
       */
      unsigned precision:2;
      /* ... */
   };
};
```

 However this gets complicated for structs because members can have their own precision.

 In that case the precision does end up being part of the glsl_type.

The plan

- The idea is to lower mediump operations to float16 types in NIR.
- We want to lower the actual operations instead of the variables.
- This needs to be done at a high level in order to implement the spec rules.

- Work being done by Hyunjun Ko and myself and Igalia.
- Working on behalf of Google.
- Based on / inspired by patches by Topi Pohjolainen.

- Aiming specifically to make this work on the Freedreno driver.
- Most of the work is reusable for any driver though.
- Currently this is done as a pass over the IR representation.

```
uniform mediump float a, b;
void main()
{
    gl_FragColor.r = a / b;
}
```

These two variables are mediump

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So this division can be done at medium precision

- We only want to lower the division operation without changing the type of the variables.
- The lowering pass will add a conversion to float16 around the variable dereferences and then add a conversion back to float32 after the division.
- This minimises the modifications to the IR.

IR tree before lowering pass

IR tree before lowering pass

IR tree before lowering pass

- Lowering pass finds sections of the tree involving only mediump/lowp operations.
- Adds f2f16 conversion after variable derefs
- Adds f2f32 conversion at root of lowered branch

Reducing conversion operations

- This will end up generating a lot of conversion operations.
- Worse:

```
precision mediump float;
uniform mediump float a;
void main()
{
    float scaled = a / 5.0;
    gl_FragColor.r = scaled + 0.5;
}
```

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Resulting NIR

```
vec1 32 ssa_1 = deref_var &a (uniform float)
vec1 32 ssa_2 = intrinsic load_deref (ssa_1)
vec1 16 ssa_3 = f2f16 ssa_2
vec1 16 ssa_6 = fdiv ssa_3, ssa_20
vec1 32 ssa_7 = f2f32 ssa_6
vec1 16 ssa_8 = f2f16 ssa_7
vec1 32 ssa_9 = f2f32 ssa_8
vec1 16 ssa_10 = f2f16 ssa_9
vec1 16 ssa_13 = fadd ssa_10, ssa_22
```

Resulting NIR

```
vec1 32 ssa 1 = deref var &a (uniform float)
vec1 32 ssa 2 = intrinsic load deref (ssa 1)
vec1 16 ssa_3 = f2f16 ssa_2
vec1 16 ssa 6 = fdiv ssa 3, ssa 20
vec1 32 ssa_7 = f2f32 ssa_6
vec1 16 ssa_8 = f2f16 ssa_7 Lots of redundant
vec1 32 ssa 9 = f2f32 ssa 8 coversions!
vec1 16 ssa_10 = f2f16 ssa_9
vec1 16 ssa_13 = fadd ssa_10, ssa_22
```

Questions?