

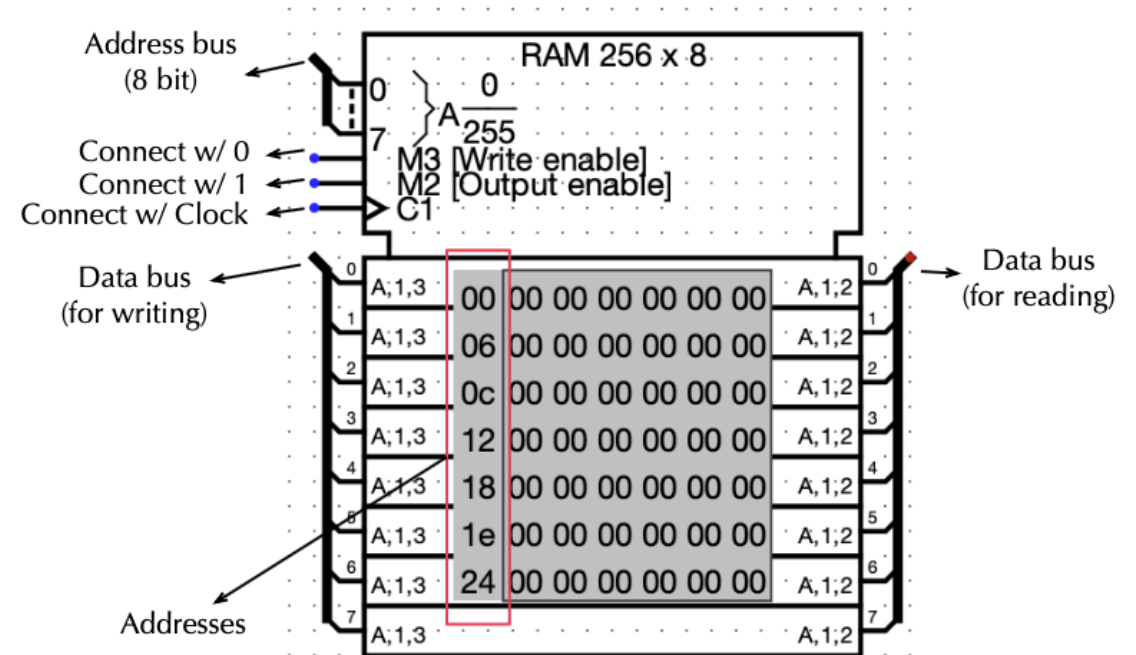
Lab 9: RAM



By: CS 382 CAs

RAM

- 0-7: Controls where the requested memory location is
- M2: Allow reading
- M3: Allow writing
- C1: Clock
- Data bus (left): Holds data that will be written
- Data bus (right): Holds the data from a given location in memory



Your Assignment

Your assignment

- Wire up a RAM module to work with reading only
- Create image file to save the contents of your RAM
- Create PC register to hold the memory location