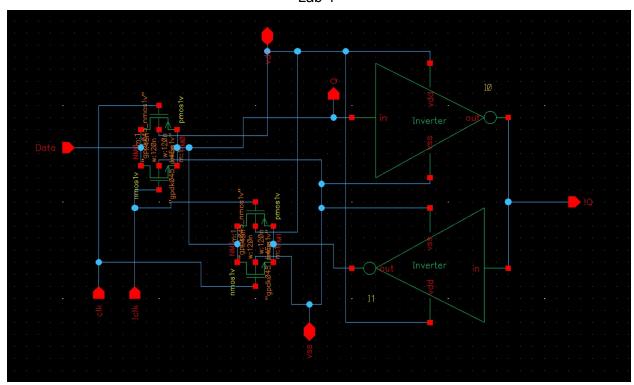
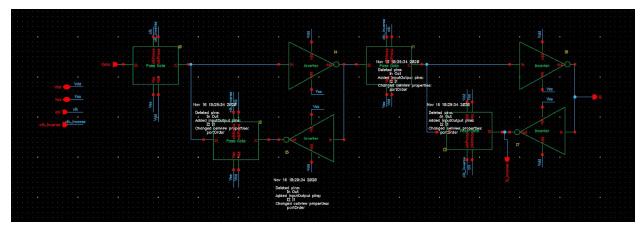
Lab 4



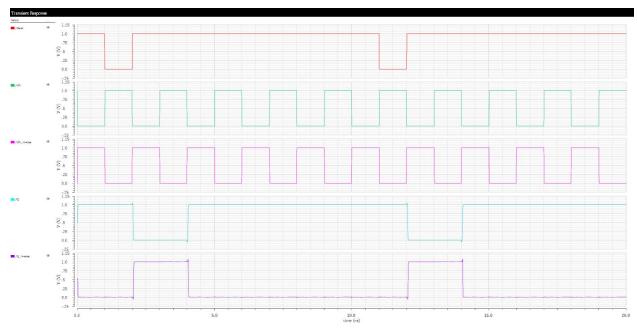
DLatch Schematic



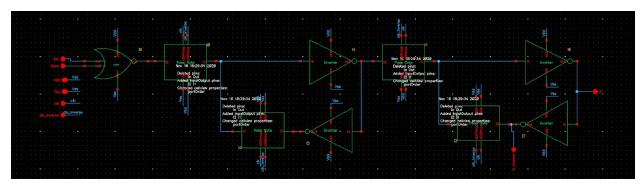
DLatch Transient Analysis



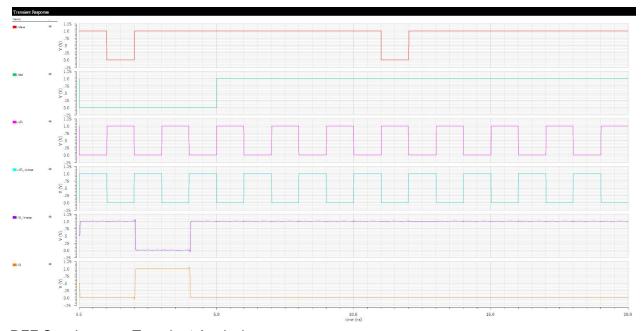
DFF Schematic



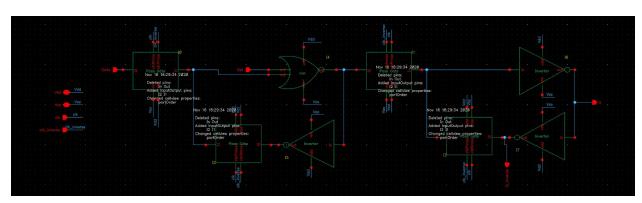
DFF Transient Analysis



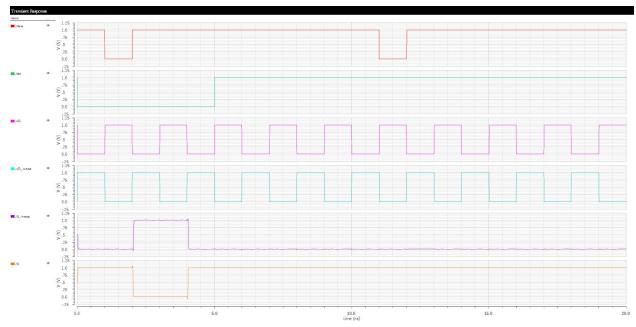
DFF Synchronous Schematic



DFF Synchronous Transient Analysis



DFF Asynchronous Schematic



DFF Asynchronous Transient Analysis