Schematic Schematic All necessary components are present All necessary components are present All VCC and GND pins are connected to flags All VCC and GND pins are connected to flags U All components are appropriately wired All components are appropriately wired No wires cross over other component pins No wires cross over other component pins Schematic has an appropriate title and company Schematic has an appropriate title and company (project name and student name) (project name and student name) **PCB** Design PCB Design Board outline is continuous and complete Board outline is continuous and complete Components are appropriately spaced and laid out Components are appropriately spaced and laid out Traces are clean, organized, with 45 degree bends Traces are clean, organized, with 45 degree bends Vias are used where appropriate Vias are used where appropriate All Nets have been completed (check Nets panel) All Nets have been completed (check Nets panel) PCB passes a Design Rules Check PCB passes a Design Rules Check VCC plane is on top and GND plane is on bottom VCC plane is on top and GND plane is on bottom No islands-parts isolated from VCC or GND planes) No islands-parts isolated from VCC or GND planes) **PopArt Specific PopArt Specific** Project Name (with version number) and Designer Project Name (with version number) and Designer Name appear on the back silkscreen. Name appear on the back silkscreen. USB connector is correctly placed near the edge USB connector is correctly placed near the edge Pushbutton is correctly placed near the edge Pushbutton is correctly placed near the edge 4 mounting holes (2.5mm) are placed on the board 4 mounting holes (2.5mm) are placed on the board NeoPixels are appropriately spaced NeoPixels are appropriately spaced **Quality Assurance Signature:** Quality Assurance Signature:

PCB Design Quality Assurance Ticket Designer: Black James

File Name: J.A.R.V.T.S

Designer Signature: Buther Feel,	
Schematic All necessary components are present All VCC and GND pins are connected to flags All components are appropriately wired No wires cross over other component pins Schematic has an appropriate title and company (project name and student name)	Schematic All necessary components are present All VCC and GND pins are connected to flags All components are appropriately wired No wires cross over other component pins Schematic has an appropriate title and company (project name and student name)
Board outline is continuous and complete Components are appropriately spaced and laid out Traces are clean, organized, with 45 degree bends Vias are used where appropriate All Nets have been completed (check Nets panel) PCB passes a Design Rules Check VCC plane is on top and GND plane is on bottom No islands-parts isolated from VCC or GND planes)	Board outline is continuous and complete Components are appropriately spaced and laid out Traces are clean, organized, with 45 degree bends Vias are used where appropriate All Nets have been completed (check Nets panel) PCB passes a Design Rules Check VCC plane is on top and GND plane is on bottom No islands-parts isolated from VCC or GND planes)
Project Name (with version number) and Designer Name appear on the back silkscreen. USB connector is correctly placed near the edge Pushbutton is correctly placed near the edge 4 mounting holes (2.5mm) are placed on the board NeoPixels are appropriately spaced	Project Name (with version number) and Designer Name appear on the back silkscreen. USB connector is correctly placed near the edge Pushbutton is correctly placed near the edge 4 mounting holes (2.5mm) are placed on the board NeoPixels are appropriately spaced
Quality Assurance Signature:	Quality Assurance Signature:
Final Approval:	