

**Schematic**

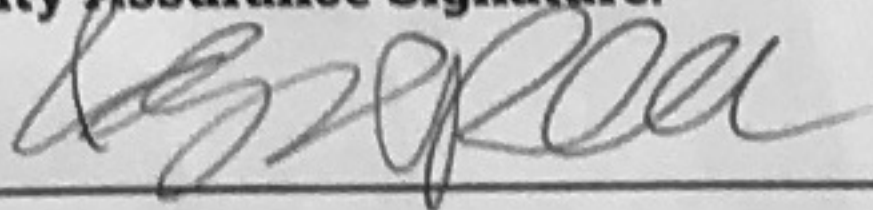
- ☒ All necessary components are present
- ☒ All VCC and GND pins are connected to flags
- ☒ All components are appropriately wired
- ☒ No wires cross over other component pins
- ☒ Schematic has an appropriate title and company (project name and student name)

**PCB Design**

- ☒ Board outline is continuous and complete
- ☒ Components are appropriately spaced and laid out
- ☒ Traces are clean, organized, with 45 degree bends
- ☒ Vias are used where appropriate
- ☒ All Nets have been completed (check Nets panel)
- ☒ PCB passes a Design Rules Check
- ☒ VCC plane is on top and GND plane is on bottom
- ☒ No islands-parts isolated from VCC or GND planes)

**PopArt Specific**

- ☒ Project Name (with version number) and Designer Name appear on the back silkscreen.
- ☒ USB connector is correctly placed near the edge
- ☒ Pushbutton is correctly placed near the edge
- ☒ 4 mounting holes (2.5mm) are placed on the board
- ☒ NeoPixels are appropriately spaced

**Quality Assurance Signature:****Schematic**

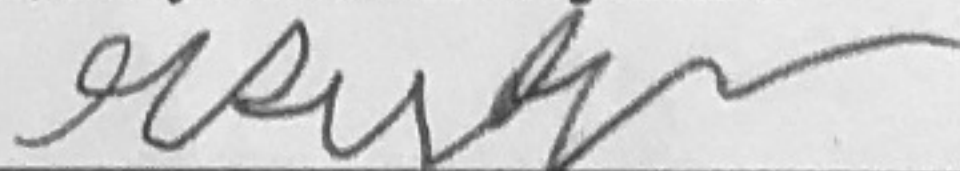
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# PCB Design Quality Assurance Ticket

Designer: *Braden Jarvis*

File Name: *J.A.R.V.I.S*

Designer Signature: *Braden Jarvis*

<b>Schematic</b> <ul style="list-style-type: none"><li><input checked="" type="checkbox"/> All necessary components are present</li><li><input checked="" type="checkbox"/> All VCC and GND pins are connected to flags</li><li><input checked="" type="checkbox"/> All components are appropriately wired</li><li><input checked="" type="checkbox"/> No wires cross over other component pins</li><li><input checked="" type="checkbox"/> Schematic has an appropriate title and company (project name and student name)</li></ul> <b>PCB Design</b> <ul style="list-style-type: none"><li><input checked="" type="checkbox"/> Board outline is continuous and complete</li><li><input checked="" type="checkbox"/> Components are appropriately spaced and laid out</li><li><input checked="" type="checkbox"/> Traces are clean, organized, with 45 degree bends</li><li><input checked="" type="checkbox"/> Vias are used where appropriate</li><li><input checked="" type="checkbox"/> All Nets have been completed (check Nets panel)</li><li><input checked="" type="checkbox"/> PCB passes a Design Rules Check</li><li><input checked="" type="checkbox"/> VCC plane is on top and GND plane is on bottom</li><li><input checked="" type="checkbox"/> No islands-parts isolated from VCC or GND planes)</li></ul> <b>PopArt Specific</b> <ul style="list-style-type: none"><li><input checked="" type="checkbox"/> Project Name (with version number) and Designer Name appear on the back silkscreen.</li><li><input checked="" type="checkbox"/> USB connector is correctly placed near the edge</li><li><input checked="" type="checkbox"/> Pushbutton is correctly placed near the edge</li><li><input checked="" type="checkbox"/> 4 mounting holes (2.5mm) are placed on the board</li><li><input checked="" type="checkbox"/> NeoPixels are appropriately spaced</li></ul>	<b>Schematic</b> <ul style="list-style-type: none"><li><input checked="" type="checkbox"/> All necessary components are present</li><li><input checked="" type="checkbox"/> All VCC and GND pins are connected to flags</li><li><input checked="" type="checkbox"/> All components are appropriately wired</li><li><input checked="" type="checkbox"/> No wires cross over other component pins</li><li><input checked="" type="checkbox"/> Schematic has an appropriate title and company (project name and student name)</li></ul> <b>PCB Design</b> <ul style="list-style-type: none"><li><input checked="" type="checkbox"/> Board outline is continuous and complete</li><li><input checked="" type="checkbox"/> Components are appropriately spaced and laid out</li><li><input checked="" type="checkbox"/> Traces are clean, organized, with 45 degree bends</li><li><input checked="" type="checkbox"/> Vias are used where appropriate</li><li><input checked="" type="checkbox"/> All Nets have been completed (check Nets panel)</li><li><input checked="" type="checkbox"/> PCB passes a Design Rules Check</li><li><input checked="" type="checkbox"/> VCC plane is on top and GND plane is on bottom</li><li><input checked="" type="checkbox"/> No islands-parts isolated from VCC or GND planes)</li></ul> <b>PopArt Specific</b> <ul style="list-style-type: none"><li><input checked="" type="checkbox"/> Project Name (with version number) and Designer Name appear on the back silkscreen.</li><li><input checked="" type="checkbox"/> USB connector is correctly placed near the edge</li><li><input checked="" type="checkbox"/> Pushbutton is correctly placed near the edge</li><li><input checked="" type="checkbox"/> 4 mounting holes (2.5mm) are placed on the board</li><li><input checked="" type="checkbox"/> NeoPixels are appropriately spaced</li></ul>
<b>Quality Assurance Signature:</b> <i>[Signature]</i>	<b>Quality Assurance Signature:</b> <i>[Signature]</i>

Final Approval:

*[Signature]*

