

Sheet1

Cycle count	65
Logic Elements	328
Registers	260
Memory	6144
Multipliers	0

Quartus II 64-Bit Version	15.0.0 Build 145 04/22/2015 SJ Web Edition
Revision Name	add_n_top
Top-level Entity Name	add_n_top
Family	Cyclone IV E
Device	EP4CE115F29C7
Timing Models	Final
Total logic elements	328 / 114,480 (< 1 %)
Total combinational functions	231 / 114,480 (< 1 %)
Dedicated logic registers	260 / 114,480 (< 1 %)
Total registers	260
Total pins	126 / 529 (24 %)
Total virtual pins	0
Total memory bits	6,144 / 3,981,312 (< 1 %)
Embedded Multiplier 9-bit elements	0 / 532 (0 %)
Total PLLs	0 / 4 (0 %)

```
# @ 4 ns: Loading file ../scripts/y.txt...
# @ 4 ns: Loading file ../scripts/x.txt...
# @ 136 ns: Beginning simulation...
# @ 266 ns: Simulation completed.
# @ 269 ns: Comparing file ../scripts/z.txt
# Total simulation cycle count: 65
# Total error count: 0
```