



## A tech conference by students, for students

With **ECE Pulse 2016**, our goal is to expose students to the wide breadth of new opportunities with an electrical or computer engineering degree. We aim to showcase the innovations of industry today. In this way, we will inspire the future generation of engineers to tackle the challenges of tomorrow. It goes without saying that University of Illinois, especially **ECE Illinois**, is a widely recognized hub for innovation and impact.

This will be our fifth year running ECE Pulse. Throughout this weeklong celebration, approximately 500 students with a passion for technology will participate in a variety of events. We begin on Jan. 23<sup>rd</sup> with student-made student-run competitions, which will allow competitors to tackle practical design challenges common in various subfields of electrical and computer engineering. The following weekend, we will host a series of technical workshops and presentations. Finally, we conclude ECE Pulse on Jan. 30<sup>th</sup> with a startup fair, encouraging our attendees to explore technology entrepreneurial pursuits.

Sponsors of ECE Pulse can enjoy interacting with one of the best and brightest engineering student bodies. By supporting our esteemed department, corporate sponsors also promote their technology and brand. Your company can directly contribute to our wide array of activities such as our competitions, tech talks, workshops, and networking sessions.

Included in this document is a sponsorship tier list, which lists specific benefits for each level. Your involvement with ECE Pulse is entirely customizable, so if you have any questions, freely contact us at [ece-pulse@illinois.edu](mailto:ece-pulse@illinois.edu).

We look forward to working with you to make ECE Pulse even bigger and better!

Thank you,

ECE Pulse Team

[ece-pulse@illinois.edu](mailto:ece-pulse@illinois.edu)  
<http://pulse.ece.illinois.edu>

<b>ECE Pulse 2016 Sponsorship Tiers</b>	<b>Startups \$500</b>	<b>Amp \$1,250</b>	<b>Volt \$2,000</b>	<b>Charge \$3,500</b>	<b>Megawatt (only 1) \$7,000</b>
<b>Invitation to Private Networking Dinner</b>	X	X	X	X	X
<b>Networking Session in ECEB</b>	X	X	X	X	X
<b>Website Advertisement</b>	X	X	X	X	X
<b>T-shirt Logo (Provided to all participants)</b>	Small	Small	Medium	Large	Primary
<b>Tech Talk Slots</b>	Available Upon Request	Available Upon Request	Available Upon Request	Available Upon Request	Available Upon Request
<b>Access to Resume Book</b>	X	X	X	X	X
<b>Social Media Advertising</b>	X	X	X	X	X
<b>Giveaways (If Desired)</b>		X	X	X	X
<b>Swag Bag (Branded goods given to students)</b>		X	X	X	X
<b>Sponsored Prizes (Competition/Raffles) *1</b>			X	X	X
<b>Invitation to private networking dinner with staff &amp; competition winners</b>				X	X
<b>Technical Workshop</b>				X	X
<b>Exclusive Meeting with Student Organization</b>					X
<b>Exclusive Title Sponsorship (example: ECE Pulse Co-Hosted by "Company")</b>					X
<b>Early Access to Resume Books</b>					X

\* For sponsorship packages with customized combinations of the above options, feel free to email us at [ece-pulse@illinois.edu](mailto:ece-pulse@illinois.edu).

\*1: Must request for sponsored competition/prizes and is on a first come first serve basis; email us at [ece-pulse@illinois.edu](mailto:ece-pulse@illinois.edu)

## ECE PULSE Tentative Schedule

<u>Date</u>	<u>Event</u>	<u>Start Time</u>	<u>End Time</u>
Saturday (1/23/16)	Competitions Start	10:00 AM	
	Breakfast	9:30 AM	10:30 AM
	Lunch	12:00 PM	1:00 PM
	Dinner	5:30 PM	6:30 PM
	Competitions End		10:00 PM
Thursday (1/28/16)	Registration	4:30 PM	5:00 PM
	Introduction Video	5:00 PM	5:05 PM
	Director's Welcome	5:05 PM	5:15 PM
	Department Head's Welcome	5:15 PM	5:30 PM
	Keynote Introduction	5:30 PM	5:35 PM
	Keynote Speech	6:30 PM	6:30 PM
	Keynote Q & A	6:30 PM	6:45 PM
	Dinner	6:45 PM	-
Friday (1/29/16)	Tech Talk #1	5:00 PM	6:00 PM
	Dinner	6:00 PM	6:30 PM
	Workshop 1	6:30 PM	7:00 PM
Saturday (1/30/16)	Workshop 2	10:00 AM	11:00 AM
	Tech Talk #2	11:15 AM	12:15 PM
	Lunch	12:15 PM	1:15 PM
	Tech Talk #3	1:15 PM	2:15 PM
	Workshop #3	2:30 PM	3:30 PM
	Tech Talk #4	3:45 PM	4:45 PM
	Startup Panel	5:00 PM	6:15 PM
	Exclusive Dinner	6:30 PM	8:00 PM
	Social Mixer	8:00 PM	10:00 PM

## ECE PULSE Curriculum Information

For sponsors who would like to contribute with a tech talk or with a competition design, we have provided you the following information for you to better understand our curriculum. Based on the listed courses, sponsors can better cater their presentations towards our students and connect with their coursework.

### Required Electrical/Computer Engineering Core Curriculum

Course Number	Core Concepts
<b>ECE 110:</b> Intro to Electrical & Computer Eng	<ul style="list-style-type: none"><li>• DC circuit analysis</li><li>• Discrete components</li></ul>
<b>ECE 120:</b> Computer Eng I	<ul style="list-style-type: none"><li>• Digital logic</li><li>• Computer organization</li><li>• </li></ul>
<b>ECE 210:</b> Analog Signal Processing	<ul style="list-style-type: none"><li>• Fourier Analysis</li><li>• Frequency domain analysis</li></ul>
<b>ECE 220:</b> Intro to Computing Systems	<ul style="list-style-type: none"><li>• C Programming</li><li>• Binary representations</li></ul>
<b>ECE 313:</b> Probability with Eng Applications	<ul style="list-style-type: none"><li>• Probability distributions (Gaussian, etc)</li><li>• Applications of probability</li></ul>
<b>ECE 329:</b> Fields and Waves I	<ul style="list-style-type: none"><li>• Maxwell's equations</li><li>• Transmission line analysis</li></ul>
<b>ECE 340:</b> Semiconductor Devices	<ul style="list-style-type: none"><li>• BJTs, MOSFETs, p-n junctions</li><li>• Semiconductor fundamentals</li></ul>
<b>ECE 385:</b> Digital System Design Lab	<ul style="list-style-type: none"><li>• VHDL or Verilog</li><li>• Digital system design using FPGA</li></ul>

### Required Supplementary Computer Engineering Curriculum

Course Number	Core Concepts
<b>CS 173:</b> Discrete Structures	<ul style="list-style-type: none"><li>• Sets, propositions, Boolean algebra</li><li>• Induction, recursion, graphs</li></ul>
<b>CS 225:</b> Data Structures & Software Principles	<ul style="list-style-type: none"><li>• C++ and basics of object-oriented programming</li><li>• Data structures (linked lists, trees, graphs)</li></ul>
<b>ECE 391:</b> Computer Systems Engineering	<ul style="list-style-type: none"><li>• Device programming</li><li>• Operating system fundamentals</li></ul>
<b>ECE 411:</b> Computer Organization & Design	<ul style="list-style-type: none"><li>• Instruction set architectures</li><li>• Memory organization</li><li>• Input-Output</li></ul>

## Required Supplementary Electrical Engineering Curriculum

Course Number	Core Concepts
<b>ECE 445:</b> Senior Design Project Lab	<ul style="list-style-type: none"> <li>• Team project development</li> <li>• Circuitry design and layout</li> <li>• Documentation process</li> </ul>

## Additional Electrical & Computer Engineering Electives

Course Number	Core Concepts
<b>ECE 310:</b> Digital Signal Processing	<ul style="list-style-type: none"> <li>• Discrete-time signal processing</li> <li>• Digital filter design</li> </ul>
<b>ECE 330:</b> Power Circuits and Electromechanics	<ul style="list-style-type: none"> <li>• Magnetic circuits</li> <li>• Three-phase circuits</li> </ul>
<b>ECE 342:</b> Electronic Circuits	<ul style="list-style-type: none"> <li>• Large-signal and small-signal circuit analysis</li> <li>• Diodes, transistors, amplifiers</li> </ul>
<b>ECE 350:</b> Fields and Waves II	<ul style="list-style-type: none"> <li>• Radiation, antennas, waveguides</li> <li>• Plane-wave propagation</li> </ul>
<b>ECE 361:</b> Digital Communications	<ul style="list-style-type: none"> <li>• Communication over additive Gaussian noise, wireline, wireless</li> <li>• Signal reliability</li> </ul>
<b>ECE 408:</b> Applied Parallel Programming	<ul style="list-style-type: none"> <li>• Computational thinking and parallelism</li> <li>• Efficient data structures</li> </ul>
<b>ECE 420:</b> Embedded DSP Lab	<ul style="list-style-type: none"> <li>• Sampling, digital filtering on chips</li> <li>• Real-time DSP applications</li> </ul>
<b>ECE 425:</b> Intro to VLSI Design	<ul style="list-style-type: none"> <li>• CMOS circuit and logic design</li> <li>• VLSI system design methods</li> </ul>
<b>ECE 431:</b> Electric Machinery	<ul style="list-style-type: none"> <li>• Induction and DC machines</li> <li>• Synchronous machines</li> </ul>
<b>ECE 438:</b> Computer Networking	<ul style="list-style-type: none"> <li>• TCP/IP and network protocols</li> <li>• Data link and multi-access protocols</li> </ul>
<b>ECE 444:</b> IC Device Theory & Fabrication	<ul style="list-style-type: none"> <li>• Photolithography and etching</li> <li>• Fabrication of IC devices</li> </ul>
<b>ECE 453:</b> Wireless Communication Systems	<ul style="list-style-type: none"> <li>• Phase-locked loops, modulation</li> <li>• Two-port network analysis</li> </ul>
<b>ECE 464:</b> Power Electronics	<ul style="list-style-type: none"> <li>• Switching converter topologies</li> <li>• Real component analysis</li> </ul>
<b>ECE 482:</b> Digital IC Design	<ul style="list-style-type: none"> <li>• Design of VLSI circuits</li> <li>• Programmable logic arrays</li> </ul>

<b>ECE 483:</b> Analog IC Design	<ul style="list-style-type: none"><li>• Large and small signal analysis</li><li>• Op amp design, feedback amplifiers</li></ul>
<b>ECE 486:</b> Control Systems	<ul style="list-style-type: none"><li>• Modeling, state space analysis</li><li>• Root locus and frequency response design method</li></ul>

*For a more complete list of the courses offered, visit [ece.illinois.edu/courses](http://ece.illinois.edu/courses).*