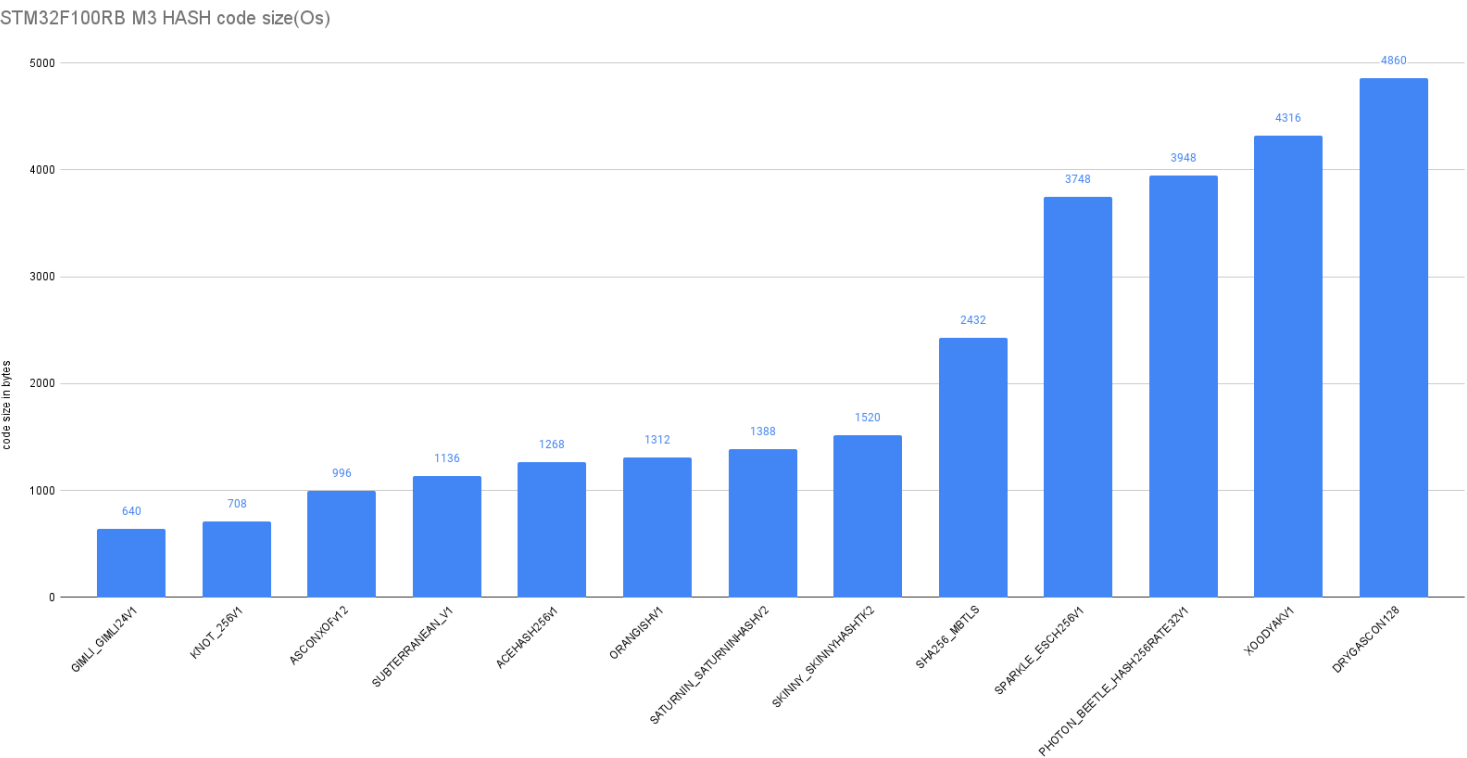
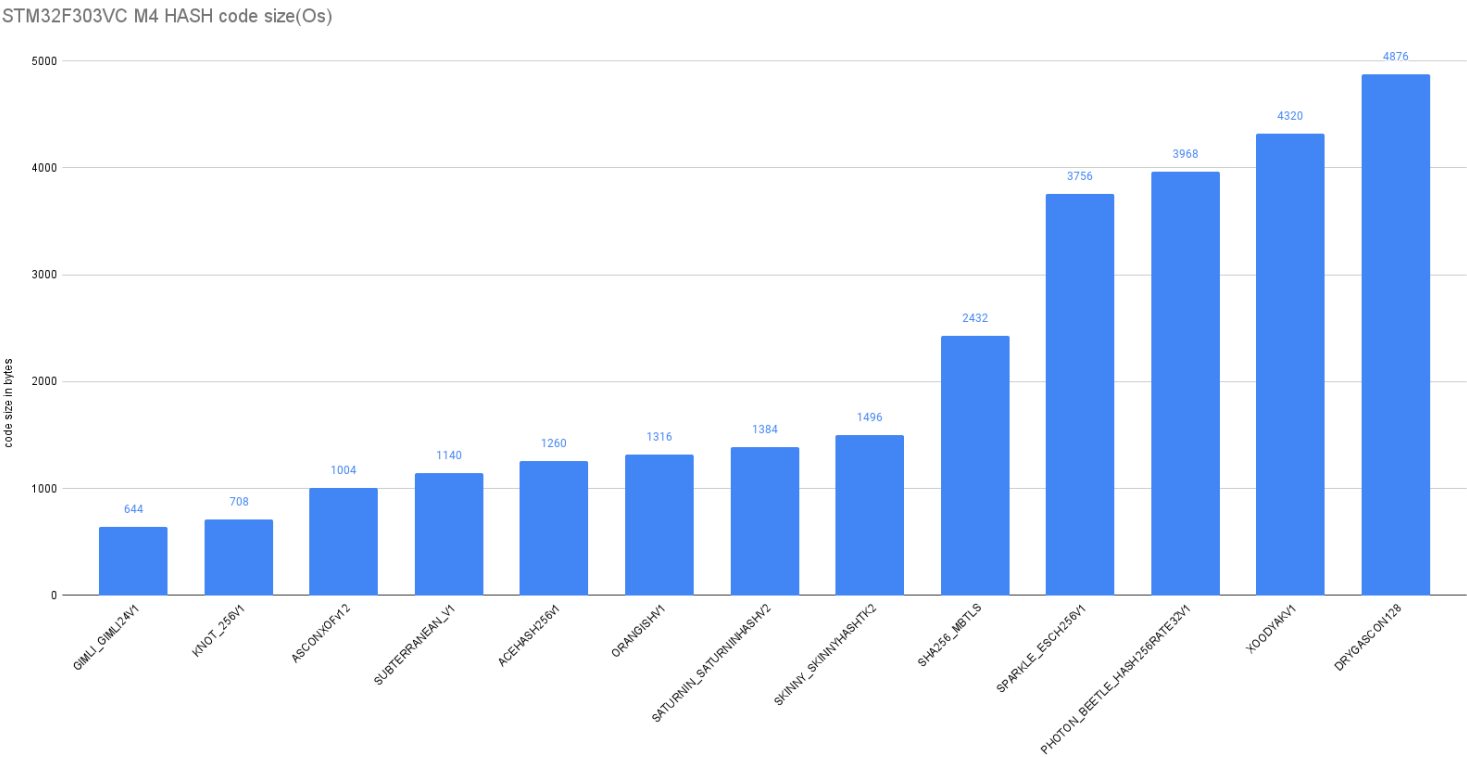
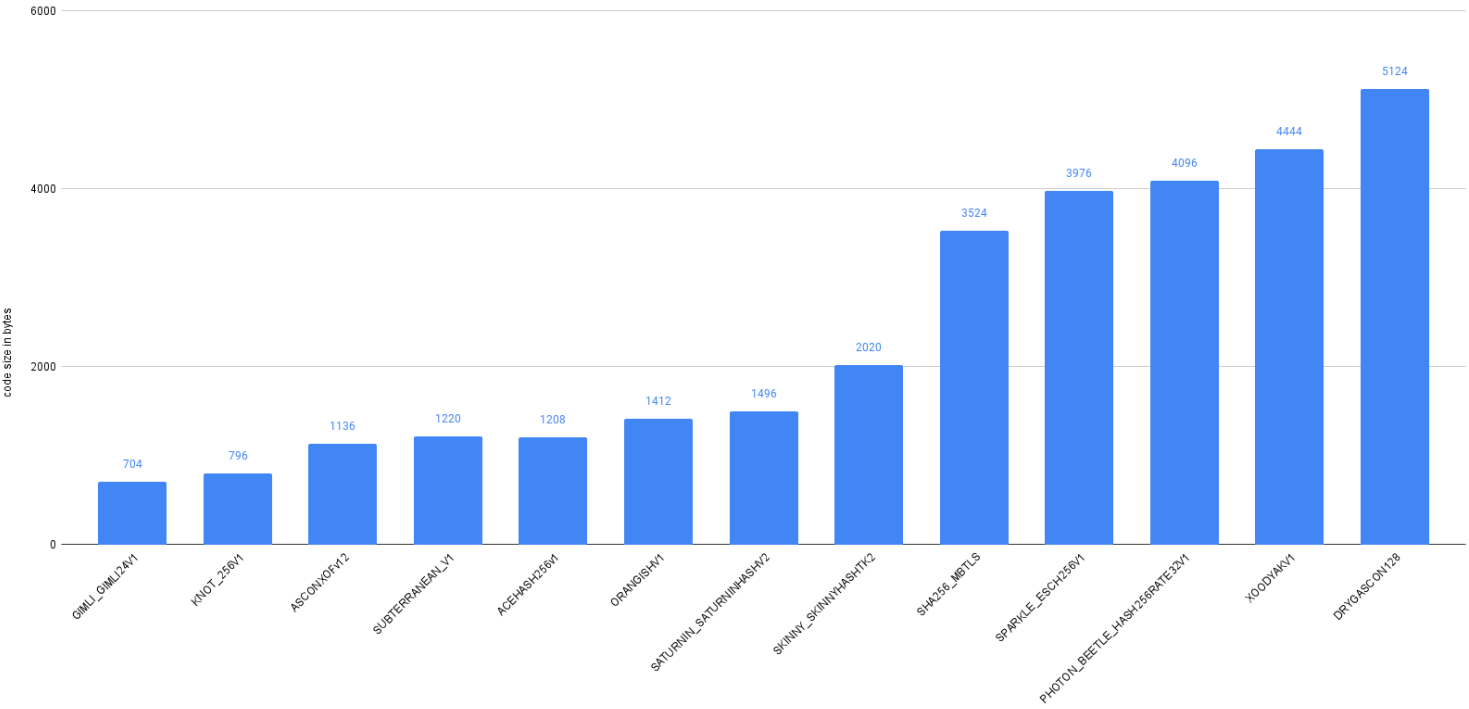


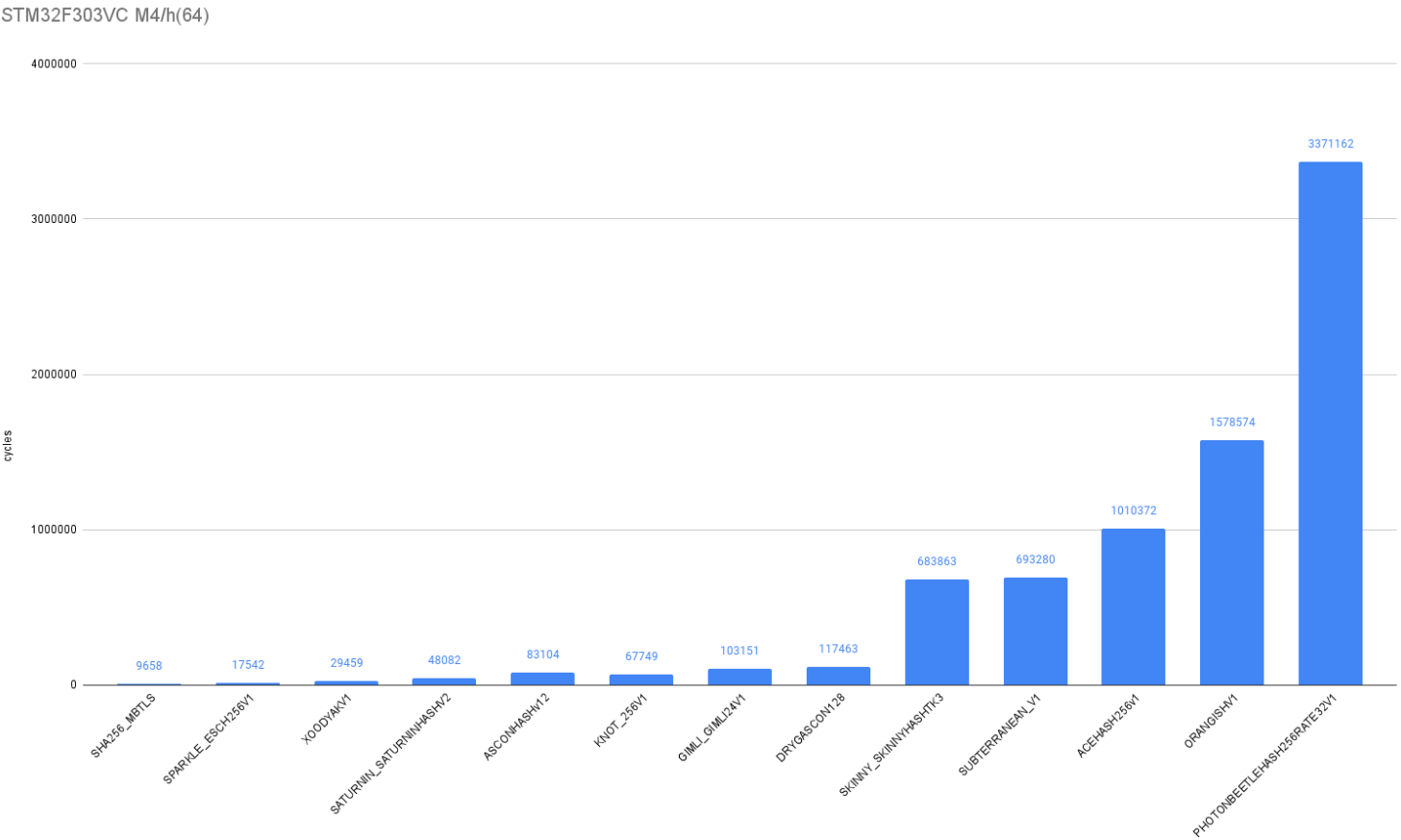
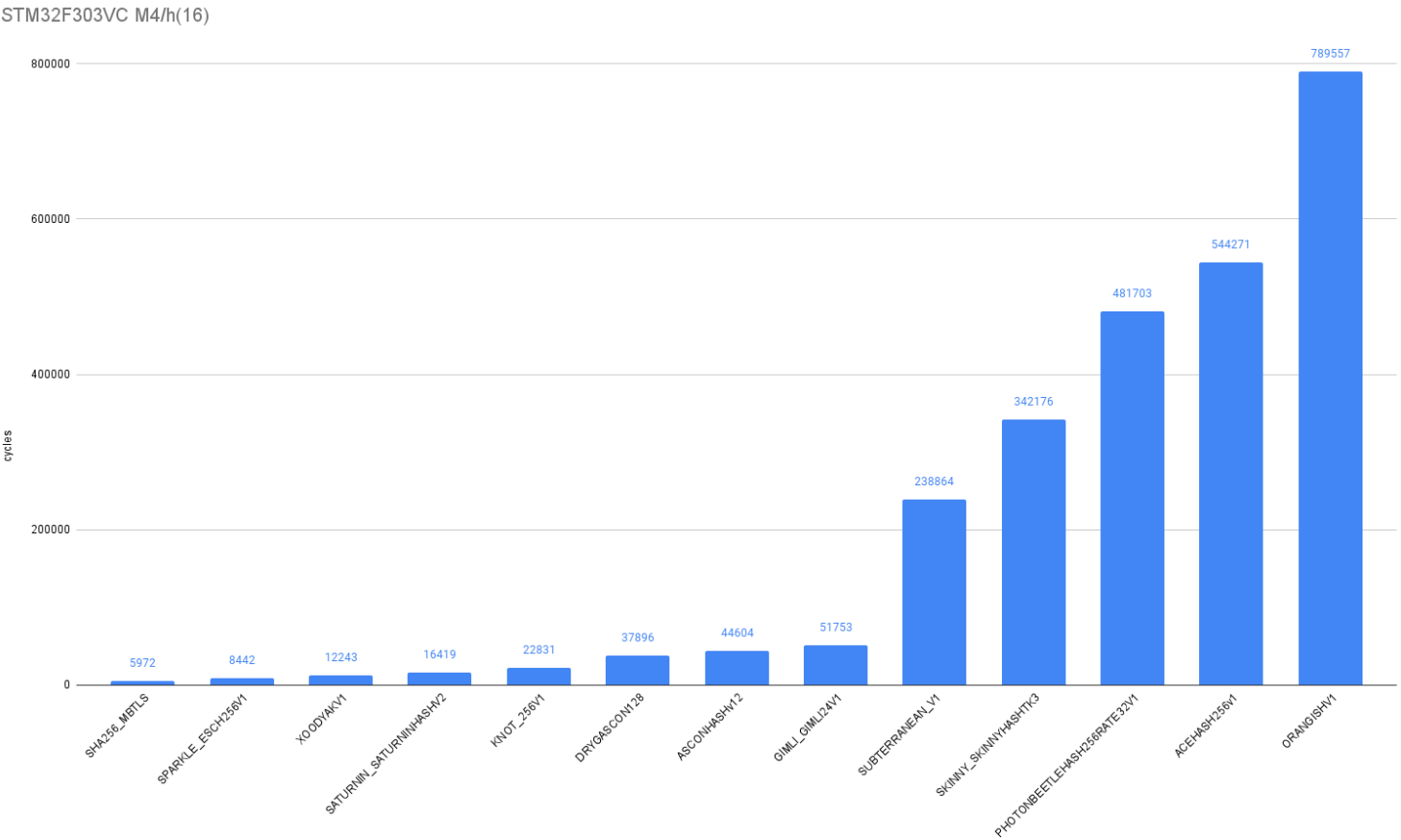
# Code size of different HASH algorithms on Cortex-M0, Cortex-M3 and Cortex-M4 Platforms

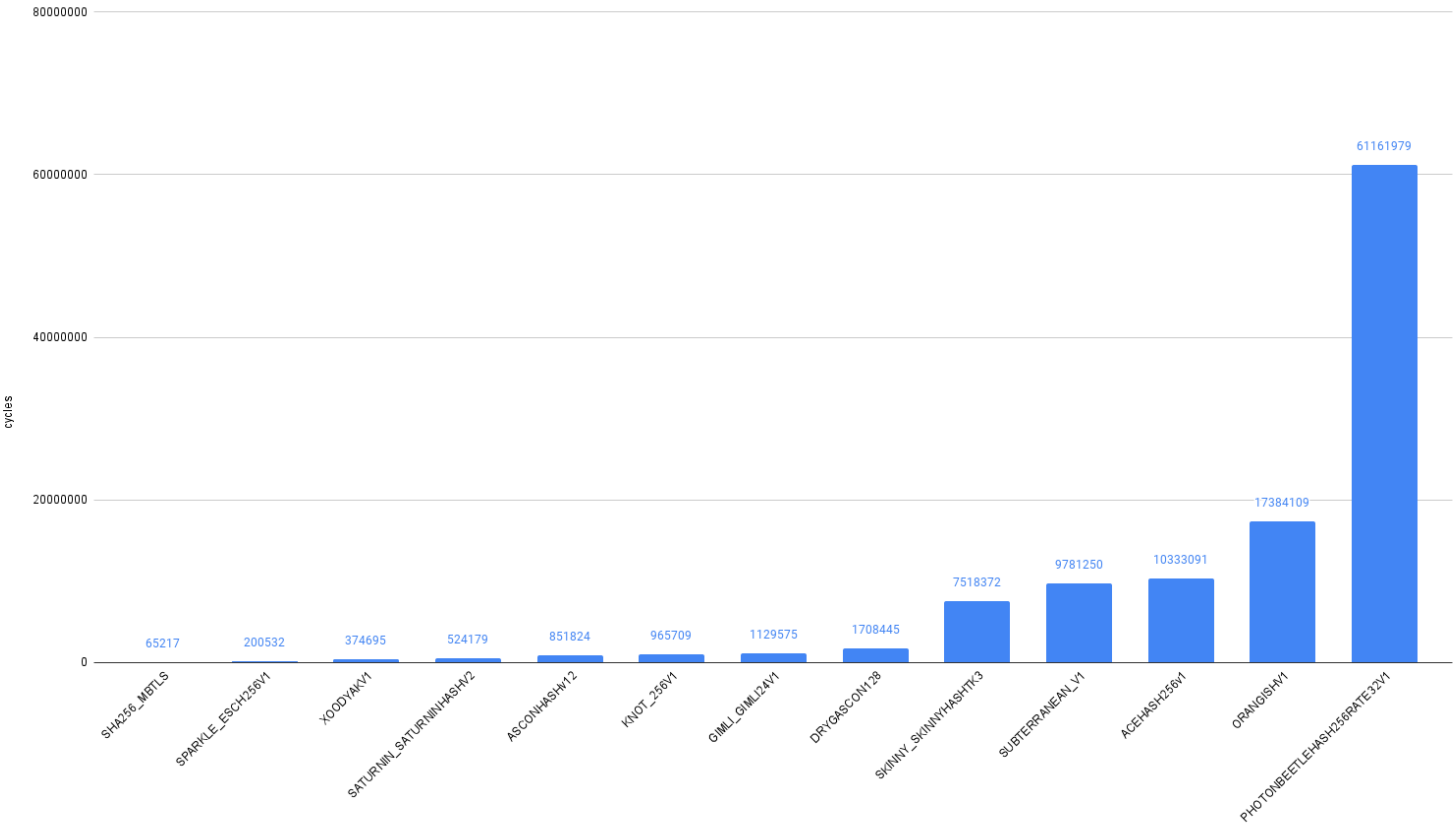


STM32F051R8 M0 HASH code size(0s)

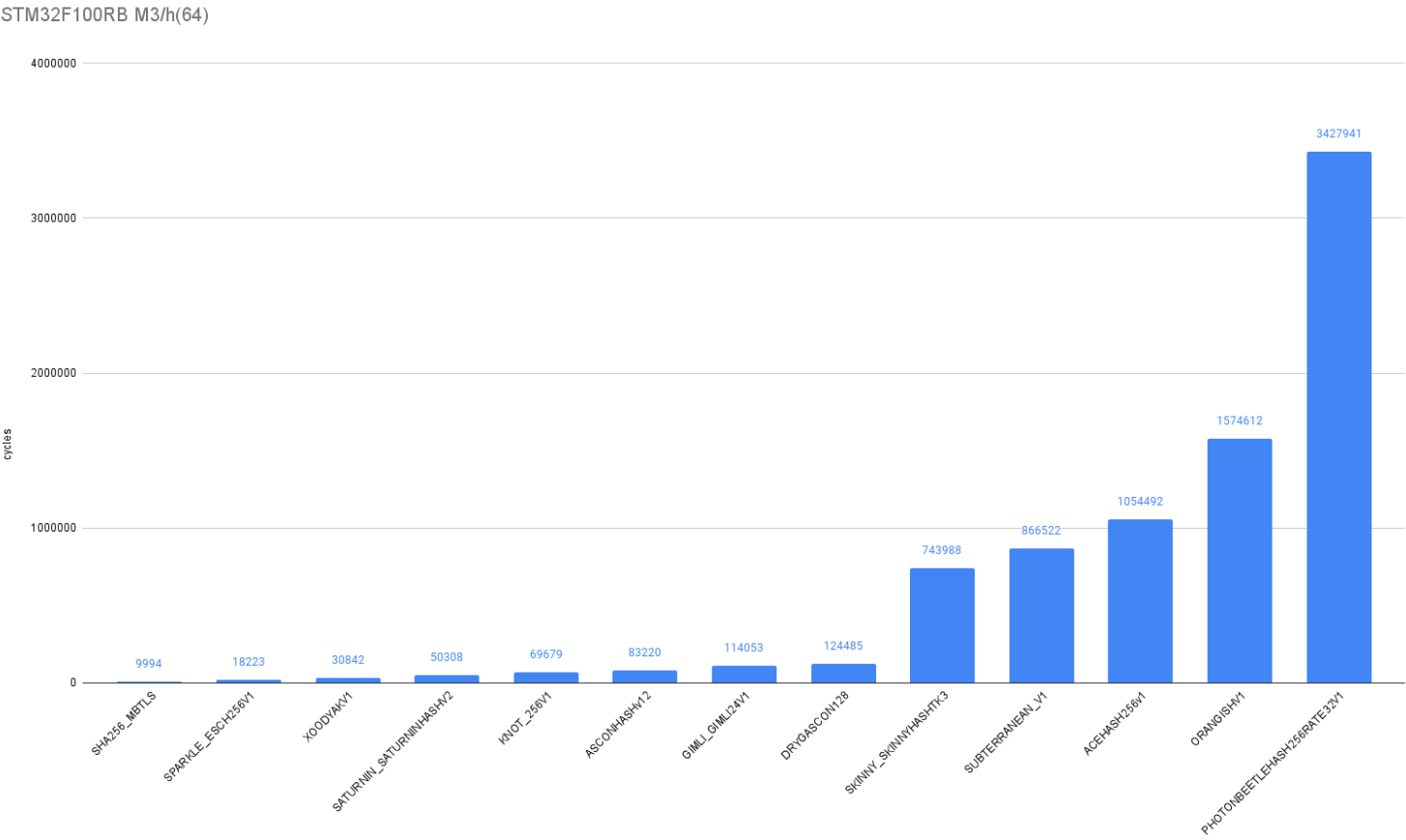
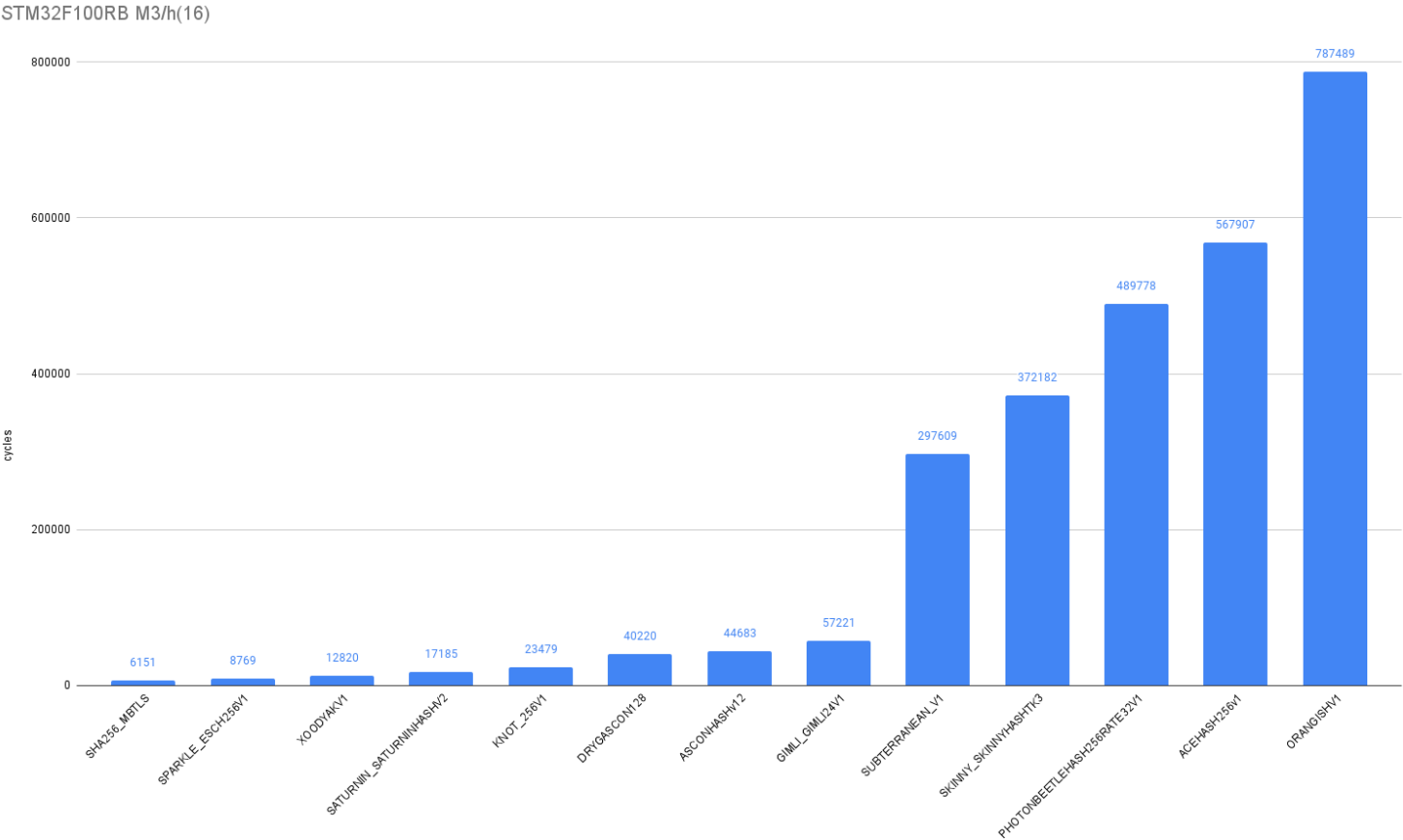


# Execution cycles of different HASH algorithms on Cortex-M4

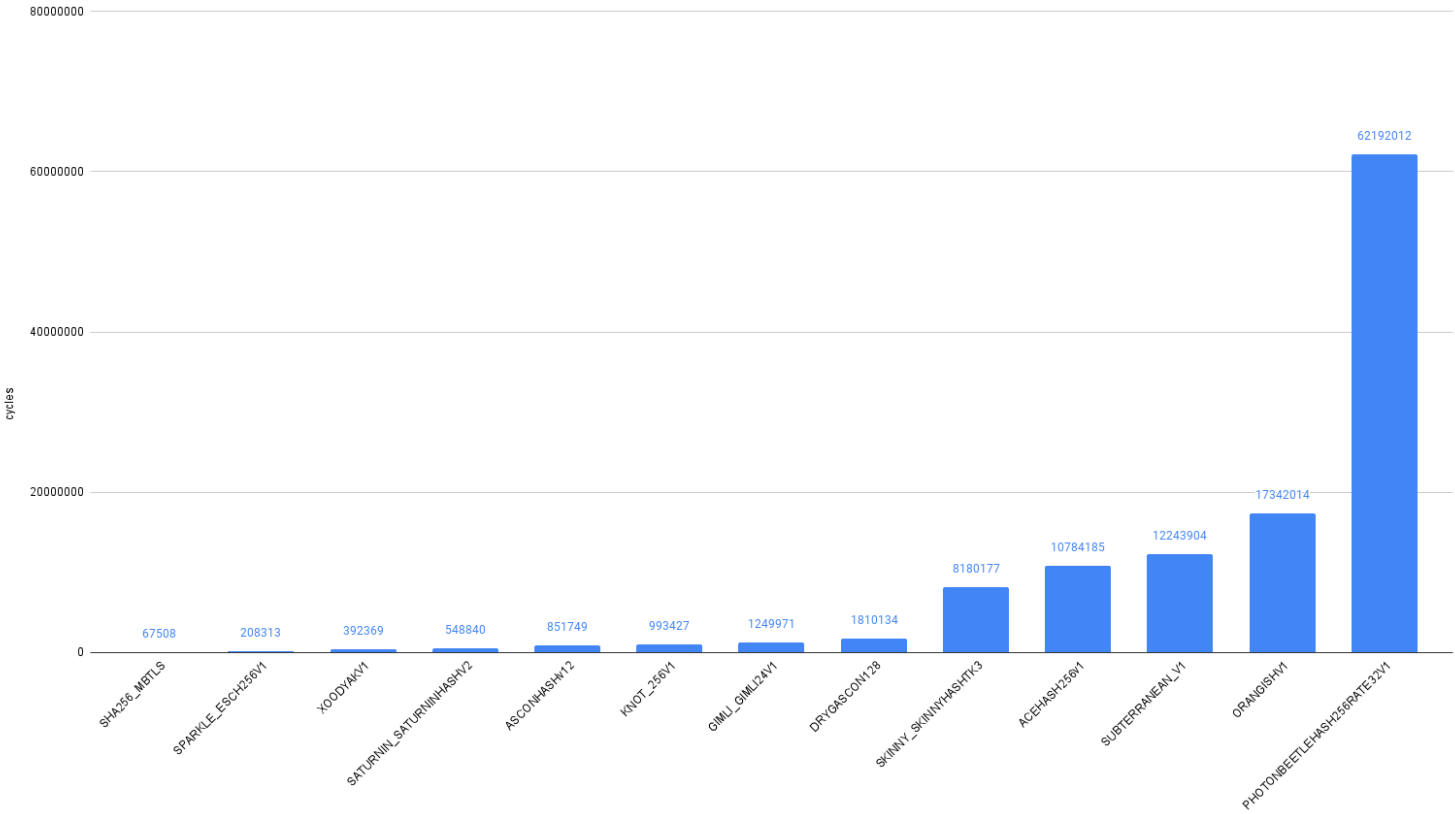




# Execution cycles of different HASH algorithms on Cortex-M3



STM32F100RB M3/h(1024)



# Execution cycles of different HASH algorithms on Cortex-M0

