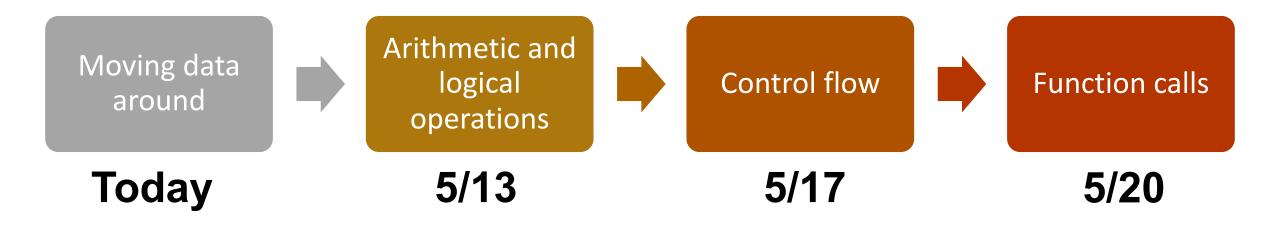
CS107, Lecture 11 Introduction to Assembly

Reading: B&O 3.1-3.4

CS107 Topic 6: How does a computer interpret and execute C programs?

Learning Assembly



Today's Learning Goals

- Learn what assembly language is and why it is important
- Become familiar with the format of human-readable assembly and x86
- Learn the mov instruction and how data moves around at the assembly level

Plan For Today

- Overview: GCC and Assembly
- **Demo:** Looking at an executable
- Registers and The Assembly Level of Abstraction
- A Brief History
- Our First Assembly
- Break: Announcements
- The mov instruction

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GCC

- GCC is the compiler that converts your human-readable code into machinereadable instructions.
- C, and other languages, are high-level abstractions we use to write code efficiently. But computers don't really understand things like data structures, variable types, etc. Compilers are the translator!
- Pure machine code is 1s and 0s everything is bits, even your programs! But we can read it in a human-readable form called **assembly**. (Engineers used to write code in assembly before C).
- There may be multiple assembly instructions needed to encode a single C instruction.
- We're going to go behind the curtain to see what the assembly code for our programs looks like.

Demo: Looking at an Executable (objdump -d)



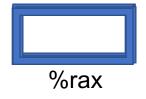
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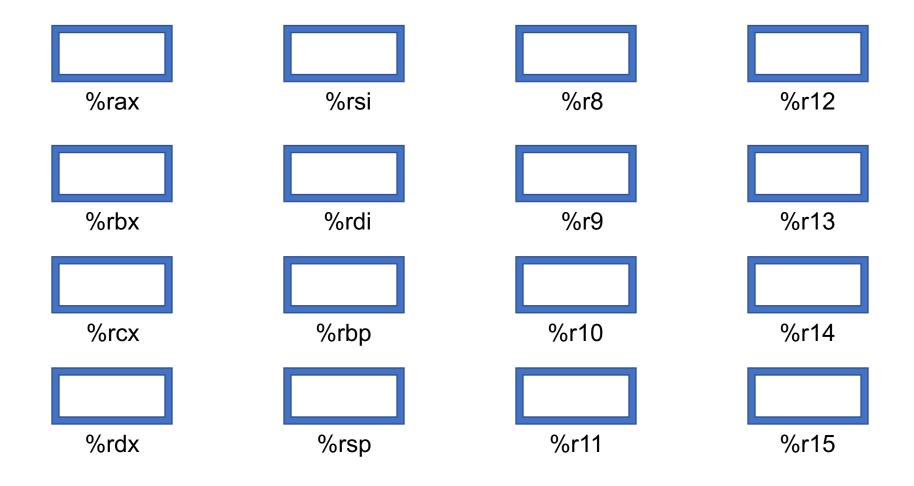
Assembly Abstraction

- C abstracts away the low level details of machine code. It lets us work using variables, variable types, and other higher level abstractions.
- C and other languages let us write code that works on most machines.
- Assembly code is just bytes! No variable types, no type checking, etc.
- Assembly/machine code is processor-specific.
- What is the level of abstraction for assembly code?

Registers



Registers



Registers

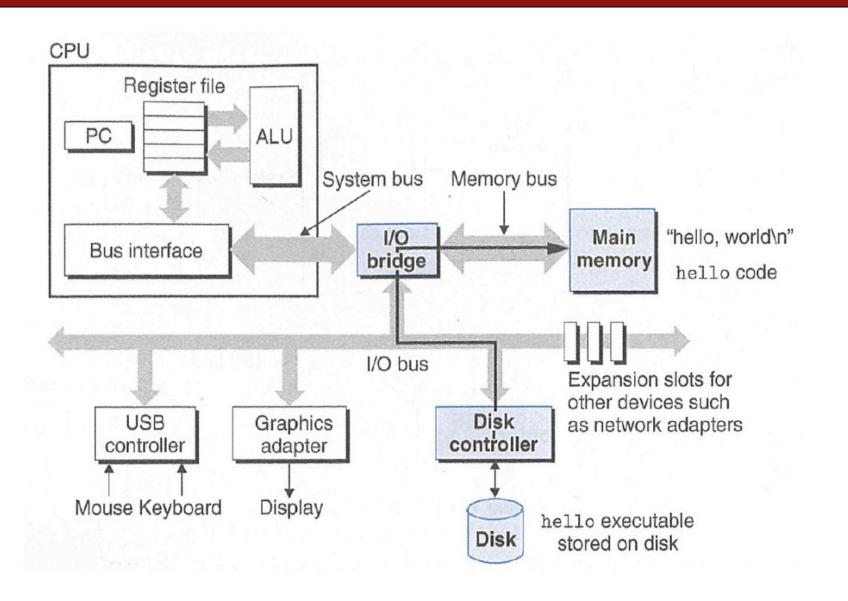
- A **register** is a 64-bit space inside the processor.
- There are 16 registers available, each with a unique name.
- Registers are like "scratch paper" for the processor. Data being calculated or manipulated is moved to registers first. Operations are performed on registers.
- Registers also hold parameters and return values for functions.
- Registers are extremely fast memory!
- Processor instructions consist mostly of moving data into/out of registers and performing arithmetic on them. This is the level of logic your program must be in to execute!

Machine-Level Code

Assembly instructions manipulate these registers. For example:

- One instruction adds two numbers in registers
- One instruction transfers data from a register to memory
- One instruction transfers data from memory to a register

Computer Architecture



GCC And Assembly

- GCC compiles your program it lays out memory on the stack and heap and generates assembly instructions to access and do calculations on those memory locations.
- Here's what the "assembly-level abstraction" of C code might look like:

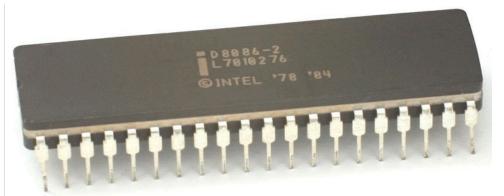
C	Assembly Abstraction
int sum = x + y;	 Copy x into register 1 Copy y into register 2 Add register 2 to register 1 Write register 1 to memory for sum

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Assembly

- We are going to learn the **x86-64** instruction set architecture. This instruction set is used by Intel and AMD processors.
- There are many other instruction sets: ARM, MIPS, etc.
- Intel originally designed their instruction set back in 1978. It has evolved significantly since then, but has aggressively preserved backwards compatibility.
- Originally 16 bit processor -> then 32 -> now 64 bit. This dictated the register sizes (and even register names).



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```
int sum_array(int arr[], int nelems) {
   int sum = 0;
   for (int i = 0; i < nelems; i++) {
      sum += arr[i];
   }
  return sum;
}</pre>
```

What does this look like in assembly?

00000000004005b6 <sum_array>:

```
ba 00 00 00 00
                                     $0x0,%edx
4005b6:
                              mov
4005bb:
       b8 00 00 00 00
                                     $0x0,%eax
                              mov
4005c0:
       eb 09
                                     4005cb <sum array+0x15>
                              jmp
                              movslq %edx,%rcx
4005c2:
          48 63 ca
                                     (%rdi,%rcx,4),%eax
          03 04 8f
                              add
4005c5:
       83 c2 01
                                     $0x1,%edx
                              add
4005c8:
       39 f2
                                     %esi,%edx
4005cb:
                              cmp
       7c f3
                              j1
4005cd:
                                     4005c2 <sum array+0xc>
          f3 c3
4005cf:
                              repz reta
```

```
00000000004005b6 <sum_array>:
                                           $0x0,%edx
                                    mov
                                           $0x0,%eax
                                    mov
                                           4005cb <sum array+0x15>
This is the name of the function (same
                                     mp
                                     lovslq %edx,%rcx
as C) and the memory address where
                                     dd
                                           (%rdi,%rcx,4),%eax
the code for this function starts.
                                     dd
                                           $0x1,%edx
                                           %esi,%edx
  4005cb:
                                    cmp
  4005cd:
                                           4005c2 <sum array+0xc>
          7c f3
  4005cf: f3 c3
                                    repz reta
```

```
00000000004005b6 <sum_array>:
  4005b6:
                                            $0x0,%edx
             ha 99 99 99 99
                                    MOV
  4005bb:
                                           $0x0,%eax
                   00 00 00
                                    MOV
  4005c0:
                                                 array+0x15>
               These are the memory addresses where
  4005c2:
                                                       ,4),%eax
  4005c5:
               each of the instructions live. Sequential
  4005c8:
               instructions are sequential in memory.
  4005cb:
  4005cd:
                                           4005c2 <sum array+0xc>
             7c f3
  4005cf:
             f3 c3
                                    repz reta
```

```
00000000004005b6 <sum_array>:
```

```
      4005b6:
      ba 00 00 00 00

      4005bb:
      b8 00 00 00 00
```

This is the assembly code:

"human-readable" versions of each machine code instruction.

```
4005cd: 7c f3
4005cf: f3 c3
```

```
mov $0x0,%edx
mov $0x0,%eax
jmp 4005cb <sum_array+0x15>
movslq %edx,%rcx
add (%rdi,%rcx,4),%eax
add $0x1,%edx
cmp %esi,%edx
jl 4005c2 <sum_array+0xc>
repz retq
```

00000000004005b6 <sum_array>:

```
      4005b6:
      ba 00 00 00 00

      4005bb:
      b8 00 00 00 00

      4005c0:
      eb 09

      4005c2:
      48 63 ca

      4005c5:
      03 04 8f

      4005c8:
      83 c2 01

      4005cb:
      39 f2

      4005cd:
      7c f3
```

4005cf: f3 c3

This is the machine code: raw hexadecimal instructions, representing binary as read by the computer. Different instructions may be different byte lengths.

00000000004005b6 <sum_array>:

```
ba 00 00 00 00
                                     $0x0,%edx
4005b6:
                              mov
4005bb:
       b8 00 00 00 00
                                     $0x0,%eax
                              mov
4005c0:
       eb 09
                                     4005cb <sum array+0x15>
                              jmp
                              movslq %edx,%rcx
4005c2:
          48 63 ca
                                     (%rdi,%rcx,4),%eax
          03 04 8f
                              add
4005c5:
       83 c2 01
                                     $0x1,%edx
                              add
4005c8:
       39 f2
                                     %esi,%edx
4005cb:
                              cmp
       7c f3
                              j1
4005cd:
                                     4005c2 <sum array+0xc>
          f3 c3
4005cf:
                              repz reta
```

```
00000000004005b6 <sum_array>:
                                      $0x0,%edx
 4005b6:
            ba 00 00 00 00
                               mov
         b8 00 00 00 00
                                      $0x0,%eax
 4005bb:
                               mov
 4005c0: eb 09
                               jmp
                                      4005cb <sum array+0x15>
                               movslq %edx,%rcx
 4005c2: 48 63 ca
                               add (%rdi, %rcx, 4), %eax
 4005c5:
         03 04 8f
                                      $0x1,%edx
         83 c2 01
                                add
 4005c8:
                                      %esi,%edx
 4005cb:
         39 f2
 4005cd: 7c f3
                                      4005c2 <sum array+0xc>
 4005cf: f3 c3
                                    reta
```

Each instruction has an operation name ("opcode").

```
00000000004005b6 <sum_array>:
 4005b6:
            ba 00 00 00 00
 4005bb:
         b8 00 00 00 00
 4005c0: eb 09
         48 63 ca
 4005c2:
 4005c5:
         03 04 8f
         83 c2 01
 4005c8:
 4005cb:
           39 f2
 4005cd:
         7c f3
        f3 c3
 4005cf:
```

```
mov $0x0,%edx
mov $0x0,%eax
jmp 4005cb <sum_array+0x15>
movslq %edx,%rcx
add (%rdi,%rcx,4),%eax
add $0x1,%edx
cmp %esi,%edy
jl 4005c2 <sum_array+0xc>
ach instruction can also have
```

Each instruction can also have arguments ("operands").

00000000004005b6 <sum_array>:

```
      4005b6:
      ba 00 00 00 00

      4005bb:
      b8 00 00 00 00

      4005c0:
      eb 09

      4005c2:
      48 63 ca

      4005c5:
      03 04 8f

      4005c8:
      83 c2 01

      4005cb:
      39 f2

      4005cf:
      7c f3

      4005cf:
      f3 c3
```

```
mov $0x0,%edx
mov $0x0,%eax
jmp 4005cb <sum_array+0x15>
movslq %edx,%rcx
add (%rdi,%rcx,4),%eax
add $0x1,%edx
cmp %eAi,%edx
jl 4005c2 <sum_array+0xc>
repz retq
```

\$[number] means a constant value (e.g. 1 here).

```
00000000004005b6 <sum_array>:
```

```
      4005b6:
      ba 00 00 00 00

      4005bb:
      b8 00 00 00 00

      4005c0:
      eb 09

      4005c2:
      48 63 ca

      4005c5:
      03 04 8f

      4005c8:
      83 c2 01

      4005cb:
      39 f2

      4005cd:
      7c f3
```

4005cf: f3 c3

```
mov $0x0,%edx
mov $0x0,%eax
jmp 4005cb <sum_array+0x15>
movslq %edx,%rcx
add (%rdi,%rcx,4),%eax
add $0x1,%edx
cmp %esi,%edx
jl 4005c2/<sum_array+0xc>
repz retq
```

%[name] means a register (e.g. edx here).

Announcements

- The midterm exam is Fri. 5/10 12:30-2:20PM in Nvidia Aud. and 420-041
 - Last names A-R: Nvidia Auditorium
 - Last Names S-Z: 420-041
- We have confirmed via email accommodations for all students who have requested midterm accommodations. If you expected accommodations but did not receive an email, please email the course staff immediately.
- We've added labels on Piazza for posts regarding the different practice exams and practice problems. Please use these when posting for quick organization!
- Assignment 4 on time deadline is tonight, assignment 5 goes out then and is due Fri. 5/17. We recommend starting to work on it after the midterm exam.

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mov

The **mov** instruction <u>copies</u> bytes from one place to another.

mov

src,dst

The **src** and **dst** can each be one of:

- Immediate (constant value, like a number)
- Register
- Memory Location (at most one of src, dst)

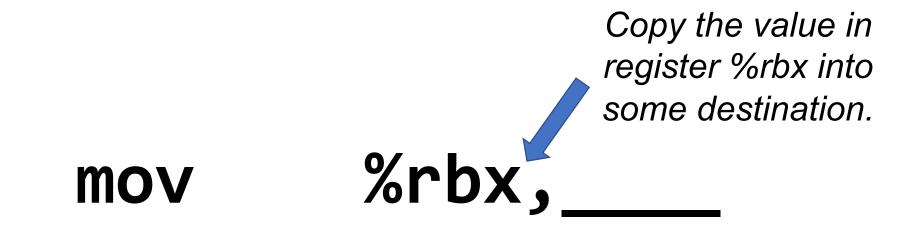
Operand Forms: Immediate

mov \$0x104, ______

Copy the value
0x104 into some

destination.

Operand Forms: Registers



mov ____,%rbx

Copy the value from some source into register %rbx.

Operand Forms: Absolute Addresses

Copy the value at address 0x104 into some destination.

MOV

0x104,

MOV

,0x104

Copy the value from some source into the memory at address 0x104.

Practice #1: Operand Forms

What are the results of the following move instructions? For this problem, assume the value 5 is stored at address 0x42, and the value 8 is stored in %rbx.

- 1. mov \$0x42,%rax
- 2. mov 0x42,%rax

3. mov %rbx,0x55

Operand Forms: Indirect

Copy the value at the address stored in register %rbx into some destination.

MOV

(%rbx),____

mov

____,(%rbx)

Copy the value from some source into the memory at the address stored in register %rbx.

Operand Forms: Base + Displacement

mov 0x10(%rax), _______

Copy the value at the address (<u>0x10 plus</u> what is stored in register %rax) into some destination.

MOV

,0x10(%rax)

Copy the value from some source into the memory at the address (<u>0x10</u> plus what is stored in register %rax).39

Operand Forms: Indexed

Copy the value at the address which is (the sum of the values in registers %rax and %rdx) into some destination.

(%rax, %rdx),

mov _____, (%rax, %rdx)

Copy the value from some source into the memory at the address which is (the sum of the values in registers %rax and %rdx).

mov

40

Operand Forms: Indexed

Copy the value at the address which is (the sum of <u>0x10 plus</u> the values in registers %rax and %rdx) into some destination.

mov

%rax and %rdx) into some destination.

0x10(%rax, %rdx),

MOV

,0x10(%rax,%rdx)

Copy the value from some source into the memory at the address which is (the sum of <u>0x10</u> <u>plus</u> the values in registers %rax and %rdx).

Practice #2: Operand Forms

What are the results of the following move instructions (executed separately)? For this problem, assume the value 0x11 is stored at address 0x10C, 0xAB is stored at address 0x104, 0x100 is stored in register %rax and 0x3 is stored in %rdx.

```
1. mov $0x42,(%rax)
```

Copy the value at the address which is (4 times the value in register %rdx) into some destination.

MOV

(, %rdx, 4),

MOV

,(,%rdx,4)

The scaling factor (e.g. 4 here) must be hardcoded to be either 1, 2, 4

Copy the value from some source into the memory at the address which is (4 times the value in register %rdx).

Copy the value at the address which is (4 times the value in register %rdx, **plus 0x4)**, into some destination.

MOV

MOV

Copy the value from some source into the memory at the address which is (4 times the value in register %rdx, plus 0x4).

Copy the value at the address which is (the value in register %rax plus 2 times the value in register %rdx) into some destination.

(%rax, %rdx, 2),

MOV

,(%rax,%rdx,2) **MOV**

> Copy the value from some source into the memory at the address which is (the value in register %rax plus 2 times the value in register %rdx).

Copy the value at the address which is (<u>0x4 plus</u> the value in register %rax plus 2 times the value in register %rdx) into some destination.

mov

mov

Copy the value from some source into the memory at the address which is (<u>0x4 plus</u> the value in register %rax plus 2 times the value in register %rdx).

Most General Operand Form

$$Imm(r_b, r_i, s)$$

is equivalent to...

$$Imm + R[r_b] + R[r_i]*s$$

Operand Forms

Туре	Form	Operand value	Name
Immediate	\$Imm	Imm	Immediate
Register	r_a	$R[r_a]$	Register
Memory	Imm	M[Imm]	Absolute
Memory	(\mathbf{r}_a)	$M[R[r_a]]$	Indirect
Memory	$Imm(r_b)$	$M[Imm + R[r_b]]$	Base + displacement
Memory	$(\mathbf{r}_b,\mathbf{r}_i)$	$M[R[\mathtt{r}_b] + R[\mathtt{r}_i]]$	Indexed
Memory	$Imm(\mathbf{r}_b,\mathbf{r}_i)$	$M[Imm + R[r_b] + R[r_i]]$	Indexed
Memory	$(,r_i,s)$	$M[R[r_i] \cdot s]$	Scaled indexed
Memory	$Imm(\mathbf{r}_i,\mathbf{s})$	$M[Imm + R[r_i] \cdot s]$	Scaled indexed
Memory	$(\mathbf{r}_b,\mathbf{r}_i,s)$	$M[R[r_b] + R[r_i] \cdot s]$	Scaled indexed
Memory	$Imm(\mathbf{r}_b,\mathbf{r}_i,s)$	$M[Imm + R[r_b] + R[r_i] \cdot s]$	Scaled indexed

Figure 3.3 Operand forms. Operands can denote immediate (constant) values, register values, or values from memory. The scaling factor s must be either 1, 2, 4, or 8.

Practice #3: Operand Forms

What are the results of the following move instructions (executed separately)? For this problem, assume the value 0x1 is stored in register %rcx, the value 0x100 is stored in register %rax, the value 0x3 is stored in register %rdx, and value 0x11 is stored at address 0x10C.

```
1. mov $0x42,0xfc(,%rcx,4)
```

Recap

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Next time: diving deeper into assembly