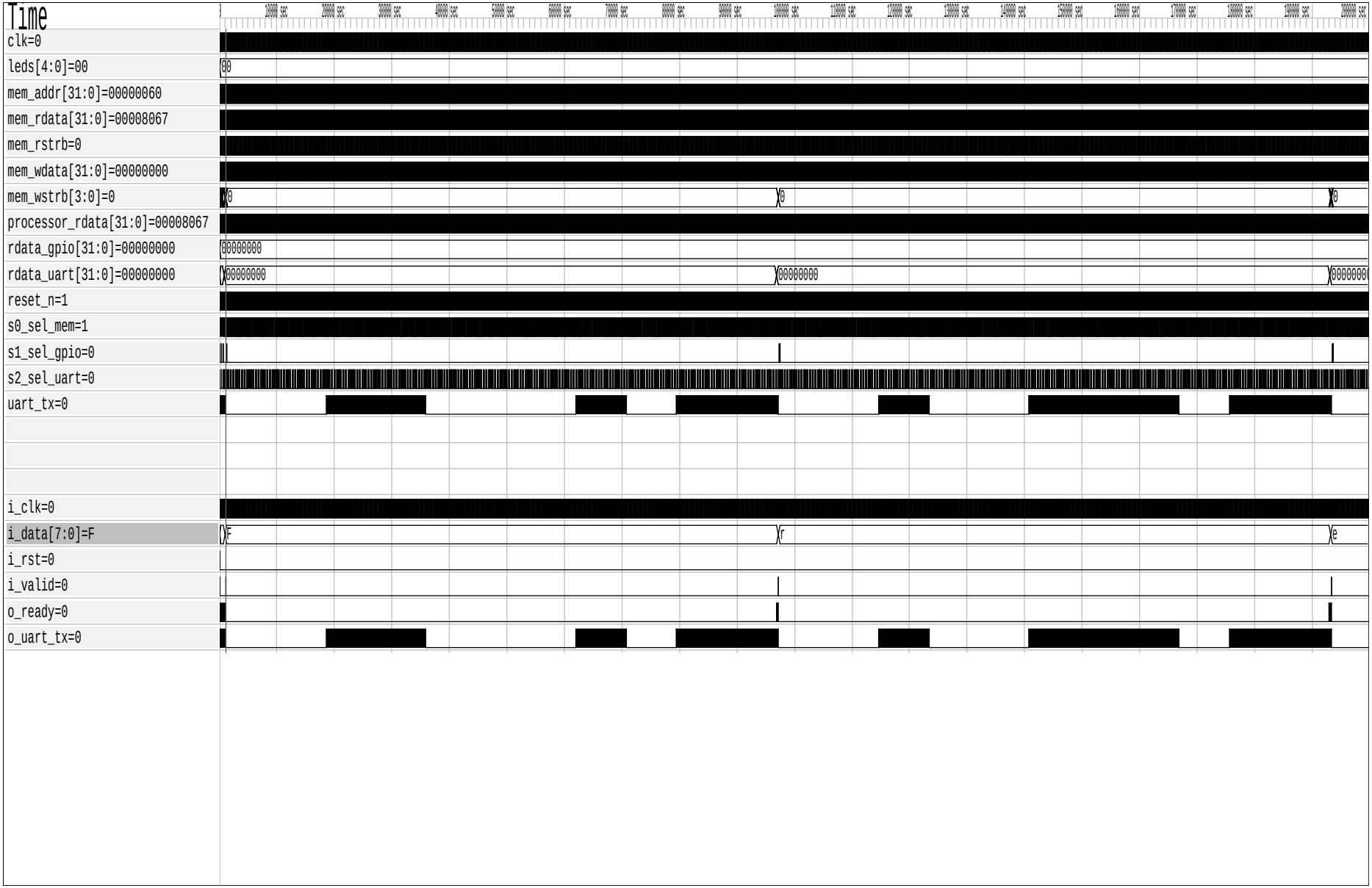
Time
clk=0
leds[4:0]=00
mem_addr[31:0]=00000060
mem_rdata[31:0]=00008067
mem_rstrb=0
mem_wdata[31:0]=00000000
mem_wstrb[3:0]=0
processor_rdata[31:0]=00008067
rdata_gpio[31:0]=00000000
rdata_uart[31:0]=00000000
reset_n=1
s0_sel_mem=1
s1_sel_gpio=0
s2_sel_uart=0
uart_tx=0
i_clk=0
i_data[7:0]=F
i_rst=0
i_valid=0
o_ready=0
o_uart_tx=0