Entire Chip Layout

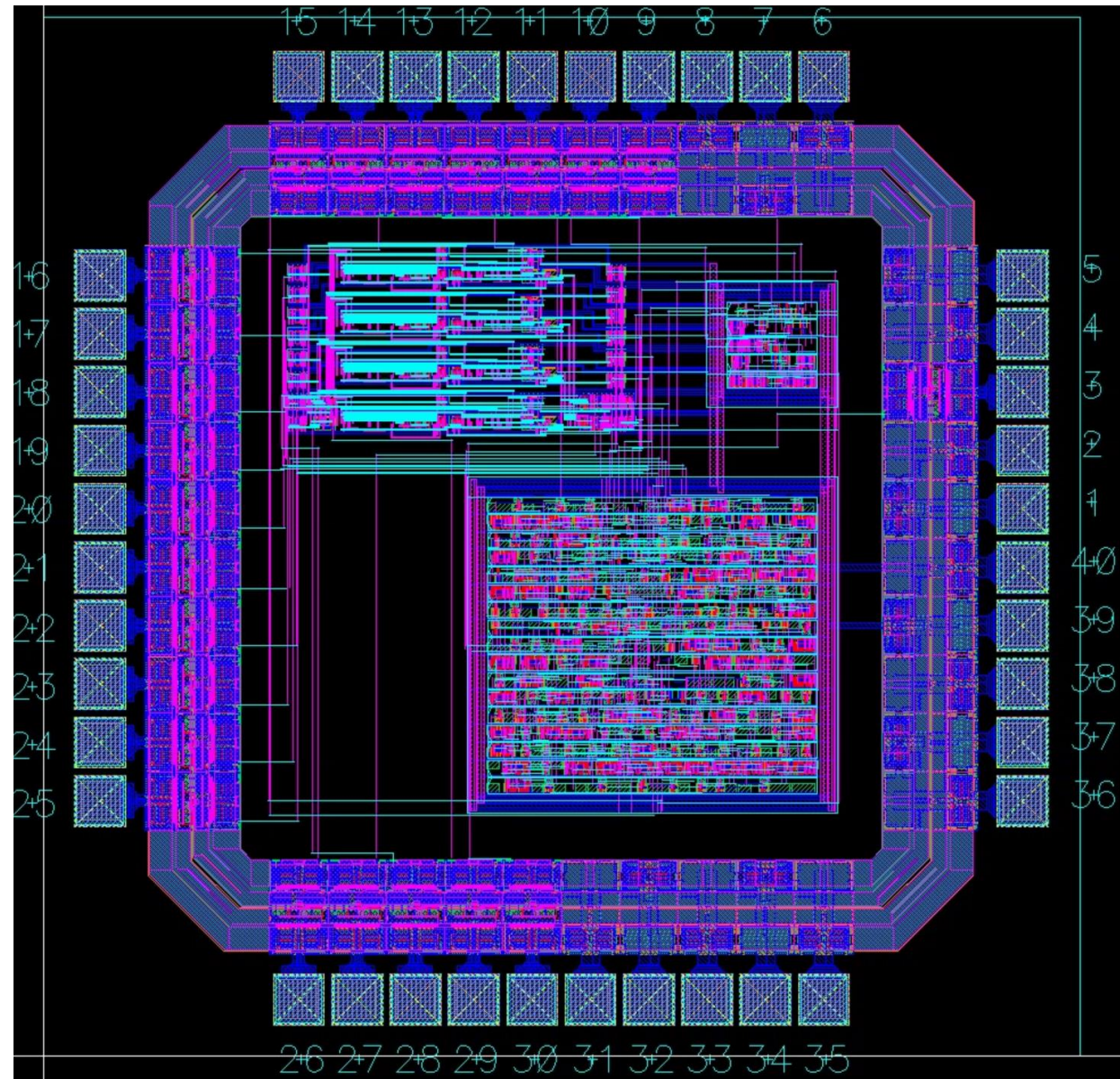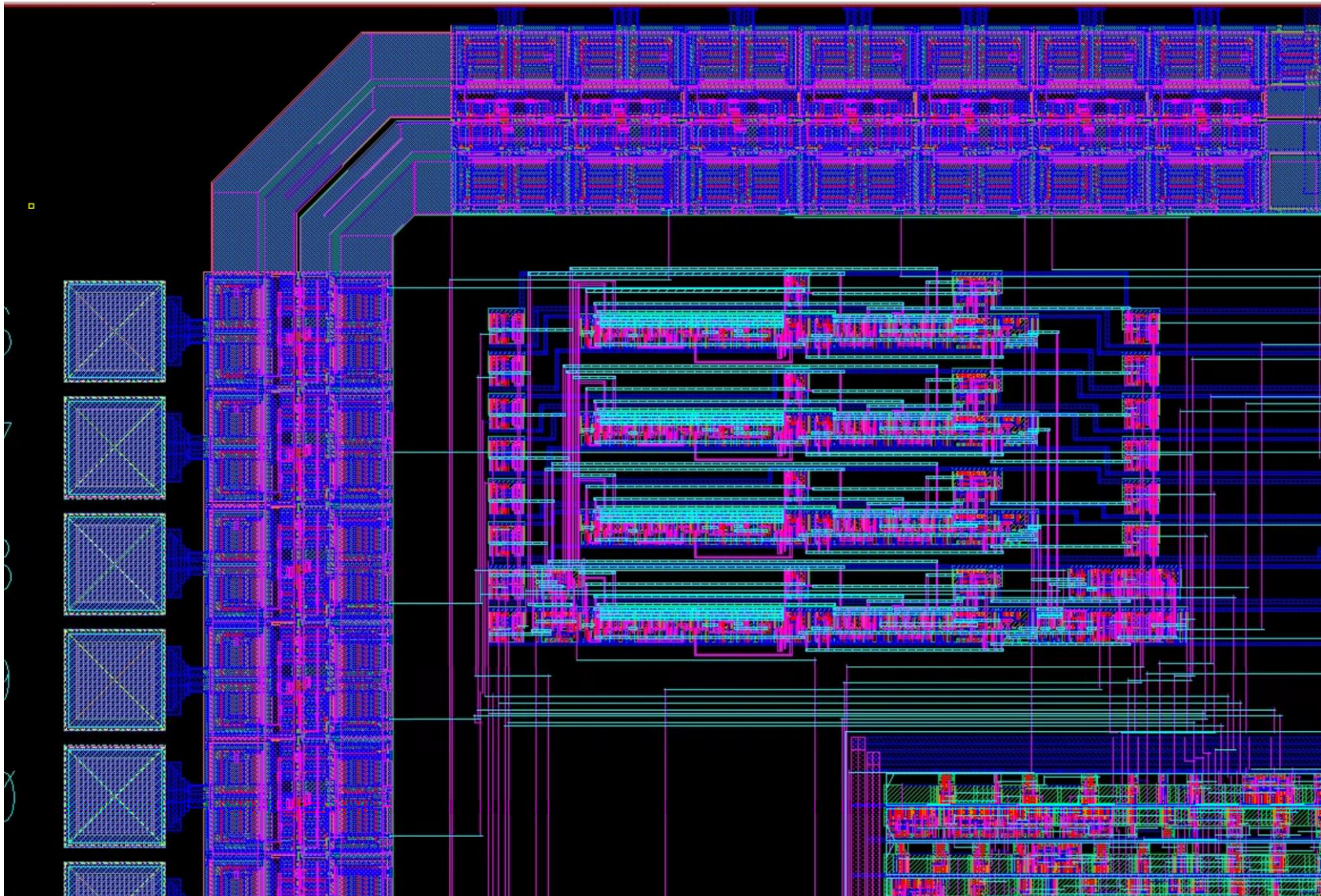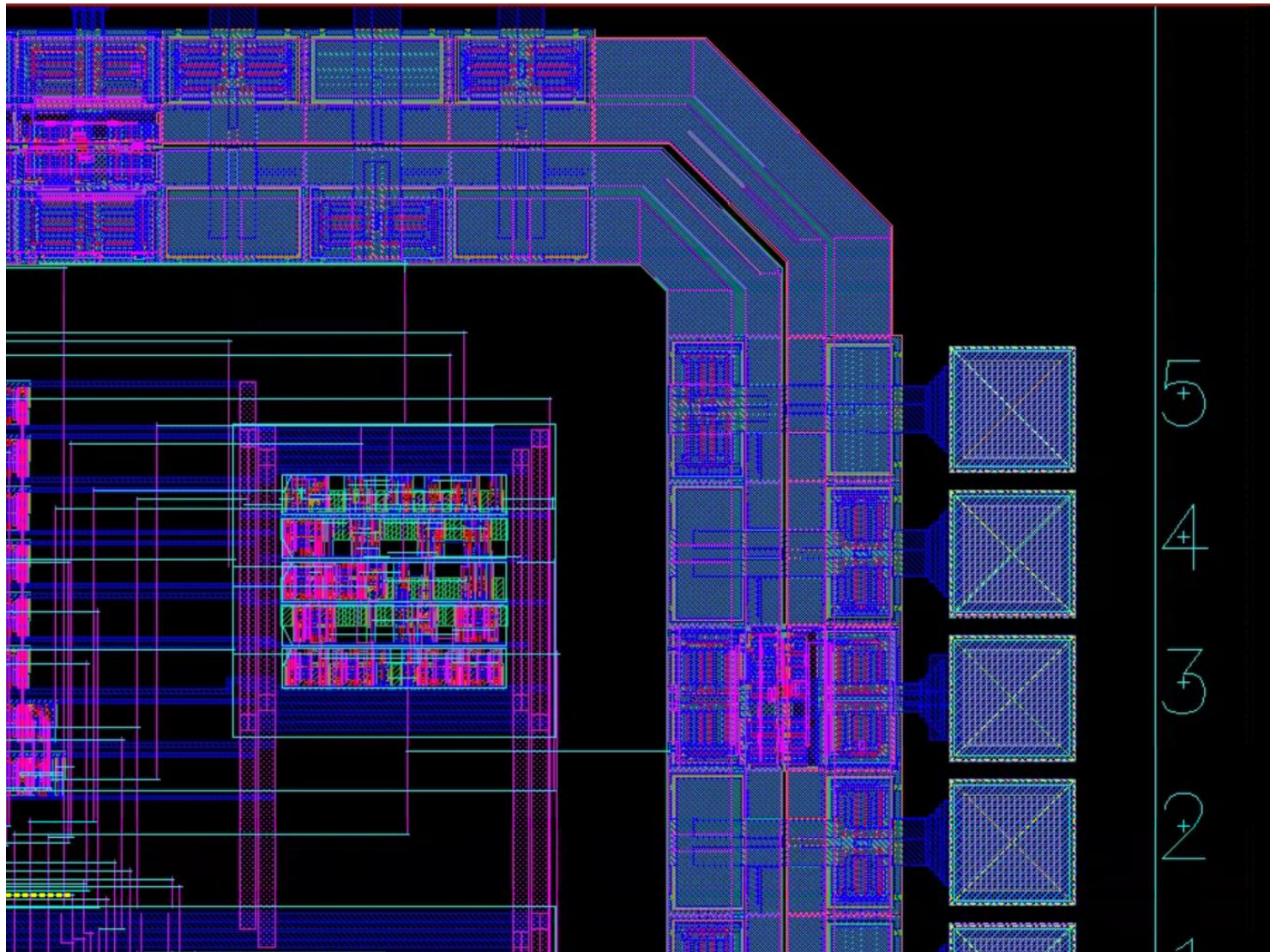**Close up of the Datapath module on the chip**

Close up of the controller module on the chip

Close up of the single_port_ram module of the chip