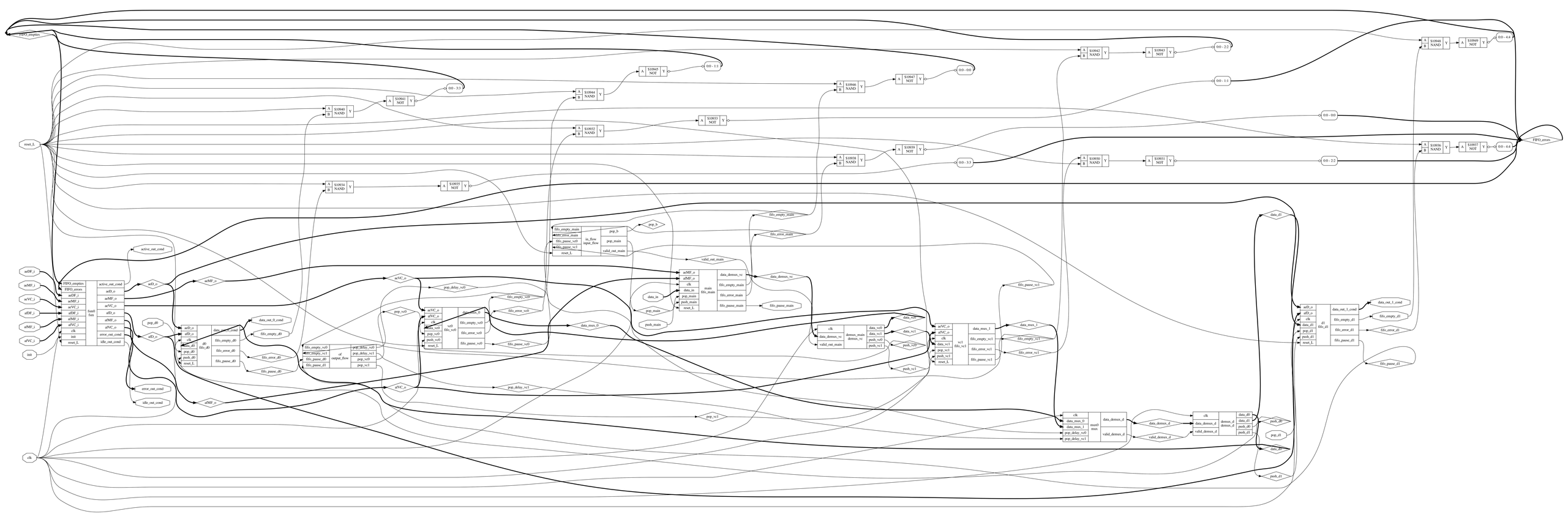
FIFO_empties
reset_L
clk
init
aeDF_i
aeMF_i
aeVC_i
afDF_i
afMF_i
afVC_i
FIFO_errors
active_out_cond
error_out_cond
idle_out_cond
pop_d0
data_out_0_cond
fifo_empty_d0
fifo_error_d0
fifo_pause_d0
pop_b
pop_main
valid_out_main
fifo_empty_main
fifo_error_main
fifo_pause_main
data_demux_vc
data_in
push_main
pop_delay_vc0
pop_delay_vc1
pop_vc1
data_mux_0
data_mux_1
data_d1
data_out_1_cond
fifo_empty_d1
fifo_error_d1
fifo_pause_d1
push_d0
pop_d1
push_d1
data_d0
data_demux_d
valid_demux_d
0.0 - 1.1
0.0 - 2.2
0.0 - 3.3
0.0 - 4.4
0.0 - 0.0
0.0 - 1.3