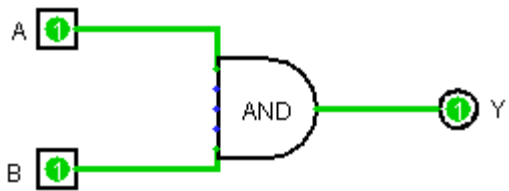


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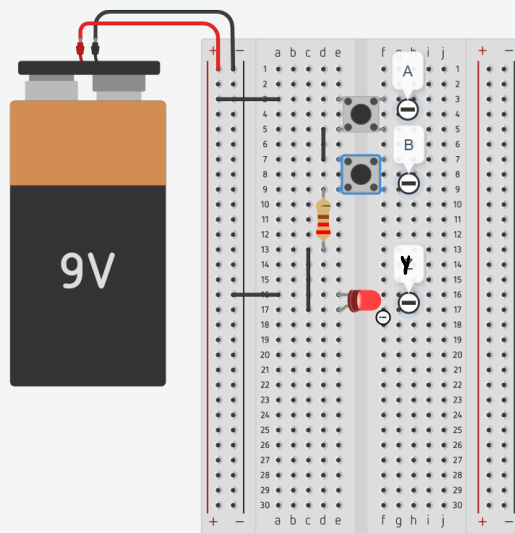
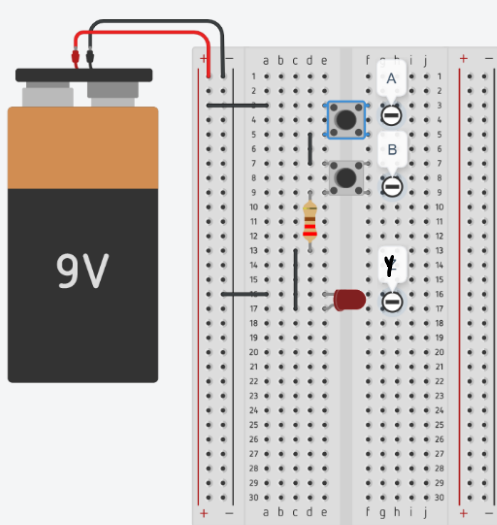
Part A: And Gate



A	B	Y
0	1	0
1	0	0
1	1	1
0	0	0

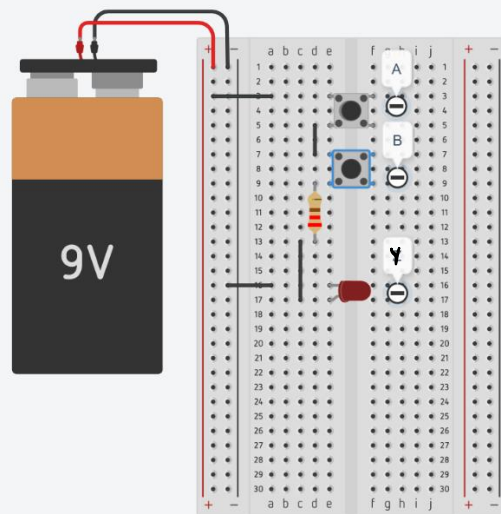
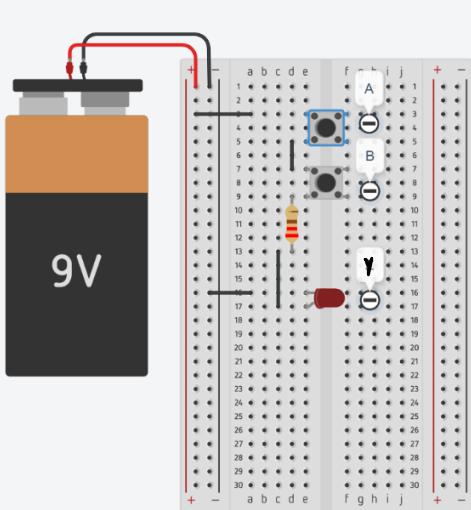
A0 B0

A1 B1



A1 B0

B1 A0

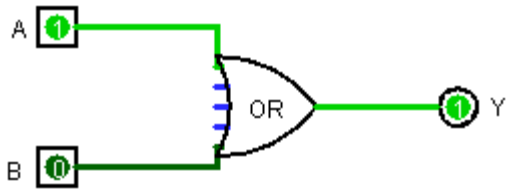


Create Digital Gates

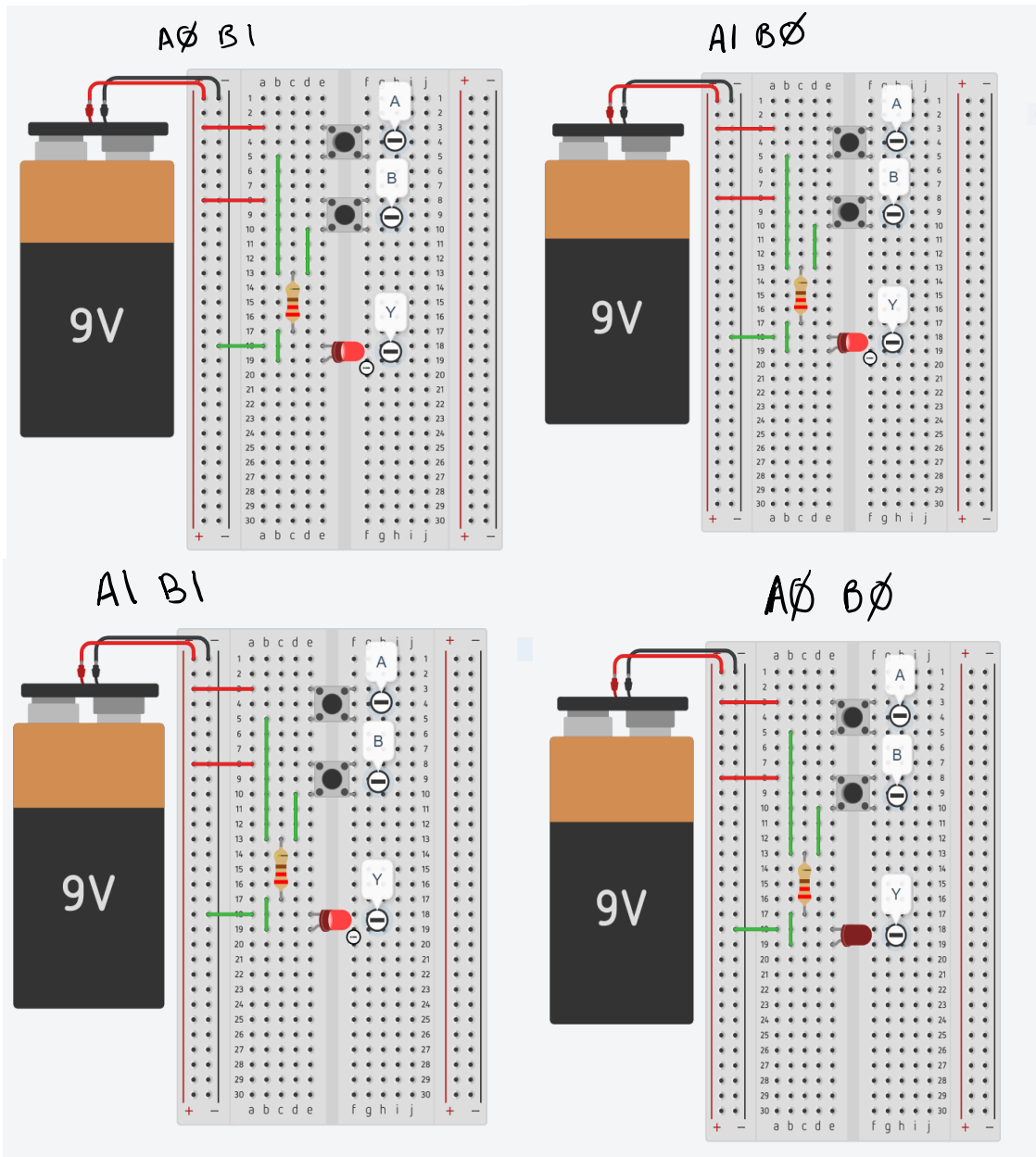
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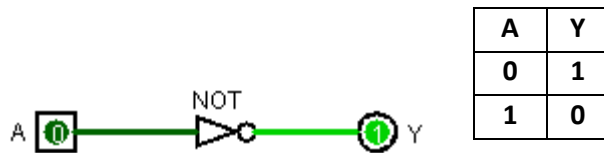
Part B: OR Gate



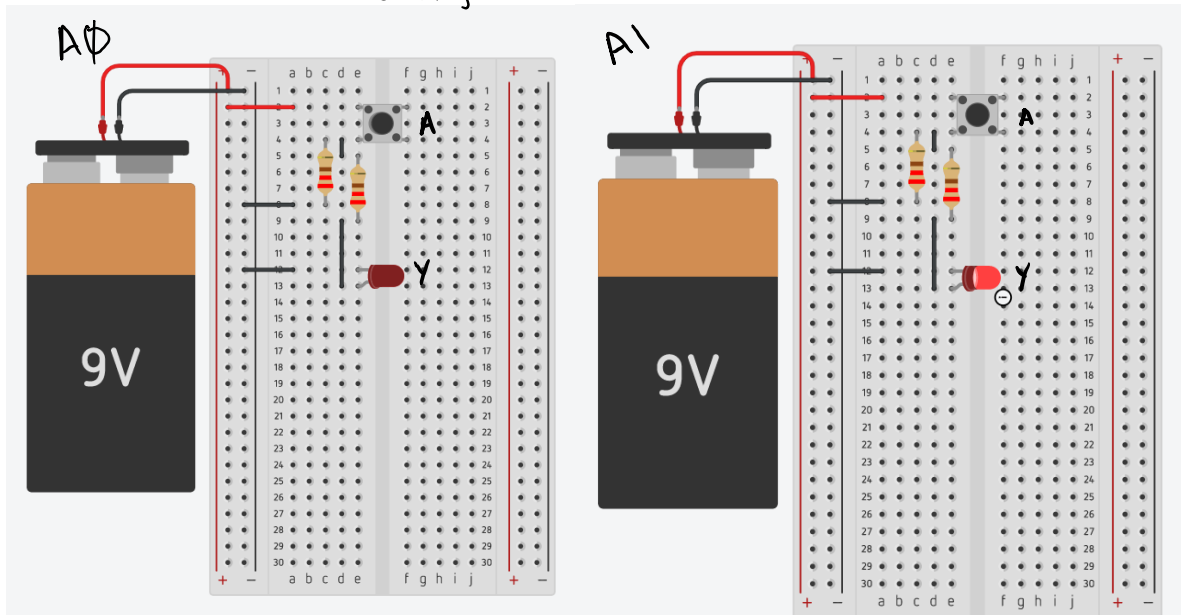
A	B	Y
1	0	1
0	1	1
1	1	1
0	0	0



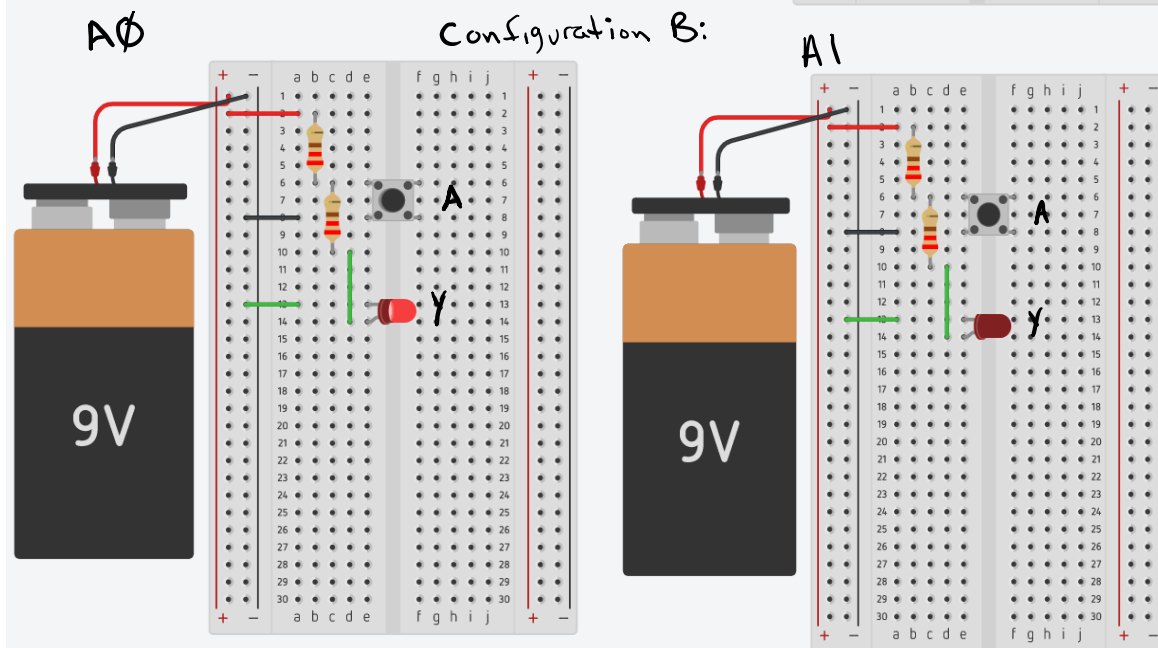
Part C: NOT Gate



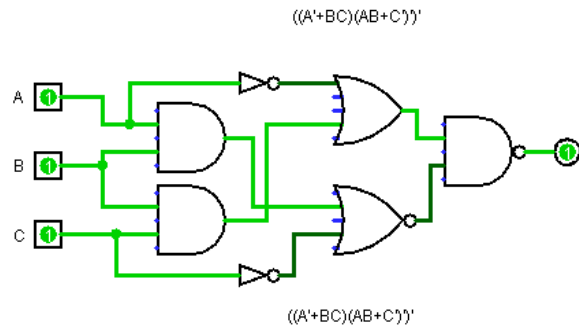
Configuration A:



Configuration B:



Part D: Construct Digital Logic Circuit from Boolean Expression $((A' + BC)(AB + C'))'$



A	B	C	Y
1	1	1	T
1	1	0	T
1	0	1	T
1	0	0	T
0	1	1	F
0	1	0	T
0	0	1	F
0	0	0	T

