Prelab Questions
1. There are 4k (4096 addresses) between 5000 and 5FFF
1. We would have to use that many more address pins into our CPLD and deal with that many more wires.
2. The complete range of memory on the XMEGA is 16 MB

Attached files
Summary.pdf
report.pdf
QAR file
Delay500.asm
Lab3Part1.asm

No problems encountered

Now I have a basic knowledge of Memory mapped IO.