

CMPE 315: Principles of VLSI Design

Lab Cover Page

Lab #: 2

Lab Title: Schematic Design and SpectreS Simulations

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TA / Grader Use Only:

Late Submission Deduction (20% per day):

Other Deductions:

Final Lab Grade:

Comments to student:

Contents

AOI Extracted View Functionality Simulation

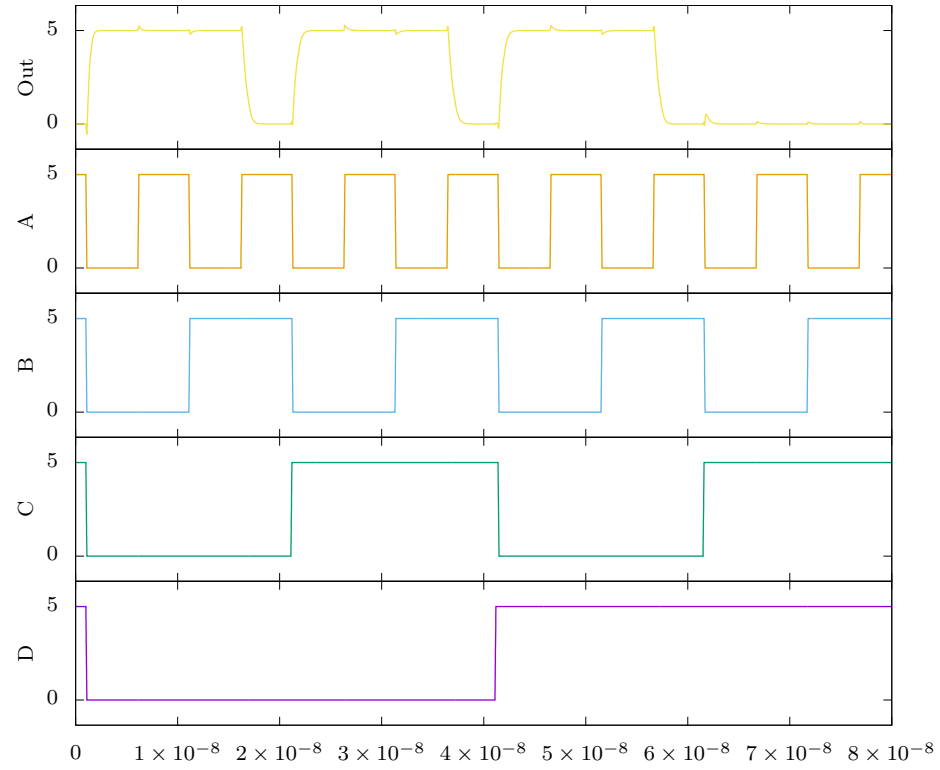


Figure 1: Waveform plots of the output of an X4 inverter driving different amounts of load inverters.

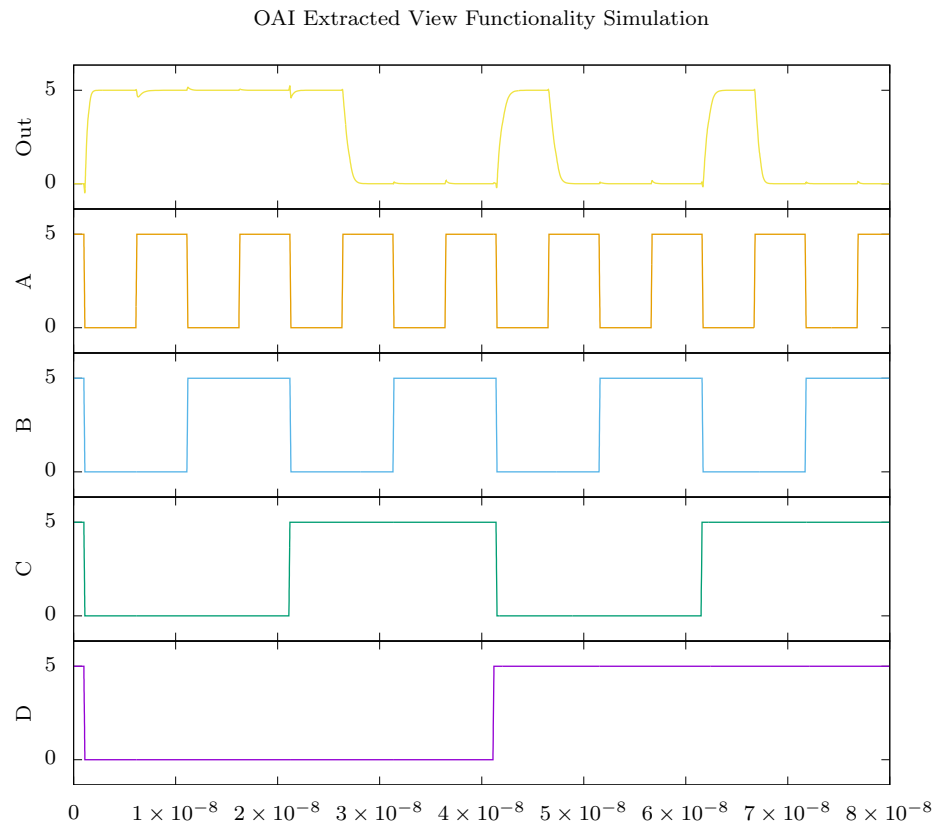


Figure 2: Waveform plots of the output of an X4 inverter driving different amounts of load inverters.

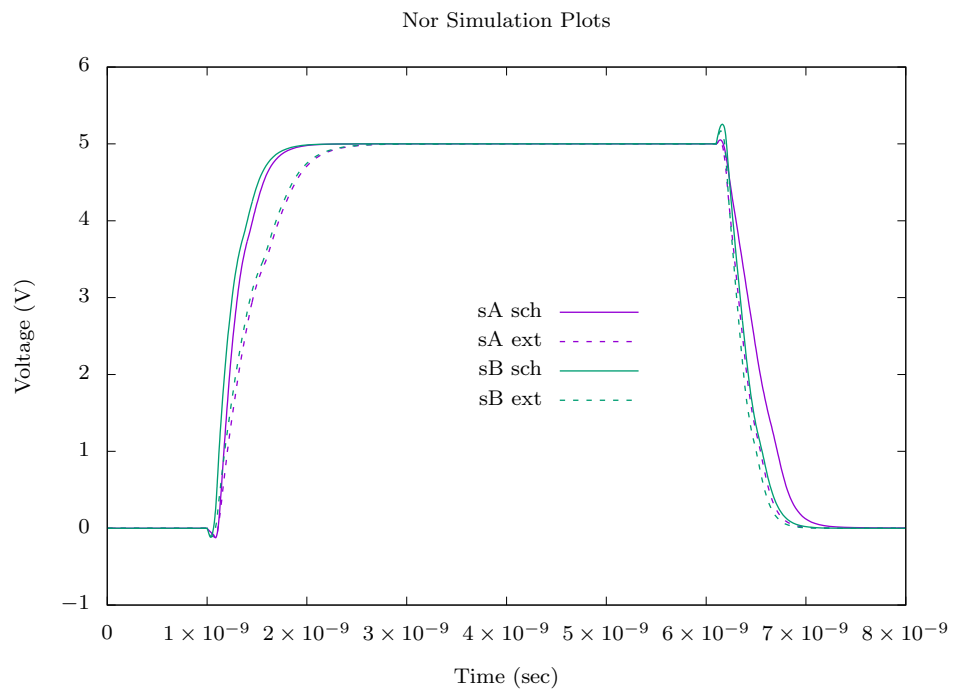


Figure 3: Waveform plots of the output of an X4 inverter driving different amounts of load inverters.

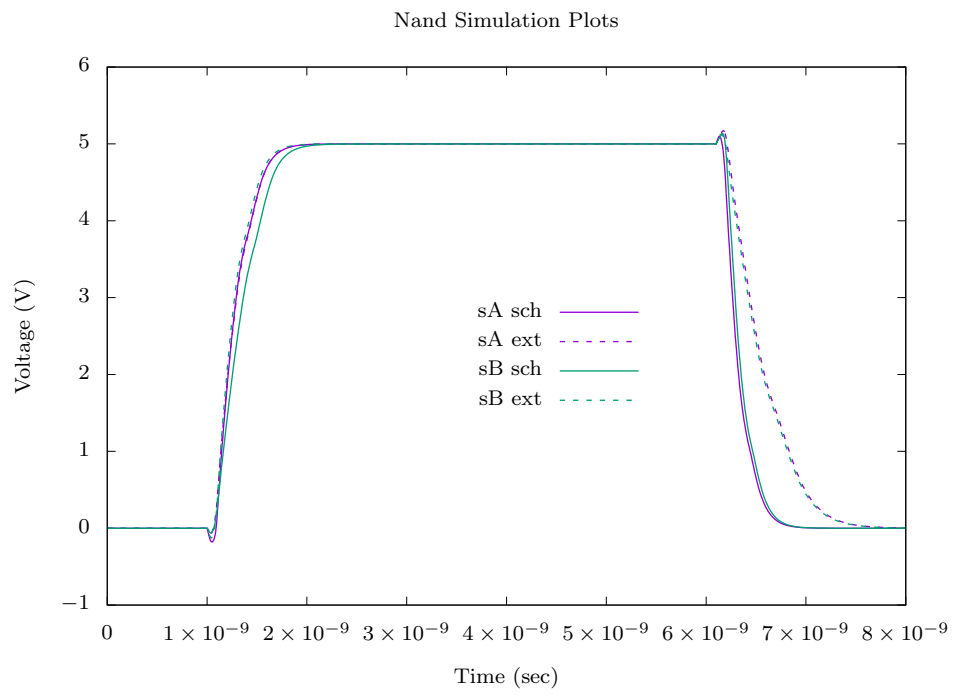


Figure 4: Waveform plots of the output of an X4 inverter driving different amounts of load inverters.

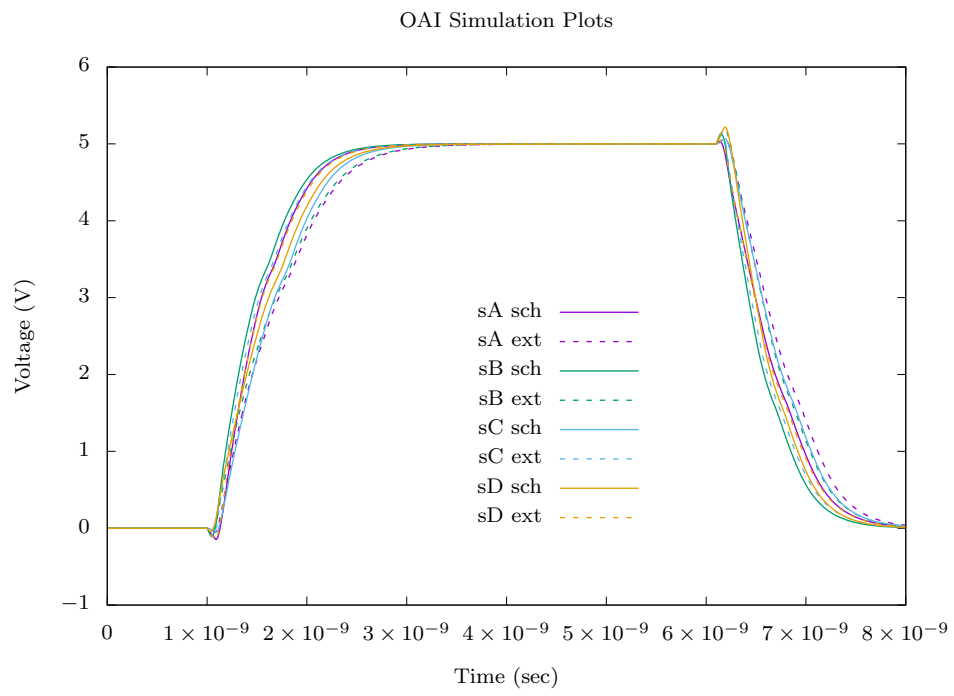


Figure 5: Waveform plots of the output of an X4 inverter driving different amounts of load inverters.

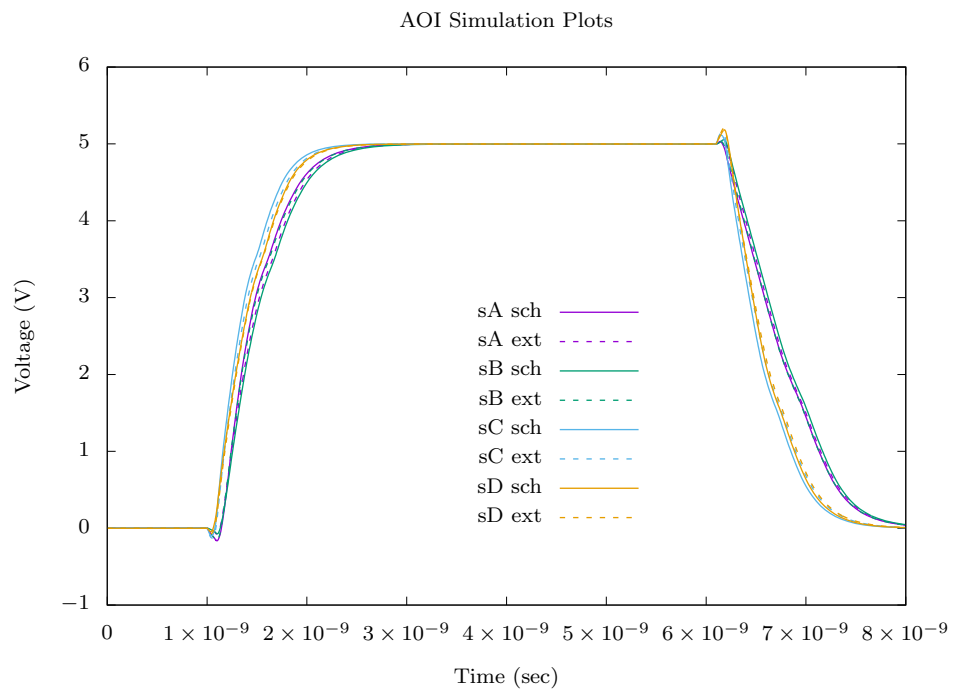


Figure 6: Waveform plots of the output of an X4 inverter driving different amounts of load inverters.

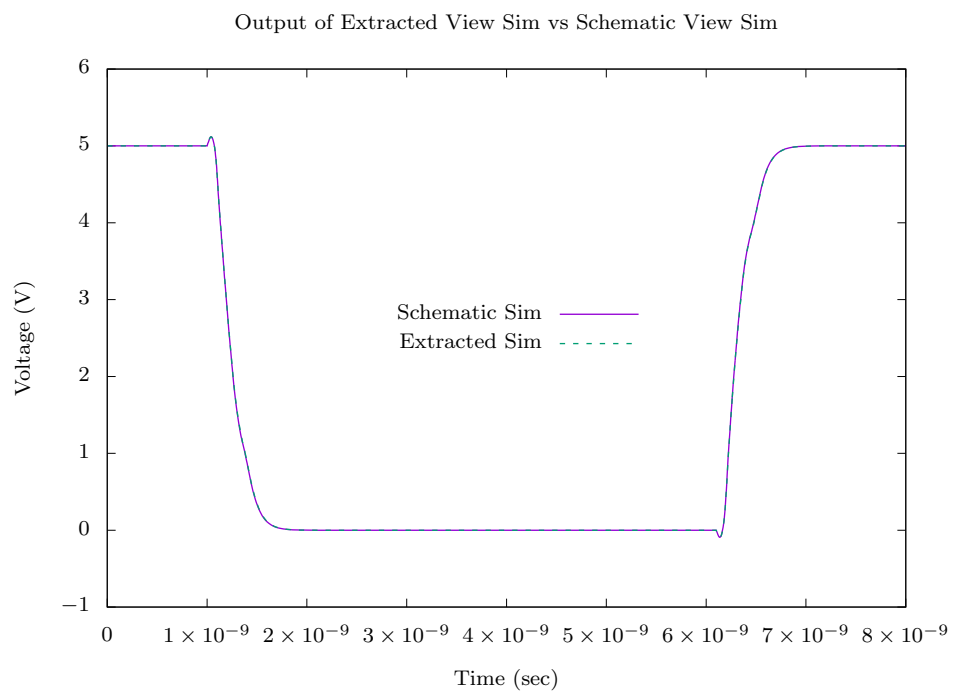


Figure 7: Waveform plots of the output of an X4 inverter driving different amounts of load inverters.