

```
1: Clock: 1 Start: 0 Cpu Read/Write: Z Reset: 1
2: CPU address: ZZZZZZZZ CPU data: ZZZZZZZZ
3: Memory data: ZZZZZZZZ
4:
5: Busy: 0 Memory Enable: 0
6: Memory Address: ZZZZZZZZ
7: -----
8: Clock: 4 Start: 1 Cpu Read/Write: 1 Reset: 0
9: CPU address: 00000000 CPU data: ZZZZZZZZ
10: Memory data: ZZZZZZZZ
11:
12: Busy: 1 Memory Enable: 0
13: Memory Address: ZZZZZZZZ
14: -----
15: Clock: 5 Start: 0 Cpu Read/Write: Z Reset: 0
16: CPU address: ZZZZZZZZ CPU data: ZZZZZZZZ
17: Memory data: ZZZZZZZZ
18:
19: Busy: 1 Memory Enable: 1
20: Memory Address: 00000000
21: -----
22: Clock: 6 Start: 0 Cpu Read/Write: Z Reset: 0
23: CPU address: ZZZZZZZZ CPU data: ZZZZZZZZ
24: Memory data: ZZZZZZZZ
25:
26: Busy: 1 Memory Enable: 0
27: Memory Address: ZZZZZZZZ
28: -----
29: Clock: 22 Start: 0 Cpu Read/Write: Z Reset: 0
30: CPU address: ZZZZZZZZ CPU data: 00000000
31: Memory data: ZZZZZZZZ
32:
33: Busy: 0 Memory Enable: 0
34: Memory Address: ZZZZZZZZ
35: -----
36: Clock: 24 Start: 1 Cpu Read/Write: 1 Reset: 0
37: CPU address: 00100101 CPU data: ZZZZZZZZ
38: Memory data: ZZZZZZZZ
39:
40: Busy: 1 Memory Enable: 0
41: Memory Address: ZZZZZZZZ
42: -----
43: Clock: 25 Start: 0 Cpu Read/Write: Z Reset: 0
44: CPU address: ZZZZZZZZ CPU data: ZZZZZZZZ
45: Memory data: ZZZZZZZZ
46:
47: Busy: 1 Memory Enable: 1
48: Memory Address: 00100101
49: -----
50: Clock: 26 Start: 0 Cpu Read/Write: Z Reset: 0
51: CPU address: ZZZZZZZZ CPU data: ZZZZZZZZ
52: Memory data: ZZZZZZZZ
53:
54: Busy: 1 Memory Enable: 0
55: Memory Address: ZZZZZZZZ
56: -----
57: Clock: 42 Start: 0 Cpu Read/Write: Z Reset: 0
58: CPU address: ZZZZZZZZ CPU data: 00010001
59: Memory data: ZZZZZZZZ
60:
61: Busy: 0 Memory Enable: 0
62: Memory Address: ZZZZZZZZ
```

```
63: -----
64: Clock: 44 Start: 1 Cpu Read/Write: 1 Reset: 0
65: CPU address: 01001010 CPU data: ZZZZZZZZ
66: Memory data: ZZZZZZZZ
67:
68: Busy: 1 Memory Enable: 0
69: Memory Address: ZZZZZZZZ
70: -----
71: Clock: 45 Start: 0 Cpu Read/Write: Z Reset: 0
72: CPU address: ZZZZZZZZ CPU data: ZZZZZZZZ
73: Memory data: ZZZZZZZZ
74:
75: Busy: 1 Memory Enable: 1
76: Memory Address: 01001010
77: -----
78: Clock: 46 Start: 0 Cpu Read/Write: Z Reset: 0
79: CPU address: ZZZZZZZZ CPU data: ZZZZZZZZ
80: Memory data: ZZZZZZZZ
81:
82: Busy: 1 Memory Enable: 0
83: Memory Address: ZZZZZZZZ
84: -----
85: Clock: 62 Start: 0 Cpu Read/Write: Z Reset: 0
86: CPU address: ZZZZZZZZ CPU data: 00100010
87: Memory data: ZZZZZZZZ
88:
89: Busy: 0 Memory Enable: 0
90: Memory Address: ZZZZZZZZ
91: -----
92: Clock: 64 Start: 1 Cpu Read/Write: 1 Reset: 0
93: CPU address: 01101111 CPU data: ZZZZZZZZ
94: Memory data: ZZZZZZZZ
95:
96: Busy: 1 Memory Enable: 0
97: Memory Address: ZZZZZZZZ
98: -----
99: Clock: 65 Start: 0 Cpu Read/Write: Z Reset: 0
100: CPU address: ZZZZZZZZ CPU data: ZZZZZZZZ
101: Memory data: ZZZZZZZZ
102:
103: Busy: 1 Memory Enable: 1
104: Memory Address: 01101111
105: -----
106: Clock: 66 Start: 0 Cpu Read/Write: Z Reset: 0
107: CPU address: ZZZZZZZZ CPU data: ZZZZZZZZ
108: Memory data: ZZZZZZZZ
109:
110: Busy: 1 Memory Enable: 0
111: Memory Address: ZZZZZZZZ
112: -----
113: Clock: 82 Start: 0 Cpu Read/Write: Z Reset: 0
114: CPU address: ZZZZZZZZ CPU data: 00110011
115: Memory data: ZZZZZZZZ
116:
117: Busy: 0 Memory Enable: 0
118: Memory Address: ZZZZZZZZ
119: -----
120: Clock: 84 Start: 1 Cpu Read/Write: 1 Reset: 0
121: CPU address: 10010000 CPU data: ZZZZZZZZ
122: Memory data: ZZZZZZZZ
123:
124: Busy: 1 Memory Enable: 0
```

test2_home.txt

```
125: Memory Address: ZZZZZZZZ
126: -----
127: Clock: 85 Start: 0 Cpu Read/Write: Z Reset: 0
128: CPU address: ZZZZZZZZ CPU data: ZZZZZZZZ
129: Memory data: ZZZZZZZZ
130:
131: Busy: 1 Memory Enable: 1
132: Memory Address: 10010000
133: -----
134: Clock: 86 Start: 0 Cpu Read/Write: Z Reset: 0
135: CPU address: ZZZZZZZZ CPU data: ZZZZZZZZ
136: Memory data: ZZZZZZZZ
137:
138: Busy: 1 Memory Enable: 0
139: Memory Address: ZZZZZZZZ
140: -----
141: Clock: 102 Start: 0 Cpu Read/Write: Z Reset: 0
142: CPU address: ZZZZZZZZ CPU data: 01000000
143: Memory data: ZZZZZZZZ
144:
145: Busy: 0 Memory Enable: 0
146: Memory Address: ZZZZZZZZ
147: -----
148: Clock: 104 Start: 1 Cpu Read/Write: 1 Reset: 0
149: CPU address: 10110101 CPU data: ZZZZZZZZ
150: Memory data: ZZZZZZZZ
151:
152: Busy: 1 Memory Enable: 0
153: Memory Address: ZZZZZZZZ
154: -----
155: Clock: 105 Start: 0 Cpu Read/Write: Z Reset: 0
156: CPU address: ZZZZZZZZ CPU data: ZZZZZZZZ
157: Memory data: ZZZZZZZZ
158:
159: Busy: 1 Memory Enable: 1
160: Memory Address: 10110101
161: -----
162: Clock: 106 Start: 0 Cpu Read/Write: Z Reset: 0
163: CPU address: ZZZZZZZZ CPU data: ZZZZZZZZ
164: Memory data: ZZZZZZZZ
165:
166: Busy: 1 Memory Enable: 0
167: Memory Address: ZZZZZZZZ
168: -----
169: Clock: 122 Start: 0 Cpu Read/Write: Z Reset: 0
170: CPU address: ZZZZZZZZ CPU data: 01010001
171: Memory data: ZZZZZZZZ
172:
173: Busy: 0 Memory Enable: 0
174: Memory Address: ZZZZZZZZ
175: -----
176: Clock: 124 Start: 1 Cpu Read/Write: 1 Reset: 0
177: CPU address: 11011010 CPU data: ZZZZZZZZ
178: Memory data: ZZZZZZZZ
179:
180: Busy: 1 Memory Enable: 0
181: Memory Address: ZZZZZZZZ
182: -----
183: Clock: 125 Start: 0 Cpu Read/Write: Z Reset: 0
184: CPU address: ZZZZZZZZ CPU data: ZZZZZZZZ
185: Memory data: ZZZZZZZZ
186:
```

```
187: Busy: 1 Memory Enable: 1
188: Memory Address: 11011010
189: -----
190: Clock: 126 Start: 0 Cpu Read/Write: Z Reset: 0
191: CPU address: ZZZZZZZZ CPU data: ZZZZZZZZ
192: Memory data: ZZZZZZZZ
193:
194: Busy: 1 Memory Enable: 0
195: Memory Address: ZZZZZZZZ
196: -----
197: Clock: 142 Start: 0 Cpu Read/Write: Z Reset: 0
198: CPU address: ZZZZZZZZ CPU data: 01100010
199: Memory data: ZZZZZZZZ
200:
201: Busy: 0 Memory Enable: 0
202: Memory Address: ZZZZZZZZ
203: -----
204: Clock: 144 Start: 1 Cpu Read/Write: 1 Reset: 0
205: CPU address: 11111111 CPU data: ZZZZZZZZ
206: Memory data: ZZZZZZZZ
207:
208: Busy: 1 Memory Enable: 0
209: Memory Address: ZZZZZZZZ
210: -----
211: Clock: 145 Start: 0 Cpu Read/Write: Z Reset: 0
212: CPU address: ZZZZZZZZ CPU data: ZZZZZZZZ
213: Memory data: ZZZZZZZZ
214:
215: Busy: 1 Memory Enable: 1
216: Memory Address: 11111111
217: -----
218: Clock: 146 Start: 0 Cpu Read/Write: Z Reset: 0
219: CPU address: ZZZZZZZZ CPU data: ZZZZZZZZ
220: Memory data: ZZZZZZZZ
221:
222: Busy: 1 Memory Enable: 0
223: Memory Address: ZZZZZZZZ
224: -----
225: Clock: 162 Start: 0 Cpu Read/Write: Z Reset: 0
226: CPU address: ZZZZZZZZ CPU data: 01110011
227: Memory data: ZZZZZZZZ
228:
229: Busy: 0 Memory Enable: 0
230: Memory Address: ZZZZZZZZ
231: -----
232: Clock: 164 Start: 1 Cpu Read/Write: 0 Reset: 0
233: CPU address: 00000000 CPU data: 00001100
234: Memory data: ZZZZZZZZ
235:
236: Busy: 1 Memory Enable: 0
237: Memory Address: ZZZZZZZZ
238: -----
239: Clock: 166 Start: 0 Cpu Read/Write: Z Reset: 0
240: CPU address: ZZZZZZZZ CPU data: ZZZZZZZZ
241: Memory data: ZZZZZZZZ
242:
243: Busy: 0 Memory Enable: 0
244: Memory Address: ZZZZZZZZ
245: -----
246: Clock: 168 Start: 1 Cpu Read/Write: 0 Reset: 0
247: CPU address: 00100101 CPU data: 00011100
248: Memory data: ZZZZZZZZ
```

```
249:
250: Busy: 1 Memory Enable: 0
251: Memory Address: ZZZZZZZZ
252: -----
253: Clock: 170 Start: 0 Cpu Read/Write: Z Reset: 0
254: CPU address: ZZZZZZZZ CPU data: ZZZZZZZZ
255: Memory data: ZZZZZZZZ
256:
257: Busy: 0 Memory Enable: 0
258: Memory Address: ZZZZZZZZ
259: -----
260: Clock: 172 Start: 1 Cpu Read/Write: 0 Reset: 0
261: CPU address: 01001010 CPU data: 00101100
262: Memory data: ZZZZZZZZ
263:
264: Busy: 1 Memory Enable: 0
265: Memory Address: ZZZZZZZZ
266: -----
267: Clock: 174 Start: 0 Cpu Read/Write: Z Reset: 0
268: CPU address: ZZZZZZZZ CPU data: ZZZZZZZZ
269: Memory data: ZZZZZZZZ
270:
271: Busy: 0 Memory Enable: 0
272: Memory Address: ZZZZZZZZ
273: -----
274: Clock: 176 Start: 1 Cpu Read/Write: 0 Reset: 0
275: CPU address: 01101111 CPU data: 00111100
276: Memory data: ZZZZZZZZ
277:
278: Busy: 1 Memory Enable: 0
279: Memory Address: ZZZZZZZZ
280: -----
281: Clock: 178 Start: 0 Cpu Read/Write: Z Reset: 0
282: CPU address: ZZZZZZZZ CPU data: ZZZZZZZZ
283: Memory data: ZZZZZZZZ
284:
285: Busy: 0 Memory Enable: 0
286: Memory Address: ZZZZZZZZ
287: -----
288: Clock: 180 Start: 1 Cpu Read/Write: 0 Reset: 0
289: CPU address: 10010000 CPU data: 01001111
290: Memory data: ZZZZZZZZ
291:
292: Busy: 1 Memory Enable: 0
293: Memory Address: ZZZZZZZZ
294: -----
295: Clock: 182 Start: 0 Cpu Read/Write: Z Reset: 0
296: CPU address: ZZZZZZZZ CPU data: ZZZZZZZZ
297: Memory data: ZZZZZZZZ
298:
299: Busy: 0 Memory Enable: 0
300: Memory Address: ZZZZZZZZ
301: -----
302: Clock: 184 Start: 1 Cpu Read/Write: 0 Reset: 0
303: CPU address: 10110101 CPU data: 01011111
304: Memory data: ZZZZZZZZ
305:
306: Busy: 1 Memory Enable: 0
307: Memory Address: ZZZZZZZZ
308: -----
309: Clock: 186 Start: 0 Cpu Read/Write: Z Reset: 0
310: CPU address: ZZZZZZZZ CPU data: ZZZZZZZZ
```

```
311: Memory data: ZZZZZZZZ
312:
313: Busy: 0 Memory Enable: 0
314: Memory Address: ZZZZZZZZ
315: -----
316: Clock: 188 Start: 1 Cpu Read/Write: 0 Reset: 0
317: CPU address: 11011010 CPU data: 01101111
318: Memory data: ZZZZZZZZ
319:
320: Busy: 1 Memory Enable: 0
321: Memory Address: ZZZZZZZZ
322: -----
323: Clock: 190 Start: 0 Cpu Read/Write: Z Reset: 0
324: CPU address: ZZZZZZZZ CPU data: ZZZZZZZZ
325: Memory data: ZZZZZZZZ
326:
327: Busy: 0 Memory Enable: 0
328: Memory Address: ZZZZZZZZ
329: -----
330: Clock: 192 Start: 1 Cpu Read/Write: 0 Reset: 0
331: CPU address: 11111111 CPU data: 01111111
332: Memory data: ZZZZZZZZ
333:
334: Busy: 1 Memory Enable: 0
335: Memory Address: ZZZZZZZZ
336: -----
337: Clock: 194 Start: 0 Cpu Read/Write: Z Reset: 0
338: CPU address: ZZZZZZZZ CPU data: ZZZZZZZZ
339: Memory data: ZZZZZZZZ
340:
341: Busy: 0 Memory Enable: 0
342: Memory Address: ZZZZZZZZ
343: -----
344: Clock: 196 Start: 1 Cpu Read/Write: 1 Reset: 0
345: CPU address: 00000000 CPU data: ZZZZZZZZ
346: Memory data: ZZZZZZZZ
347:
348: Busy: 1 Memory Enable: 0
349: Memory Address: ZZZZZZZZ
350: -----
351: Clock: 197 Start: 0 Cpu Read/Write: Z Reset: 0
352: CPU address: ZZZZZZZZ CPU data: 00001100
353: Memory data: ZZZZZZZZ
354:
355: Busy: 0 Memory Enable: 0
356: Memory Address: ZZZZZZZZ
357: -----
358: Clock: 199 Start: 1 Cpu Read/Write: 1 Reset: 0
359: CPU address: 00100101 CPU data: ZZZZZZZZ
360: Memory data: ZZZZZZZZ
361:
362: Busy: 1 Memory Enable: 0
363: Memory Address: ZZZZZZZZ
364: -----
365: Clock: 200 Start: 0 Cpu Read/Write: Z Reset: 0
366: CPU address: ZZZZZZZZ CPU data: 00011100
367: Memory data: ZZZZZZZZ
368:
369: Busy: 0 Memory Enable: 0
370: Memory Address: ZZZZZZZZ
371: -----
372: Clock: 202 Start: 1 Cpu Read/Write: 1 Reset: 0
```

```
373: CPU address: 01001010 CPU data: ZZZZZZZZ
374: Memory data: ZZZZZZZZ
375:
376: Busy: 1 Memory Enable: 0
377: Memory Address: ZZZZZZZZ
378: -----
379: Clock: 203 Start: 0 Cpu Read/Write: Z Reset: 0
380: CPU address: ZZZZZZZZ CPU data: 00101100
381: Memory data: ZZZZZZZZ
382:
383: Busy: 0 Memory Enable: 0
384: Memory Address: ZZZZZZZZ
385: -----
386: Clock: 205 Start: 1 Cpu Read/Write: 1 Reset: 0
387: CPU address: 01101111 CPU data: ZZZZZZZZ
388: Memory data: ZZZZZZZZ
389:
390: Busy: 1 Memory Enable: 0
391: Memory Address: ZZZZZZZZ
392: -----
393: Clock: 206 Start: 0 Cpu Read/Write: Z Reset: 0
394: CPU address: ZZZZZZZZ CPU data: 00111100
395: Memory data: ZZZZZZZZ
396:
397: Busy: 0 Memory Enable: 0
398: Memory Address: ZZZZZZZZ
399: -----
400: Clock: 208 Start: 1 Cpu Read/Write: 1 Reset: 0
401: CPU address: 10010000 CPU data: ZZZZZZZZ
402: Memory data: ZZZZZZZZ
403:
404: Busy: 1 Memory Enable: 0
405: Memory Address: ZZZZZZZZ
406: -----
407: Clock: 209 Start: 0 Cpu Read/Write: Z Reset: 0
408: CPU address: ZZZZZZZZ CPU data: 01001111
409: Memory data: ZZZZZZZZ
410:
411: Busy: 0 Memory Enable: 0
412: Memory Address: ZZZZZZZZ
413: -----
414: Clock: 211 Start: 1 Cpu Read/Write: 1 Reset: 0
415: CPU address: 10110101 CPU data: ZZZZZZZZ
416: Memory data: ZZZZZZZZ
417:
418: Busy: 1 Memory Enable: 0
419: Memory Address: ZZZZZZZZ
420: -----
421: Clock: 212 Start: 0 Cpu Read/Write: Z Reset: 0
422: CPU address: ZZZZZZZZ CPU data: 01011111
423: Memory data: ZZZZZZZZ
424:
425: Busy: 0 Memory Enable: 0
426: Memory Address: ZZZZZZZZ
427: -----
428: Clock: 214 Start: 1 Cpu Read/Write: 1 Reset: 0
429: CPU address: 11011010 CPU data: ZZZZZZZZ
430: Memory data: ZZZZZZZZ
431:
432: Busy: 1 Memory Enable: 0
433: Memory Address: ZZZZZZZZ
434: -----
```

```
435: Clock: 215 Start: 0 Cpu Read/Write: Z Reset: 0
436: CPU address: ZZZZZZZZ CPU data: 01101111
437: Memory data: ZZZZZZZZ
438:
439: Busy: 0 Memory Enable: 0
440: Memory Address: ZZZZZZZZ
441: -----
442: Clock: 217 Start: 1 Cpu Read/Write: 1 Reset: 0
443: CPU address: 11111111 CPU data: ZZZZZZZZ
444: Memory data: ZZZZZZZZ
445:
446: Busy: 1 Memory Enable: 0
447: Memory Address: ZZZZZZZZ
448: -----
449: Clock: 218 Start: 0 Cpu Read/Write: Z Reset: 0
450: CPU address: ZZZZZZZZ CPU data: 01111111
451: Memory data: ZZZZZZZZ
452:
453: Busy: 0 Memory Enable: 0
454: Memory Address: ZZZZZZZZ
455: -----
456: Clock: 220 Start: 1 Cpu Read/Write: 0 Reset: 0
457: CPU address: 11100000 CPU data: 00000000
458: Memory data: ZZZZZZZZ
459:
460: Busy: 1 Memory Enable: 0
461: Memory Address: ZZZZZZZZ
462: -----
463: Clock: 222 Start: 0 Cpu Read/Write: Z Reset: 0
464: CPU address: ZZZZZZZZ CPU data: ZZZZZZZZ
465: Memory data: ZZZZZZZZ
466:
467: Busy: 0 Memory Enable: 0
468: Memory Address: ZZZZZZZZ
469: -----
470: Clock: 224 Start: 1 Cpu Read/Write: 0 Reset: 0
471: CPU address: 00000101 CPU data: 00000000
472: Memory data: ZZZZZZZZ
473:
474: Busy: 1 Memory Enable: 0
475: Memory Address: ZZZZZZZZ
476: -----
477: Clock: 226 Start: 0 Cpu Read/Write: Z Reset: 0
478: CPU address: ZZZZZZZZ CPU data: ZZZZZZZZ
479: Memory data: ZZZZZZZZ
480:
481: Busy: 0 Memory Enable: 0
482: Memory Address: ZZZZZZZZ
483: -----
484: Clock: 228 Start: 1 Cpu Read/Write: 0 Reset: 0
485: CPU address: 00001010 CPU data: 00000000
486: Memory data: ZZZZZZZZ
487:
488: Busy: 1 Memory Enable: 0
489: Memory Address: ZZZZZZZZ
490: -----
491: Clock: 230 Start: 0 Cpu Read/Write: Z Reset: 0
492: CPU address: ZZZZZZZZ CPU data: ZZZZZZZZ
493: Memory data: ZZZZZZZZ
494:
495: Busy: 0 Memory Enable: 0
496: Memory Address: ZZZZZZZZ
```

```
497: -----
498: Clock: 232 Start: 1 Cpu Read/Write: 0 Reset: 0
499: CPU address: 00001111 CPU data: 00000000
500: Memory data: ZZZZZZZZ
501:
502: Busy: 1 Memory Enable: 0
503: Memory Address: ZZZZZZZZ
504: -----
505: Clock: 234 Start: 0 Cpu Read/Write: Z Reset: 0
506: CPU address: ZZZZZZZZ CPU data: ZZZZZZZZ
507: Memory data: ZZZZZZZZ
508:
509: Busy: 0 Memory Enable: 0
510: Memory Address: ZZZZZZZZ
511: -----
512: Clock: 236 Start: 1 Cpu Read/Write: 0 Reset: 0
513: CPU address: 00010000 CPU data: 00000000
514: Memory data: ZZZZZZZZ
515:
516: Busy: 1 Memory Enable: 0
517: Memory Address: ZZZZZZZZ
518: -----
519: Clock: 238 Start: 0 Cpu Read/Write: Z Reset: 0
520: CPU address: ZZZZZZZZ CPU data: ZZZZZZZZ
521: Memory data: ZZZZZZZZ
522:
523: Busy: 0 Memory Enable: 0
524: Memory Address: ZZZZZZZZ
525: -----
526: Clock: 240 Start: 1 Cpu Read/Write: 0 Reset: 0
527: CPU address: 00010101 CPU data: 00000000
528: Memory data: ZZZZZZZZ
529:
530: Busy: 1 Memory Enable: 0
531: Memory Address: ZZZZZZZZ
532: -----
533: Clock: 242 Start: 0 Cpu Read/Write: Z Reset: 0
534: CPU address: ZZZZZZZZ CPU data: ZZZZZZZZ
535: Memory data: ZZZZZZZZ
536:
537: Busy: 0 Memory Enable: 0
538: Memory Address: ZZZZZZZZ
539: -----
540: Clock: 244 Start: 1 Cpu Read/Write: 0 Reset: 0
541: CPU address: 00011010 CPU data: 00000000
542: Memory data: ZZZZZZZZ
543:
544: Busy: 1 Memory Enable: 0
545: Memory Address: ZZZZZZZZ
546: -----
547: Clock: 246 Start: 0 Cpu Read/Write: Z Reset: 0
548: CPU address: ZZZZZZZZ CPU data: ZZZZZZZZ
549: Memory data: ZZZZZZZZ
550:
551: Busy: 0 Memory Enable: 0
552: Memory Address: ZZZZZZZZ
553: -----
554: Clock: 248 Start: 1 Cpu Read/Write: 0 Reset: 0
555: CPU address: 00011111 CPU data: 00000000
556: Memory data: ZZZZZZZZ
557:
558: Busy: 1 Memory Enable: 0
```

```
559: Memory Address: ZZZZZZZZ
560: -----
561: Clock: 250 Start: 0 Cpu Read/Write: Z Reset: 0
562: CPU address: ZZZZZZZZ CPU data: ZZZZZZZZ
563: Memory data: ZZZZZZZZ
564:
565: Busy: 0 Memory Enable: 0
566: Memory Address: ZZZZZZZZ
567: -----
568: Clock: 252 Start: 1 Cpu Read/Write: 1 Reset: 0
569: CPU address: 00000000 CPU data: ZZZZZZZZ
570: Memory data: ZZZZZZZZ
571:
572: Busy: 1 Memory Enable: 0
573: Memory Address: ZZZZZZZZ
574: -----
575: Clock: 253 Start: 0 Cpu Read/Write: Z Reset: 0
576: CPU address: ZZZZZZZZ CPU data: 00000000
577: Memory data: ZZZZZZZZ
578:
579: Busy: 0 Memory Enable: 0
580: Memory Address: ZZZZZZZZ
581: -----
582: Clock: 255 Start: 1 Cpu Read/Write: 1 Reset: 0
583: CPU address: 00100101 CPU data: ZZZZZZZZ
584: Memory data: ZZZZZZZZ
585:
586: Busy: 1 Memory Enable: 0
587: Memory Address: ZZZZZZZZ
588: -----
589: Clock: 256 Start: 0 Cpu Read/Write: Z Reset: 0
590: CPU address: ZZZZZZZZ CPU data: 00011100
591: Memory data: ZZZZZZZZ
592:
593: Busy: 0 Memory Enable: 0
594: Memory Address: ZZZZZZZZ
595: -----
596: Clock: 258 Start: 1 Cpu Read/Write: 1 Reset: 0
597: CPU address: 01001010 CPU data: ZZZZZZZZ
598: Memory data: ZZZZZZZZ
599:
600: Busy: 1 Memory Enable: 0
601: Memory Address: ZZZZZZZZ
602: -----
603: Clock: 259 Start: 0 Cpu Read/Write: Z Reset: 0
604: CPU address: ZZZZZZZZ CPU data: 00101100
605: Memory data: ZZZZZZZZ
606:
607: Busy: 0 Memory Enable: 0
608: Memory Address: ZZZZZZZZ
609: -----
610: Clock: 261 Start: 1 Cpu Read/Write: 1 Reset: 0
611: CPU address: 01101111 CPU data: ZZZZZZZZ
612: Memory data: ZZZZZZZZ
613:
614: Busy: 1 Memory Enable: 0
615: Memory Address: ZZZZZZZZ
616: -----
617: Clock: 262 Start: 0 Cpu Read/Write: Z Reset: 0
618: CPU address: ZZZZZZZZ CPU data: 00111100
619: Memory data: ZZZZZZZZ
620:
```

```
621: Busy: 0 Memory Enable: 0
622: Memory Address: ZZZZZZZZ
623: -----
624: Clock: 264 Start: 1 Cpu Read/Write: 1 Reset: 0
625: CPU address: 10010000 CPU data: ZZZZZZZZ
626: Memory data: ZZZZZZZZ
627:
628: Busy: 1 Memory Enable: 0
629: Memory Address: ZZZZZZZZ
630: -----
631: Clock: 265 Start: 0 Cpu Read/Write: Z Reset: 0
632: CPU address: ZZZZZZZZ CPU data: 01001111
633: Memory data: ZZZZZZZZ
634:
635: Busy: 0 Memory Enable: 0
636: Memory Address: ZZZZZZZZ
637: -----
638: Clock: 267 Start: 1 Cpu Read/Write: 1 Reset: 0
639: CPU address: 10110101 CPU data: ZZZZZZZZ
640: Memory data: ZZZZZZZZ
641:
642: Busy: 1 Memory Enable: 0
643: Memory Address: ZZZZZZZZ
644: -----
645: Clock: 268 Start: 0 Cpu Read/Write: Z Reset: 0
646: CPU address: ZZZZZZZZ CPU data: 01011111
647: Memory data: ZZZZZZZZ
648:
649: Busy: 0 Memory Enable: 0
650: Memory Address: ZZZZZZZZ
651: -----
652: Clock: 270 Start: 1 Cpu Read/Write: 1 Reset: 0
653: CPU address: 11011010 CPU data: ZZZZZZZZ
654: Memory data: ZZZZZZZZ
655:
656: Busy: 1 Memory Enable: 0
657: Memory Address: ZZZZZZZZ
658: -----
659: Clock: 271 Start: 0 Cpu Read/Write: Z Reset: 0
660: CPU address: ZZZZZZZZ CPU data: 01101111
661: Memory data: ZZZZZZZZ
662:
663: Busy: 0 Memory Enable: 0
664: Memory Address: ZZZZZZZZ
665: -----
666: Clock: 273 Start: 1 Cpu Read/Write: 1 Reset: 0
667: CPU address: 11111111 CPU data: ZZZZZZZZ
668: Memory data: ZZZZZZZZ
669:
670: Busy: 1 Memory Enable: 0
671: Memory Address: ZZZZZZZZ
672: -----
673: Clock: 274 Start: 0 Cpu Read/Write: Z Reset: 0
674: CPU address: ZZZZZZZZ CPU data: 01111111
675: Memory data: ZZZZZZZZ
676:
677: Busy: 0 Memory Enable: 0
678: Memory Address: ZZZZZZZZ
679: -----
680:
```