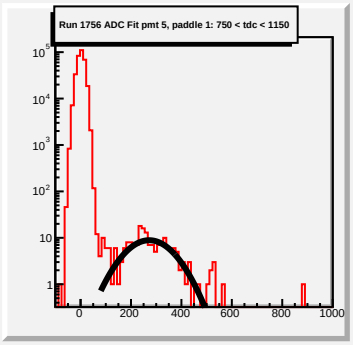
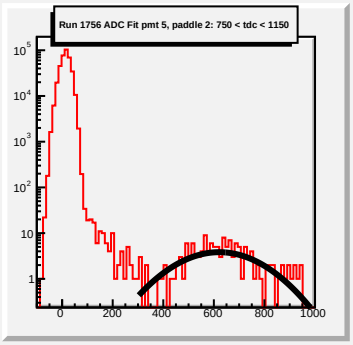
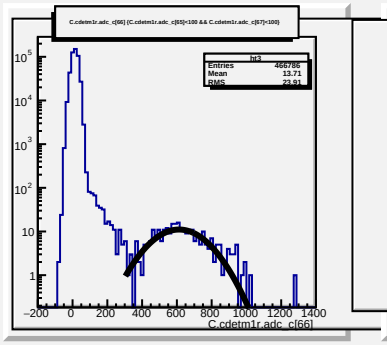
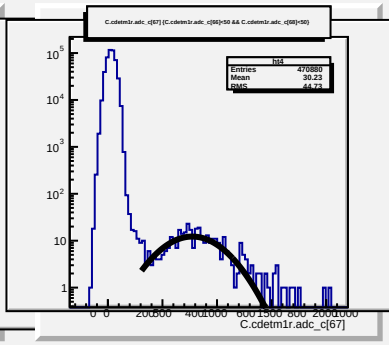
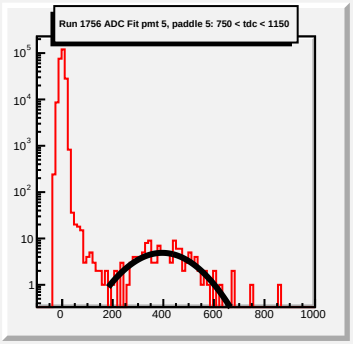
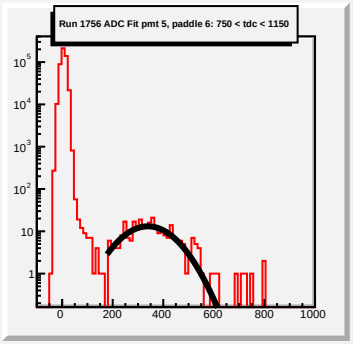

Run 1756 ADC Fit pmt 5, paddle 1: 750 < tdc < 1150

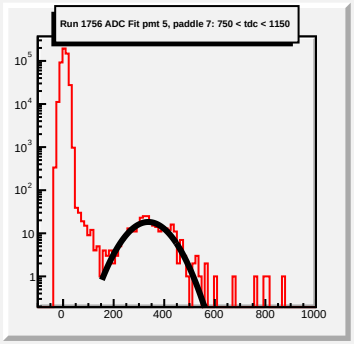

Run 1756 ADC Fit pmt 5, paddle 2: 750 < tdc < 1150

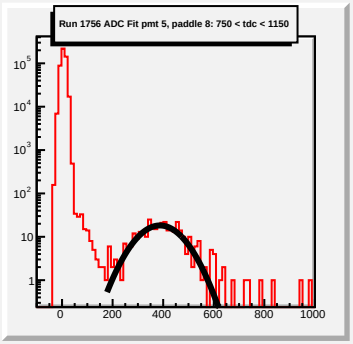

C.cdetm1r.adc_c[66] (C.cdetm1r.adc_c[65]<100 && C.cdetm1r.adc_c[67]<100)
ht3
Entries 466786
Mean 13.71
RMS 23.91
C.cdetm1r.adc_c[66]

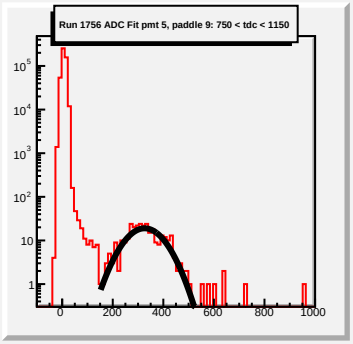

C.cdetm1r.adc_c[67] (C.cdetm1r.adc_c[66]>50 && C.cdetm1r.adc_c[68]>50)
ht4
Entries 470880
Mean 30.23
RMS 44.73
C.cdetm1r.adc_c[67]

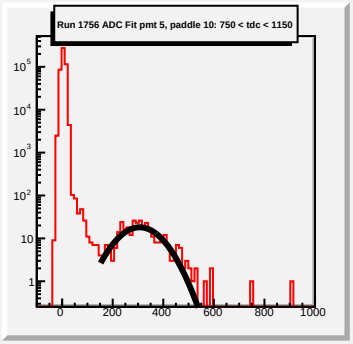

Run 1756 ADC Fit pmt 5, paddle 5: 750 < tdc < 1150

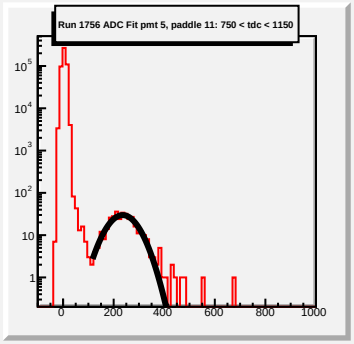

Run 1756 ADC Fit pmt 5, paddle 6: 750 < tdc < 1150

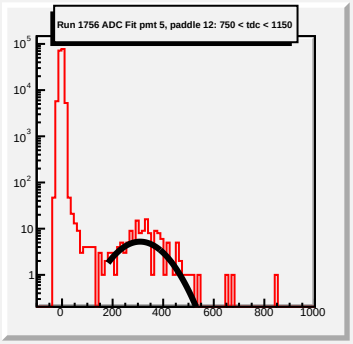

Run 1756 ADC Fit pmt 5, paddle 7: 750 < tdc < 1150

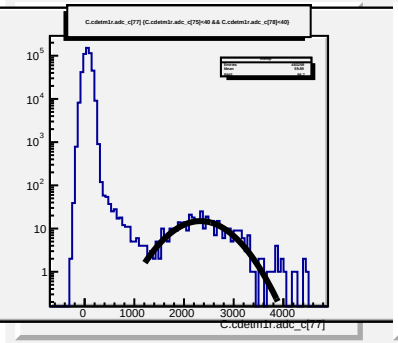

Run 1756 ADC Fit pmt 5, paddle 8: 750 < tdc < 1150

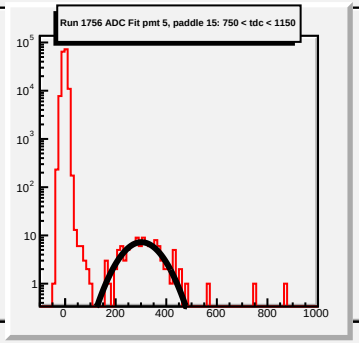

Run 1756 ADC Fit pmt 5, paddle 9: 750 < tdc < 1150


Run 1756 ADC Fit pmt 5, paddle 10: 750 < tdc < 1150


Run 1756 ADC Fit pmt 5, paddle 11: 750 < tdc < 1150


Run 1756 ADC Fit pmt 5, paddle 12: 750 < tdc < 1150


C.cdetm1r.adc_c[77] (C.cdetm1r.adc_c[75]<40 && C.cdetm1r.adc_c[78]>40)
C.cdetm1r.adc_c[77]


Run 1756 ADC Fit pmt 5, paddle 15: 750 < tdc < 1150