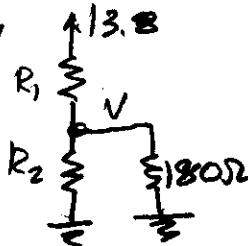


① Let's go this path: $R_L = \frac{4.5V}{25mA} = 180\Omega = R_L$

and for $i < 25mA$, $R_L > 180\Omega$.

Under full load



$V = 13.8 \left(\frac{R_2 \parallel 180}{R_1 + R_2 \parallel 180} \right) = 4.5V$ (1)

And no load: $13.8 \left(\frac{R_2}{R_1 + R_2} \right) = 5.1V$ (2)

2 eqns w/ 2 unknowns

Invert them:

$\frac{R_1 + x}{x} = \frac{13.8}{4.5} = 3.07 \rightarrow R_1 + x = 3.07x \rightarrow R_1 = 2.07x = 2.07(R_2 \parallel 180\Omega)$

$\frac{R_1 + R_2}{R_2} = \frac{13.8}{5.1} = 2.71 \rightarrow R_1 + R_2 = 2.71 R_2 \rightarrow R_1 = 1.71 R_2$

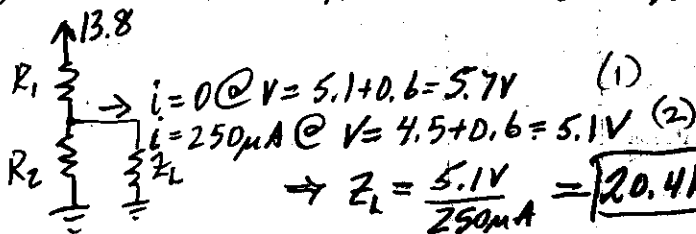
$\Rightarrow 1.71 R_2 = 2.07 \left(\frac{R_2 \cdot 180}{R_2 + 180} \right) \rightarrow R_2 + 180 = \frac{2.07}{1.71} \cdot 180 = 218 \rightarrow R_2 = 38\Omega$

$R_1 = 1.71 R_2 = 65\Omega = R_1$

③ Power: No load, $R_{TOT} = R_1 + R_2 = (38 + 65)\Omega = 103\Omega$, $P = \frac{V^2}{R} = \frac{13.8^2}{103} = 1.85W$
 With load $R_{TOT} = R_1 + R_2 \parallel 180 = 65 + \frac{38 \cdot 180}{38 + 180} = 96.4\Omega \rightarrow P = 1.98W$ loaded

Efficiency = $\frac{\text{power to load}}{\text{total power}} \cdot 100\% = \frac{(4.5V)^2 / 180\Omega}{1.98W} = \frac{113mW}{2W} = 5.6\%$ efficient

④ To get $25mA = I_E = h_{fe} \cdot I_B \rightarrow I_B = \frac{25mA}{100} = 250\mu A$. So 2 extremes:



(1) $13.8 \left(\frac{R_2}{R_1 + R_2} \right) = 5.7 \rightarrow R_1 + R_2 = \frac{13.8}{5.7} R_2 \rightarrow R_1 = 1.42 R_2$

(2) $13.8 \left(\frac{R_2 \parallel 20.4K}{R_1 + R_2 \parallel 20.4K} \right) = 5.1 \rightarrow \frac{R_1 + x}{x} = 2.71 \rightarrow R_1 = 1.71 \frac{R_2 \cdot 20.4K}{R_2 + 20.4K}$

$\Rightarrow 24.5K = R_2 + 20.4K \rightarrow R_2 = 4.11K\Omega$ $R_1 = 5.83K\Omega$

⑥ No load power $P = \frac{V^2}{R_1 + R_2} = \frac{13.8^2}{4.11k + 5.83k} = \boxed{19.2mW}$

What about the transistor? $R_L = \infty \Rightarrow I_C = 0 \Rightarrow I_B = 0 \Rightarrow I_C = 0 \dots$
it's turned off.

⑦ When $R_L = 180\Omega$, drawing $25mA$, $250\mu A$ comes from I_B and the rest comes from I_C . We've shown that the base-emitter - $R_L = 180\Omega$ series combination $= 20.4k\Omega \rightarrow$ Power through that part of the circuit is $P = \frac{V^2}{R_1 + R_2 + 20.4k} = \frac{13.8^2}{5.83k + \frac{4.11k \cdot 20.4k}{4.11k + 20.4k}} = \underline{20.6mW}$

But the majority of power comes through I_C : $P = VI = 13.8 \cdot (25mA - 250\mu A)$
 $= \underline{342mW}$

So total power $= 363mW$

The transistor will dissipate $P = V_{CE} I_C = (13.8 - 4.5)(25mA) = \boxed{233mW}$

The load power $= 113mW$ (#4) \Rightarrow

Efficiency $= \frac{113mW}{363mW} \cdot 100\% = \boxed{31\%}$, 5.9 times more efficient than the passive circuit