

1. Consider the following common-emitter amplifier circuit (Figure 1). Estimate the Q-point, small signal voltage gain, input and output resistances. Given that $\beta = 65$ and $V_A = 50$ V. Assume $V_{BE,ON} = 0.7$ V.

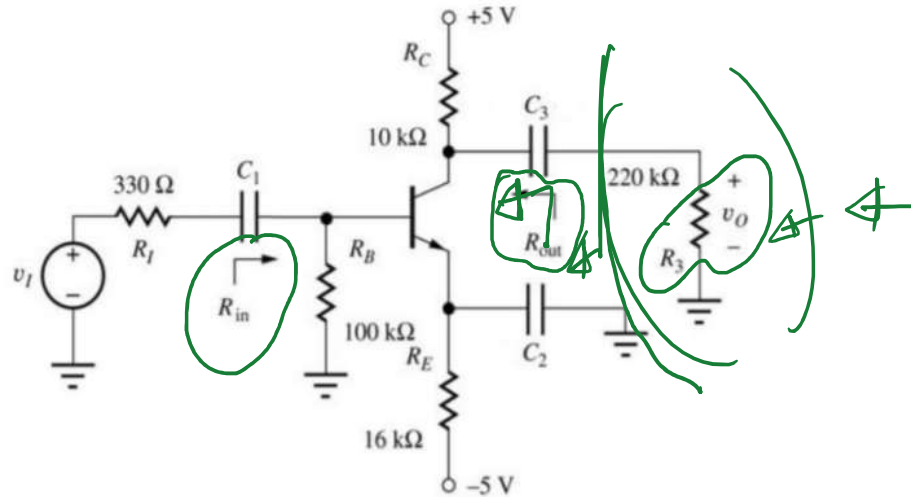


Figure 1

①

DC analysis

KVL base loop:

$$100 \times 10^3 \times I_B + 0.7 + (1 + \beta) \times 16 \times 10^3 I_B = 5$$

$$\Rightarrow I_B = 3.71 \mu A$$

$$I_C = 65 \times 3.71 \mu A = 241.1 \mu A$$

$$I_E = 244.86 \mu A$$

KVL in output loop:

$$5 - 10 \times 10^3 I_C - V_{CE} - 16 \times 10^3 I_E - (-5) = 0$$

$$\Rightarrow V_{CE} = 3.67 V$$

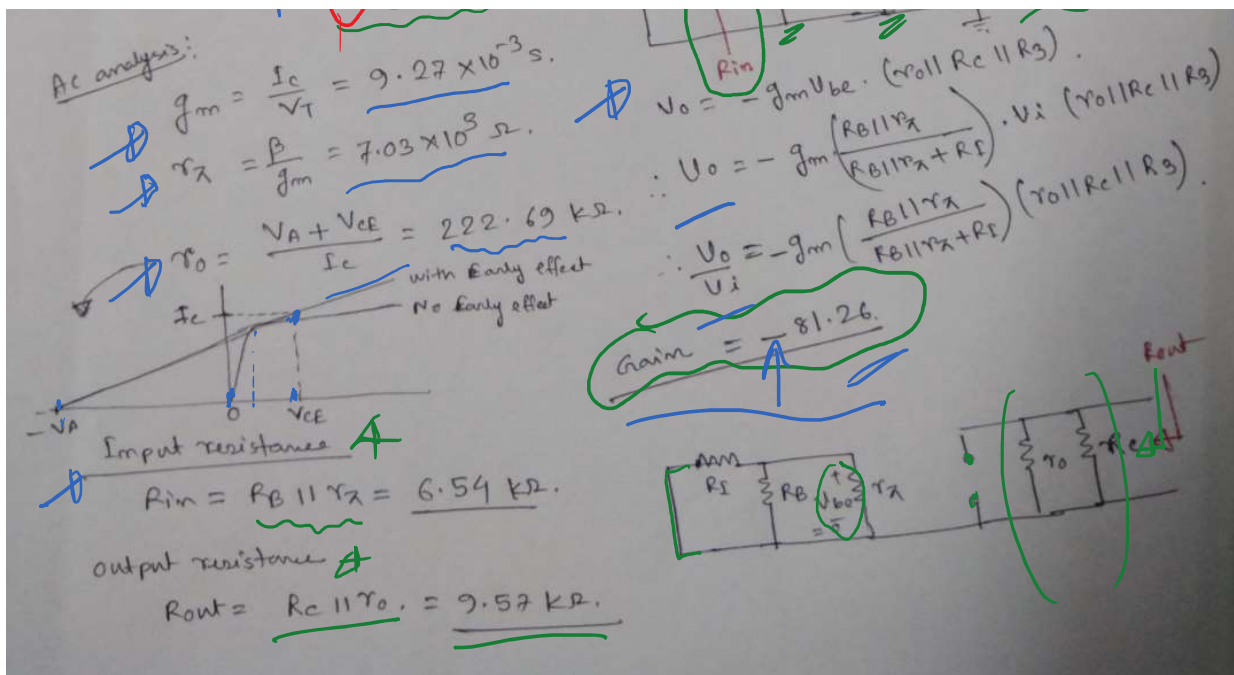
Q-point:

AC analysis:

$$\frac{I_C}{f} = 9.27 \times 10^{-3} s$$

Handwritten notes and diagrams include:

- Given parameters: $\beta_F = 65$, $V_A = 50 V$, $V_{BE,ON} = 0.7 V$.
- A red circle around the Q-point calculation.
- A small diagram showing the base-emitter loop with I_B , I_C , I_E , and V_{BE} .
- A diagram showing the input resistance R_{in} looking into the base.
- A diagram showing the output resistance R_{out} looking into the collector.



2. For the following amplifier circuit (shown in Fig. 2) the Si BJT has $\beta = 200$ and $V_A = 150 \text{ V}$.

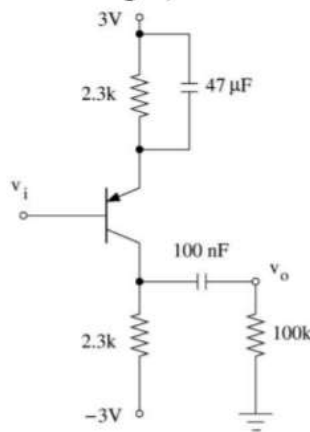
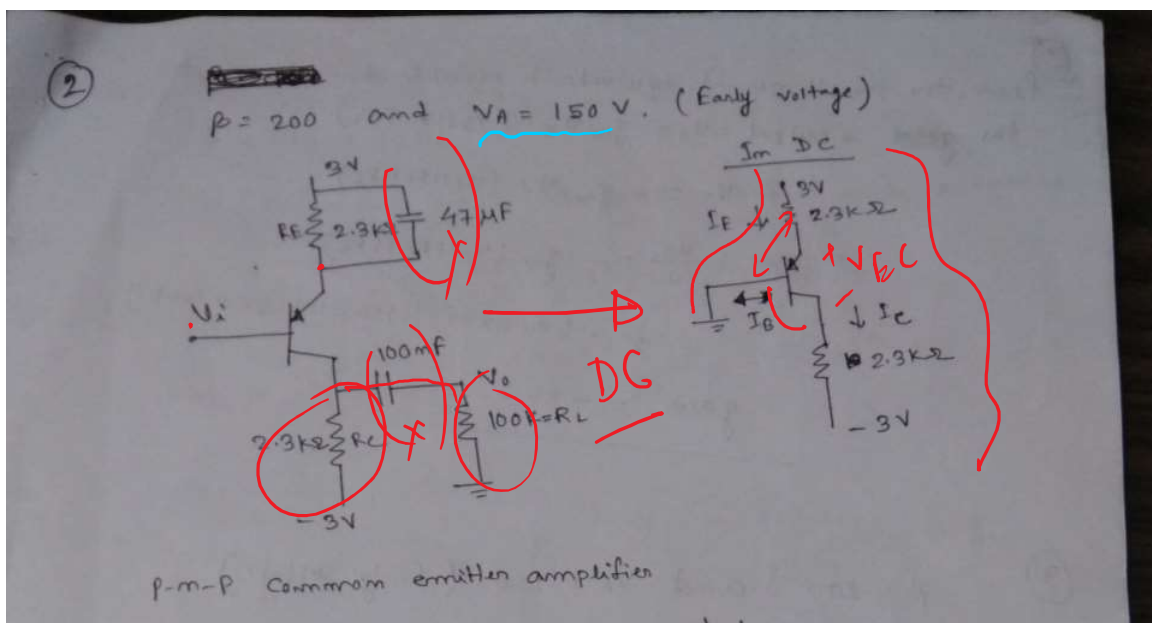


Figure 2

- Calculate I_C , I_B , I_E , V_{EC} . You can ignore the Early effect in DC bias calculations.
- Draw the AC equivalent circuit and determine the AC model parameters.
- Calculate the voltage gain.



p-n-p common emitter amplifier

(a) Assuming the BJT is in active mode:

$$V_{EB} = 0.7 \text{ V}, I_C > 0 \text{ and } V_{EC} > 0.7 \text{ V.}$$

Apply KVL in B-E loop, $3 = 2.3 \times 10^3 \times I_E + V_{EB}$

$$\therefore I_E = 1 \text{ mA} \approx I_C$$

[as $\beta = 200$]

$$I_B = \frac{I_C}{\beta} = 5 \text{ } \mu\text{A.}$$

Apply KVL in C-E loop, $3 = 2.3 \times 10^3 I_E + V_{EC} +$

$$2.3 \times 10^3 I_C - 3$$

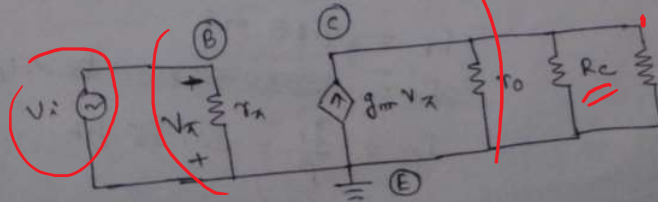
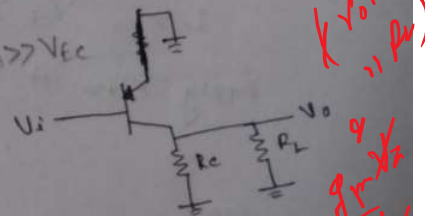
$$\therefore V_{EC} = 1.4 \text{ V} (> 0.7)$$

So, in active region as assumed.

(b) $g_m = \frac{I_C}{V_T} = \frac{10^{-3}}{26 \times 10^{-3}} = 38.5 \text{ mS}$

$$r_o = \frac{V_A}{I_C} = \frac{150}{1 \text{ mA}} = 150 \text{ k}\Omega ; V_A \gg V_{EC}$$

$$r_\pi = \frac{\beta V_T}{I_C} = 5.2 \text{ k}\Omega$$



gain = $\frac{V_o}{V_i} = -g_m r_o \parallel R_L$

5. The parameters of the transistor in the circuit in Figure 5 are $\beta = 100$ and $V_A = 100 \text{ V}$.

(a) Find the dc voltages at the base and emitter terminals.

(b) Find R_C such that $V_{CEQ} = 3.5 \text{ V}$.

(c) Assuming C_C and C_E act as short circuits, determine the small-signal voltage gain $A_v = v_o/v_s$.

(d) Repeat part (c) if the magnitude of source resistance (R_S) is changed to 500Ω .

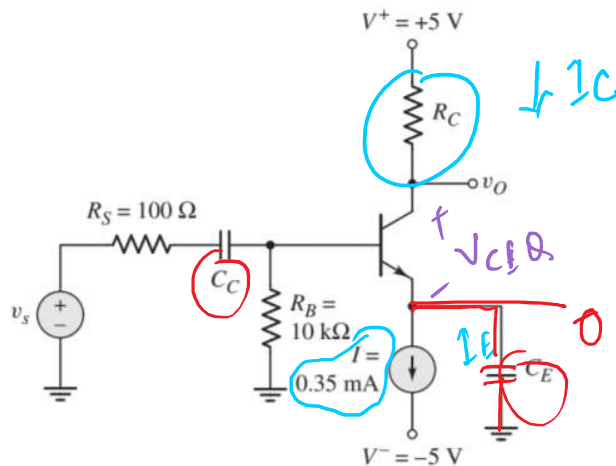
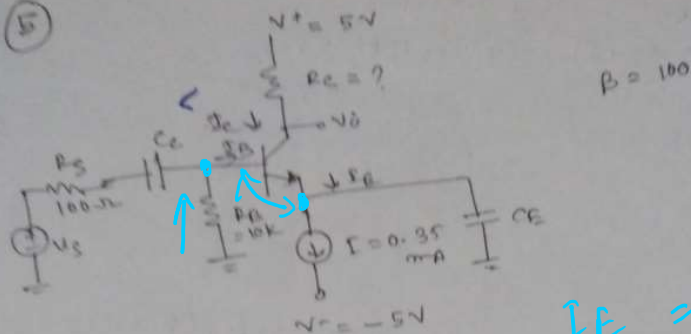


Figure 5

5



$$\frac{I_E}{\beta + 1} = I_B$$

7

So, $I_E = 0.35 \text{ mA}$

$$I_B = \frac{0.35}{101} = 0.00347 \text{ mA} = 3.47 \mu\text{A}$$

$$V_B = -I_B R_B = -(3.47 \mu\text{A}) \times 10 \text{ k}\Omega$$

$$V_B = -0.0347 \text{ V}$$

$$V_{BE(ON)} = 0.7$$

$$V_B - V_E = 0.7$$

$$V_E = -0.7 + V_B = -0.7347 \text{ V}$$

~~10. VCE, in CE amplifier~~

6

$$\frac{5 - V_O}{R_C} = I_C = \frac{\beta}{\beta + 1} I_E$$

$$\frac{5 - 2.77}{R_C} = 0.347$$

$$R_C = 6.43 \text{ k}\Omega$$

$$V_{CEQ} = 3.5 \text{ V}$$

$$V_O = V_{CEQ} + V_E$$

$$= 3.5 - 0.7347$$

$$= 2.77 \text{ V}$$

7

$$V_A = 100 \text{ V}$$

$$r_o = \frac{V_A}{I_C} = 288 \text{ k}\Omega$$

$$g_m = \frac{I_C}{V_T} = 13.3 \text{ mA/V}$$

$$r_{\pi} = \frac{100 \times V_T}{I_C} = 7.49 \text{ k}\Omega$$

$$\text{gain}(A_v) = \frac{V_O}{V_S} = -g_m \left(\frac{R_B \parallel r_{\pi}}{R_S + R_B \parallel r_{\pi}} \right) (R_C \parallel r_o)$$

$$= -81.7$$

④ if, $R_s = 500 \Omega$

$$A_v = -g_m \times \frac{R_B \parallel r_x}{R_B \parallel r_x + R_s} \times (R_C \parallel r_o)$$

$$= -74.9 \quad (\text{Comment: gain reduces when } R_s \text{ increases})$$

6. Find the expression of small signal voltage gain for the following circuit (Figure 6)

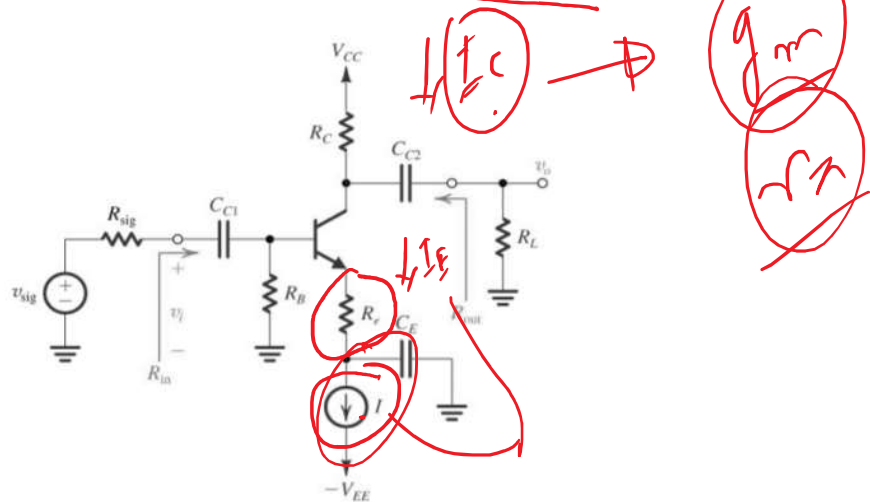


Figure 6

⑥

$$v_{in} = i_b r_x + (1 + \beta) i_b R_E \quad \therefore R_{ib} = \frac{v_{in}}{i_b} = r_x + (1 + \beta) R_E$$

$$R_{in} = R_B \parallel R_{ib}$$

$$\therefore v_{in} = \frac{R_{in}}{R_{in} + R_{sig}} (v_{sig})$$

$$v_o = -\beta i_b (R_C \parallel R_L)$$

$$v_o = -\beta \left(\frac{v_{in}}{R_{ib}} \right) (R_C \parallel R_L)$$

$$v_o = -\frac{\beta (R_C \parallel R_L)}{r_x + (1 + \beta) R_E} \cdot v_{in}$$

$$\therefore \frac{v_o}{v_{in}} = -\frac{\beta (R_C \parallel R_L)}{r_x + (1 + \beta) R_E}$$

$$\therefore \left(\frac{v_o}{v_{sig}} \right) = -\frac{\beta R_{in} (R_C \parallel R_L)}{(R_{in} + R_{sig}) [r_x + (1 + \beta) R_E]} = A_v \quad (\text{Small-signal voltage gain}).$$

Doubt??

$$\frac{U_o}{U_{sig}} = - \frac{\beta R_{in}}{(R_{in} + R_{sig}) [r_x + (1 + \beta) R_e]} \quad \text{voltage gain}$$