

Switching Theory & Logic Design Laboratory
CSE Department, IIT Kharagpur
Spring Semester 2022-23

Module 1 : Combinational Logic Circuits

Assignment 1: Realize the following function:

$$f(w, x, y, z) = \Sigma (0, 1, 2, 3, 5, 7, 8, 9, 10, 12, 13)$$

(a) Using only NAND gates

(b) Using 8 x 1 MUX

(1 day)