

Switching Theory & Logic Design Laboratory
CSE Department, IIT Kharagpur
Spring Semester 2022-23

Module 4 : Pulse Circuits

Assignment 9 :

1. a. Design an Astable Multivibrator using a 555-timer chip to operate at 20 KHz frequency with 50% duty cycle.
- b. Design a Monostable Multivibrator using a 555-timer chip for a positive (+) pulse width of 100 μ sec.

(1 day)