

Switching Circuits and Logic Design Laboratory

CSE Department, IIT Kharagpur

Spring Semester 2022-23

Module 3: SEQUENTIAL CIRCUITS AND FSM

Assignment 6:

1.
 - a. Implement a master-slave JK flip-flop using NAND gates, and verify its functionality.
 - b. Configure it as a T flip-flop, and verify that it divides the frequency of an input pulse train by two.
 - c. Implement D flip-flop using JK flip-flop and verify its functionality.
- (1 day)