

CMP 334 (3/6/19)

HW 8 (two's complement circuit)

Combinational circuit design

Inverters, decoders, multiplexers

Signed Arithmetic (review)

Condition flags and comparisons

Arithmetic / Logical Units

HW 7 ($W \leftarrow X + Y + Z$)

ALU operations (explicit and implicit)

Condition flags and conditional branches

The **TOY** ALU

HW 8: Two's Complement Circuit

Use the four step *Combinational Circuit Design Process* presented in class to design circuits that take a 3-bit unsigned binary integer **X** as input and produces as output a 3-bit unsigned binary integer **Y** that is the two's complement of **X**.

$$\mathbf{Y} = \mathbf{2}^3 - \mathbf{X} = \overline{\mathbf{X}} + \mathbf{1}$$

Do not minimize the circuits for **Y** as a part of this assignments.

Extra credit: minimize the circuits for **Y**.

Combinational Circuit Design

Combinational circuit

Output determined by input

Design process

1. Specify semantics

Black Box: *input* and *output* (informal semantics)

Truth Table (formal semantics)

2. Truth table \rightarrow Boolean formula

3. Minimize boolean formula (optional)

Boolean algebra

Karnaugh maps

4. Boolean formula \rightarrow combinational circuit

Combinational Circuit Design

Combinational circuit

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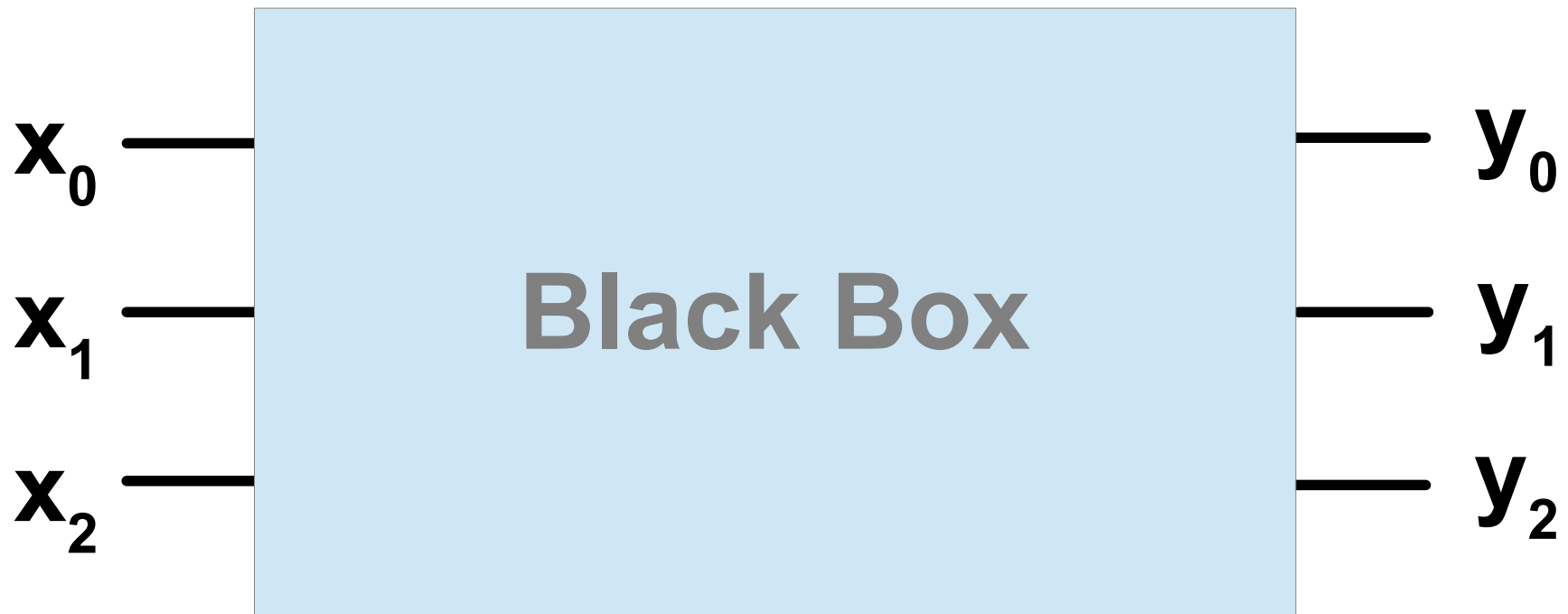
3. Minimize boolean formula (optional)

Boolean algebra

Karnaugh maps

4. Boolean formula \rightarrow combinational circuit

HW 8: Two's Complement



Informal semantics:

$$\mathbf{X} = 4 \cdot x_2 + 2 \cdot x_1 + x_0$$

$$\mathbf{Y} = 4 \cdot y_2 + 2 \cdot y_1 + y_0 = \mathbf{\bar{\bar{\bar{\bar{X}}}}} = 8 - \mathbf{X} = \bar{\mathbf{X}} + 1$$

Two's Complement Truth Table

X	x₂	x₁	x₀	Y	y₂	y₁	y₀
	0	0	0				
	0	0	1				
	0	1	0				
	0	1	1				
	1	0	0				
	1	0	1				
	1	1	0				
	1	1	1				

Two's Complement Truth Table

X	x₂	x₁	x₀	Y	y₂	y₁	y₀
0	0	0	0				
1	0	0	1				
2	0	1	0				
3	0	1	1				
4	1	0	0				
5	1	0	1				
6	1	1	0				
7	1	1	1				

Two's Complement Truth Table

X	x₂	x₁	x₀	Y	y₂	y₁	y₀
0	0	0	0	8			
1	0	0	1	7			
2	0	1	0	6			
3	0	1	1	5			
4	1	0	0	4			
5	1	0	1	3			
6	1	1	0	2			
7	1	1	1	1			

Two's Complement Truth Table

X	x₂	x₁	x₀	Y	y₂	y₁	y₀
0	0	0	0	8	0	0	0
1	0	0	1	7	1	1	1
2	0	1	0	6	1	1	0
3	0	1	1	5	1	0	1
4	1	0	0	4	1	0	0
5	1	0	1	3	0	1	1
6	1	1	0	2	0	1	0
7	1	1	1	1	0	0	1

Combinational Circuit Design

Combinational circuit

Output determined by input

Design process

1. Specify semantics

Black Box: *input* and *output* (informal semantics)

Truth Table (formal semantics)

2. Truth table \rightarrow Boolean formula

3. Minimize boolean formula (optional)

Boolean algebra

Karnaugh maps

4. Boolean formula \rightarrow combinational circuit

Boolean Formula for y_0

X	x₂	x₁	x₀	Y	y₂	y₁	y₀
0	0	0	0	8	0	0	0
1	0	0	1	7	1	1	1
2	0	1	0	6	1	1	0
3	0	1	1	5	1	0	1
4	1	0	0	4	1	0	0
5	1	0	1	3	0	1	1
6	1	1	0	2	0	1	0
7	1	1	1	1	0	0	1

Boolean Formula for y_0

X	x_2	x_1	x_0	Y	y_0
0	0	0	0	8	0
1	0	0	1	7	1
2	0	1	0	6	0
3	0	1	1	5	1
4	1	0	0	4	0
5	1	0	1	3	1
6	1	1	0	2	0
7	1	1	1	1	1

Boolean Formula for y_0

X	x_2	x_1	x_0	Y	y_0
0	0	0	0	8	0
1	0	0	1	7	1
2	0	1	0	6	0
3	0	1	1	5	1
4	1	0	0	4	0
5	1	0	1	3	1
6	1	1	0	2	0
7	1	1	1	1	1

$$\bar{x}_2 \bar{x}_1 x_0$$

Boolean Formula for y_0

X	x_2	x_1	x_0	Y	y_0
0	0	0	0	8	0
1	0	0	1	7	1
2	0	1	0	6	0
3	0	1	1	5	1
4	1	0	0	4	0
5	1	0	1	3	1
6	1	1	0	2	0
7	1	1	1	1	1

$$\bar{x}_2 \bar{x}_1 x_0$$

$$\bar{x}_2 x_1 x_0$$

Boolean Formula for y_0

X	x_2	x_1	x_0	Y	y_0	
0	0	0	0	8	0	
1	0	0	1	7	1	$\bar{x}_2 \bar{x}_1 x_0$
2	0	1	0	6	0	
3	0	1	1	5	1	$\bar{x}_2 x_1 x_0$
4	1	0	0	4	0	
5	1	0	1	3	1	$x_2 \bar{x}_1 x_0$
6	1	1	0	2	0	
7	1	1	1	1	1	

Boolean Formula for y_0

X	x_2	x_1	x_0	Y	y_0	
0	0	0	0	8	0	
1	0	0	1	7	1	$\bar{x}_2 \bar{x}_1 x_0$
2	0	1	0	6	0	
3	0	1	1	5	1	$\bar{x}_2 x_1 x_0$
4	1	0	0	4	0	
5	1	0	1	3	1	$x_2 \bar{x}_1 x_0$
6	1	1	0	2	0	
7	1	1	1	1	1	$x_2 x_1 x_0$

$$y_0 = \bar{x}_2 \bar{x}_1 x_0 + \bar{x}_2 x_1 x_0 + x_2 \bar{x}_1 x_0 + x_2 x_1 x_0$$

X	x₂	x₁	x₀	Y	y₀
0	0	0	0	8	0
1	0	0	1	7	1
2	0	1	0	6	0
3	0	1	1	5	1
4	1	0	0	4	0
5	1	0	1	3	1
6	1	1	0	2	0
7	1	1	1	1	1

$$\bar{x}_2 \bar{x}_1 x_0$$

$$\bar{x}_2 x_1 x_0$$

$$x_2 \bar{x}_1 x_0$$

$$x_2 x_1 x_0$$

Boolean Formula for y_1

X	x₂	x₁	x₀	Y	y₂	y₁	y₀
0	0	0	0	8	0	0	0
1	0	0	1	7	1	1	1
2	0	1	0	6	1	1	0
3	0	1	1	5	1	0	1
4	1	0	0	4	1	0	0
5	1	0	1	3	0	1	1
6	1	1	0	2	0	1	0
7	1	1	1	1	0	0	1

Boolean Formula for y_1

X	x_2	x_1	x_0	Y	y_1
0	0	0	0	8	0
1	0	0	1	7	1
2	0	1	0	6	1
3	0	1	1	5	0
4	1	0	0	4	0
5	1	0	1	3	1
6	1	1	0	2	1
7	1	1	1	1	0

Boolean Formula for y_1

X	x_2	x_1	x_0	Y	y_1
0	0	0	0	8	0
1	0	0	1	7	1
2	0	1	0	6	1
3	0	1	1	5	0
4	1	0	0	4	0
5	1	0	1	3	1
6	1	1	0	2	1
7	1	1	1	1	0

$$\bar{x}_2 \bar{x}_1 x_0$$

Boolean Formula for y_1

X	x_2	x_1	x_0	Y	y_1
0	0	0	0	8	0
1	0	0	1	7	1
2	0	1	0	6	1
3	0	1	1	5	0
4	1	0	0	4	0
5	1	0	1	3	1
6	1	1	0	2	1
7	1	1	1	1	0

$$\bar{x}_2 \bar{x}_1 x_0$$

$$\bar{x}_2 x_1 \bar{x}_0$$

Boolean Formula for y_1

X	x_2	x_1	x_0	Y	y_1
0	0	0	0	8	0
1	0	0	1	7	1
2	0	1	0	6	1
3	0	1	1	5	0
4	1	0	0	4	0
5	1	0	1	3	1
6	1	1	0	2	1
7	1	1	1	1	0

$$\bar{x}_2 \bar{x}_1 x_0$$

$$\bar{x}_2 x_1 \bar{x}_0$$

$$x_2 \bar{x}_1 x_0$$

Boolean Formula for y_1

X	x_2	x_1	x_0	Y	y_1
0	0	0	0	8	0
1	0	0	1	7	1
2	0	1	0	6	1
3	0	1	1	5	0
4	1	0	0	4	0
5	1	0	1	3	1
6	1	1	0	2	1
7	1	1	1	1	0

$$\bar{x}_2 \bar{x}_1 x_0$$

$$\bar{x}_2 x_1 \bar{x}_0$$

$$x_2 \bar{x}_1 x_0$$

$$x_2 x_1 \bar{x}_0$$

$$y_1 = \bar{x}_2 \bar{x}_1 x_0 + \bar{x}_2 x_1 \bar{x}_0 + x_2 \bar{x}_1 x_0 + x_2 x_1 \bar{x}_0$$

X	x₂	x₁	x₀	Y	y₁
0	0	0	0	8	0
1	0	0	1	7	1
2	0	1	0	6	1
3	0	1	1	5	0
4	1	0	0	4	0
5	1	0	1	3	1
6	1	1	0	2	1
7	1	1	1	1	0

$$\bar{x}_2 \bar{x}_1 x_0$$

$$\bar{x}_2 x_1 \bar{x}_0$$

$$x_2 \bar{x}_1 x_0$$

$$x_2 x_1 \bar{x}_0$$

Boolean Formula for y_2

X	x₂	x₁	x₀	Y	y₂	y₁	y₀
0	0	0	0	8	0	0	0
1	0	0	1	7	1	1	1
2	0	1	0	6	1	1	0
3	0	1	1	5	1	0	1
4	1	0	0	4	1	0	0
5	1	0	1	3	0	1	1
6	1	1	0	2	0	1	0
7	1	1	1	1	0	0	1

Boolean Formula for y_2

X	x_2	x_1	x_0	Y	y_2
0	0	0	0	8	0
1	0	0	1	7	1
2	0	1	0	6	1
3	0	1	1	5	1
4	1	0	0	4	1
5	1	0	1	3	0
6	1	1	0	2	0
7	1	1	1	1	0

Boolean Formula for y_2

X	x_2	x_1	x_0	Y	y_2
0	0	0	0	8	0
1	0	0	1	7	1
2	0	1	0	6	1
3	0	1	1	5	1
4	1	0	0	4	1
5	1	0	1	3	0
6	1	1	0	2	0
7	1	1	1	1	0

$$\bar{x}_2 \bar{x}_1 x_0$$

Boolean Formula for y_2

X	x_2	x_1	x_0	Y	y_2
0	0	0	0	8	0
1	0	0	1	7	1
2	0	1	0	6	1
3	0	1	1	5	1
4	1	0	0	4	1
5	1	0	1	3	0
6	1	1	0	2	0
7	1	1	1	1	0

$$\bar{x}_2 \bar{x}_1 x_0$$

$$\bar{x}_2 x_1 \bar{x}_0$$

Boolean Formula for y_2

X	x_2	x_1	x_0	Y	y_2
0	0	0	0	8	0
1	0	0	1	7	1
2	0	1	0	6	1
3	0	1	1	5	1
4	1	0	0	4	1
5	1	0	1	3	0
6	1	1	0	2	0
7	1	1	1	1	0

$$\bar{x}_2 \bar{x}_1 x_0$$

$$\bar{x}_2 x_1 \bar{x}_0$$

$$\bar{x}_2 x_1 x_0$$

Boolean Formula for y_2

X	x_2	x_1	x_0	Y	y_2
0	0	0	0	8	0
1	0	0	1	7	1
2	0	1	0	6	1
3	0	1	1	5	1
4	1	0	0	4	1
5	1	0	1	3	0
6	1	1	0	2	0
7	1	1	1	1	0

$$\bar{x}_2 \bar{x}_1 x_0$$

$$\bar{x}_2 x_1 \bar{x}_0$$

$$\bar{x}_2 x_1 x_0$$

$$x_2 \bar{x}_1 \bar{x}_0$$

$$y_2 = \bar{x}_2 \bar{x}_1 x_0 + \bar{x}_2 x_1 \bar{x}_0 + \bar{x}_2 x_1 x_0 + x_2 \bar{x}_1 \bar{x}_0$$

X	x₂	x₁	x₀	Y	y₂
0	0	0	0	8	0
1	0	0	1	7	1
2	0	1	0	6	1
3	0	1	1	5	1
4	1	0	0	4	1
5	1	0	1	3	0
6	1	1	0	2	0
7	1	1	1	1	0

$$\bar{x}_2 \bar{x}_1 x_0$$

$$\bar{x}_2 x_1 \bar{x}_0$$

$$\bar{x}_2 x_1 x_0$$

$$x_2 \bar{x}_1 \bar{x}_0$$

Two's Complement Formulas

$$y_0 = \bar{x}_2 \bar{x}_1 x_0 + \bar{x}_2 x_1 x_0 + x_2 \bar{x}_1 x_0 + x_2 x_1 x_0$$

$$y_1 = \bar{x}_2 \bar{x}_1 x_0 + \bar{x}_2 x_1 \bar{x}_0 + x_2 \bar{x}_1 x_0 + x_2 x_1 \bar{x}_0$$

$$y_2 = \bar{x}_2 \bar{x}_1 x_0 + \bar{x}_2 x_1 \bar{x}_0 + \bar{x}_2 x_1 x_0 + x_2 \bar{x}_1 \bar{x}_0$$

Combinational Circuit Design

Combinational circuit

Output determined by input

Design process

1. Specify semantics

Black Box input and output (informal semantics)

Truth Table (formal semantics)

2. Truth table \rightarrow Boolean formula

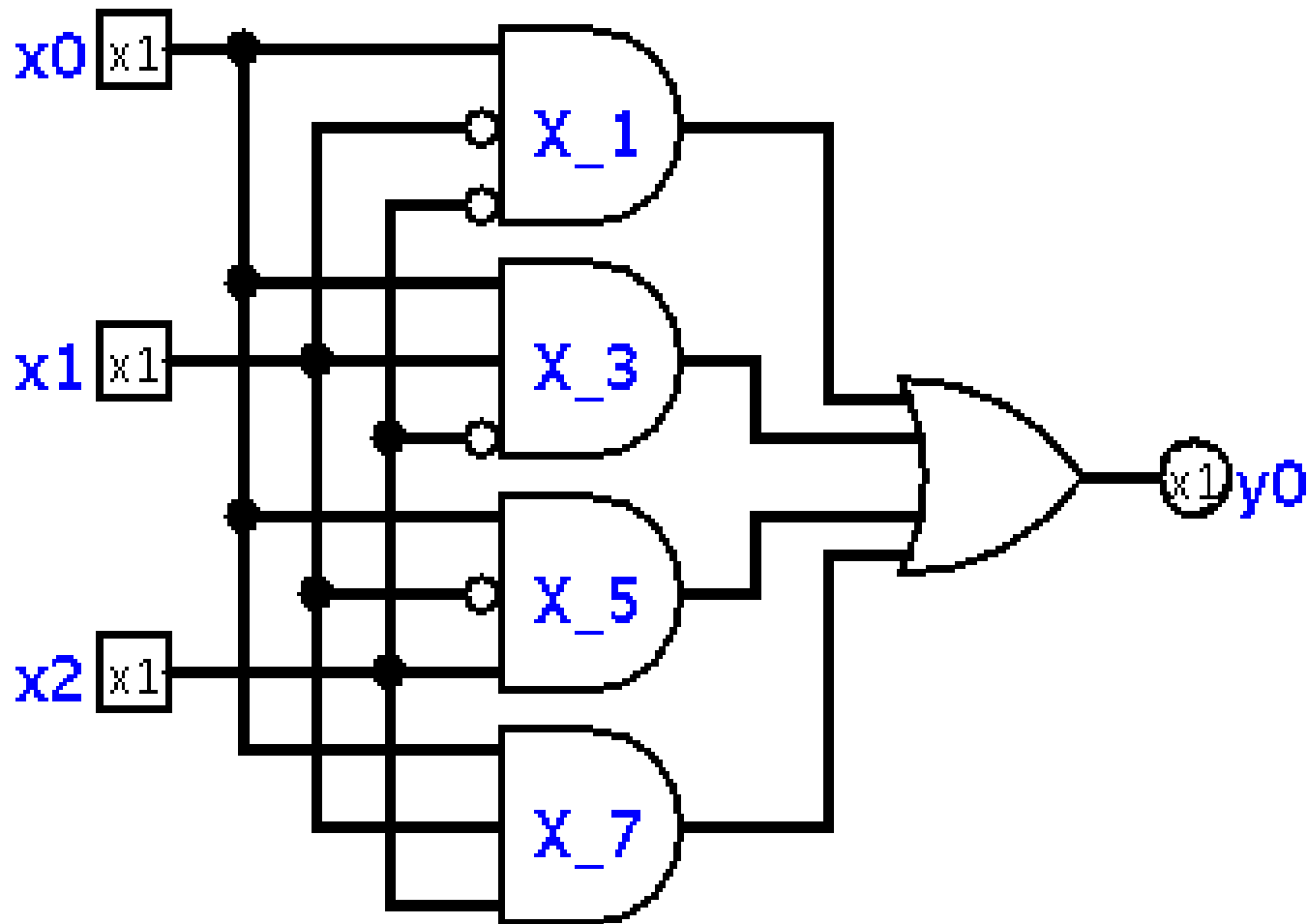
3. Minimize boolean formula (optional)

Boolean algebra

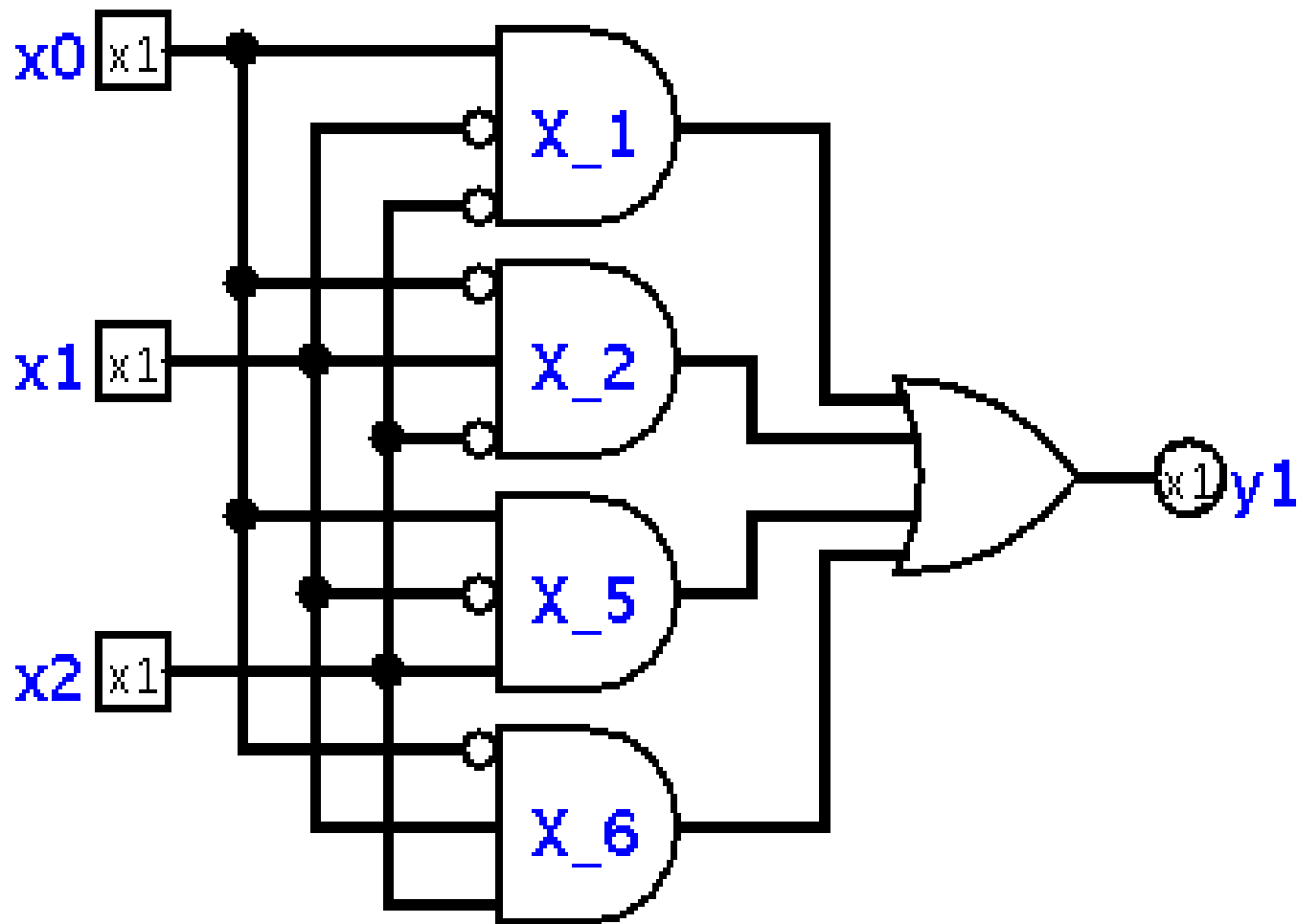
Karnaugh maps

4. Boolean formula \rightarrow combinational circuit

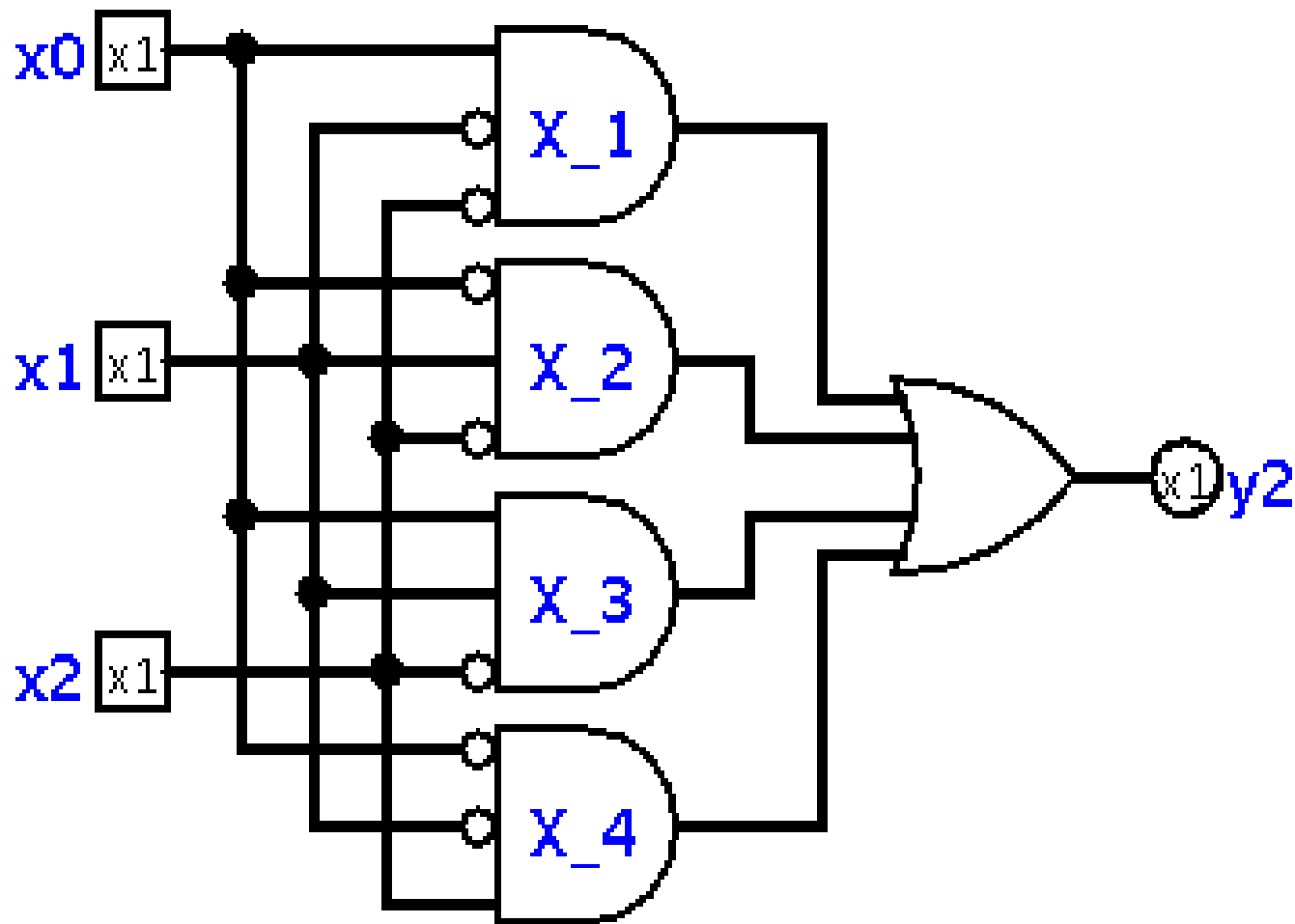
$$y_0 = \bar{x}_2 \bar{x}_1 x_0 + \bar{x}_2 x_1 x_0 + x_2 \bar{x}_1 x_0 + x_2 x_1 x_0$$



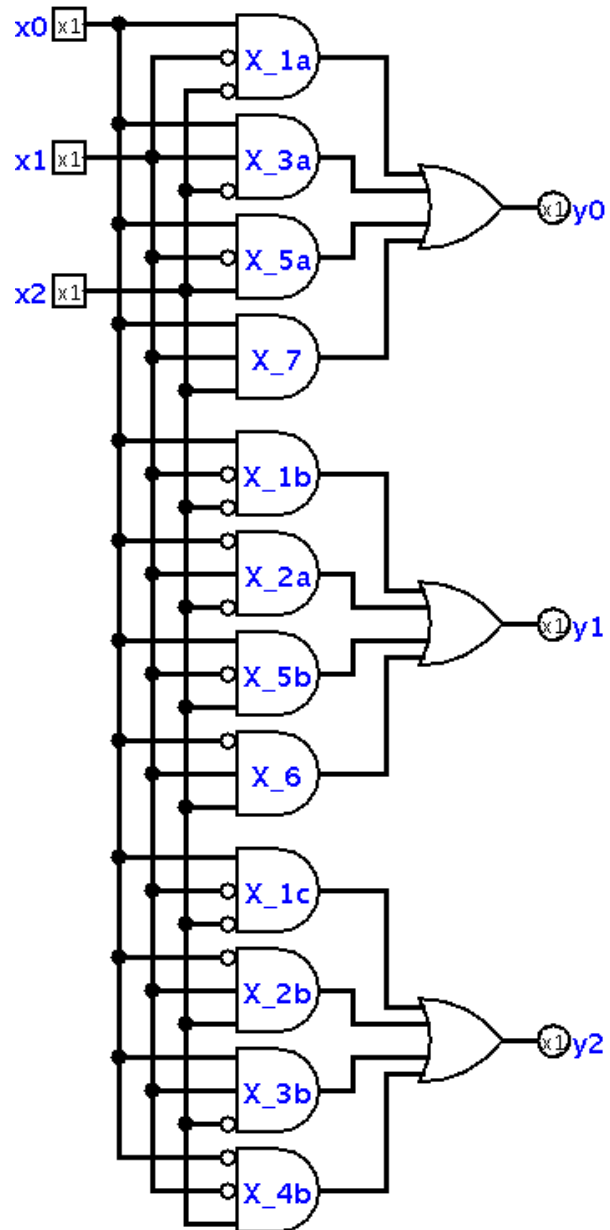
$$y_1 = \bar{x}_2 \bar{x}_1 x_0 + \bar{x}_2 x_1 \bar{x}_0 + x_2 \bar{x}_1 x_0 + x_2 x_1 \bar{x}_0$$



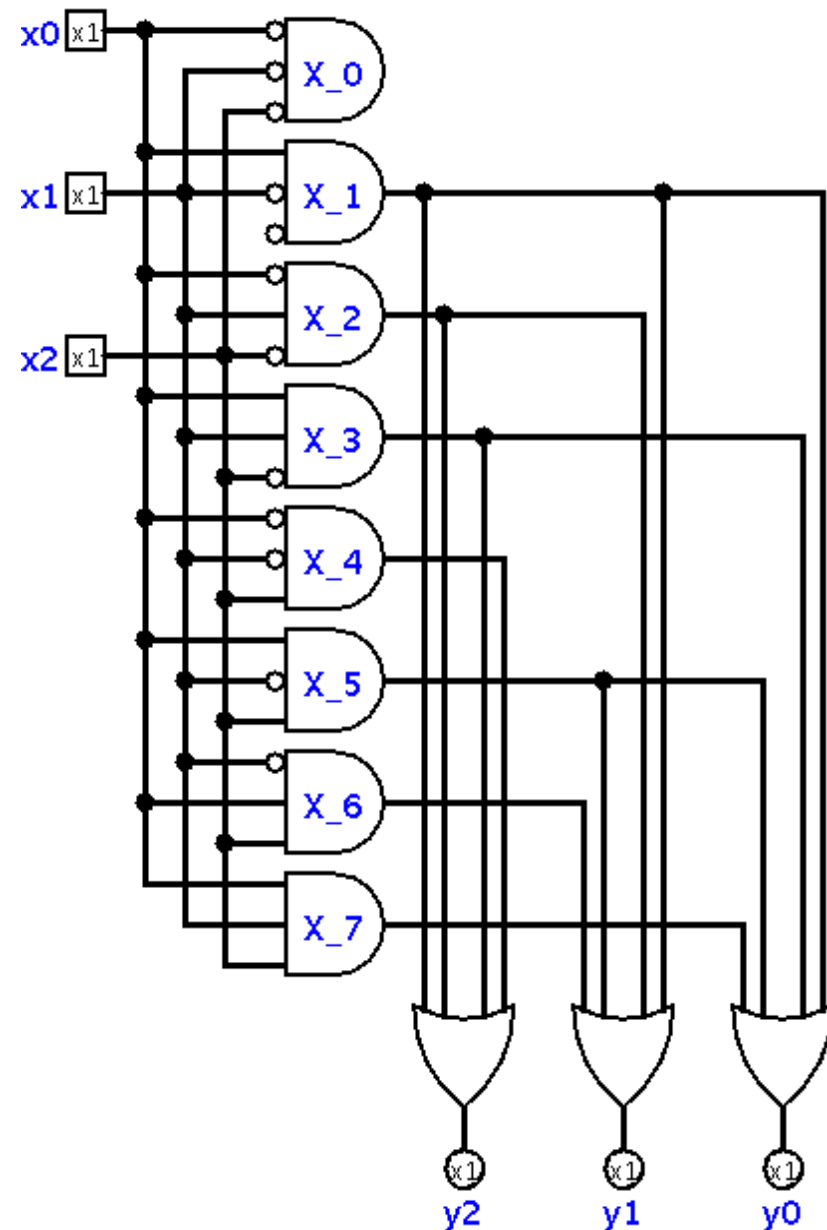
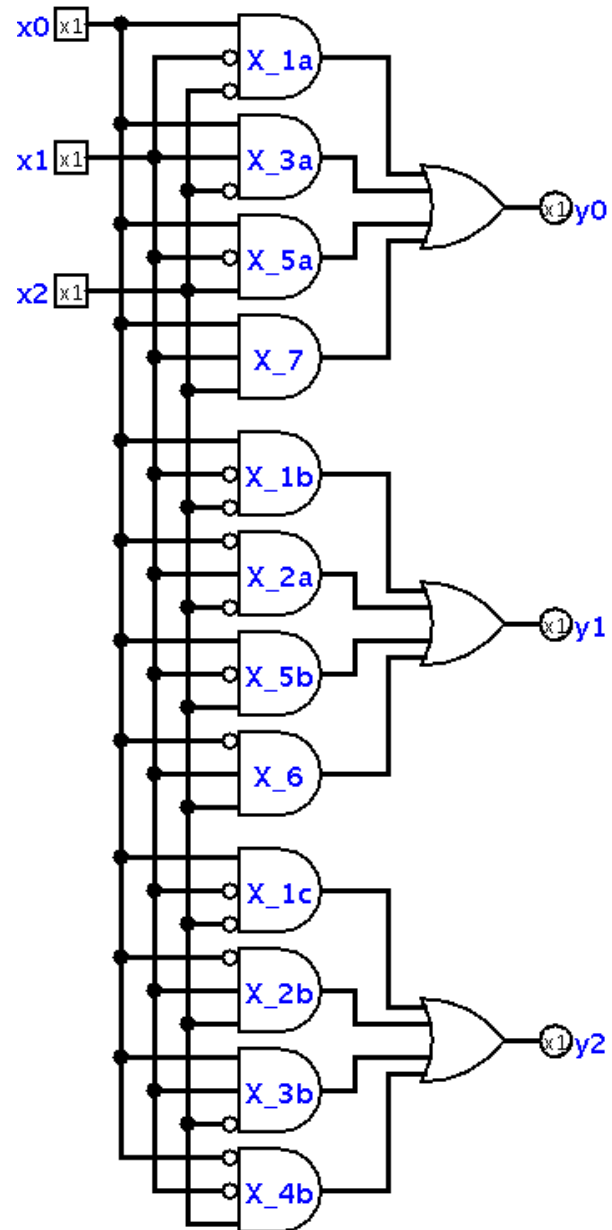
$$y_2 = \bar{x}_2 \bar{x}_1 x_0 + \bar{x}_2 x_1 \bar{x}_0 + \bar{x}_2 x_1 x_0 + x_2 \bar{x}_1 \bar{x}_0$$



Two's Complement Circuits



Two's Complement Gate Array



Combinational Circuit Design

Combinational circuit

Output determined by input

Design process

1. Specify semantics

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3. Minimize boolean formula (optional)

Boolean algebra

Karnaugh maps

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Simplification of Boolean Formulas

$$y_0 = \bar{x}_2 \bar{x}_1 x_0 + \bar{x}_2 x_1 x_0 + x_2 \bar{x}_1 x_0 + x_2 x_1 x_0$$

$$y_1 = \bar{x}_2 \bar{x}_1 x_0 + \bar{x}_2 x_1 \bar{x}_0 + x_2 \bar{x}_1 x_0 + x_2 x_1 \bar{x}_0$$

$$y_2 = \bar{x}_2 \bar{x}_1 x_0 + \bar{x}_2 x_1 \bar{x}_0 + \bar{x}_2 x_1 x_0 + x_2 \bar{x}_1 \bar{x}_0$$

Simplification of y_0 Formula

$$y_0 = \bar{x}_2 \bar{x}_1 x_0 + \bar{x}_2 x_1 x_0 + x_2 \bar{x}_1 x_0 + x_2 x_1 x_0$$

$$y_1 = \bar{x}_2 \bar{x}_1 x_0 + \bar{x}_2 x_1 \bar{x}_0 + x_2 \bar{x}_1 x_0 + x_2 x_1 \bar{x}_0$$

$$y_2 = \bar{x}_2 \bar{x}_1 x_0 + \bar{x}_2 x_1 \bar{x}_0 + \bar{x}_2 x_1 x_0 + x_2 \bar{x}_1 \bar{x}_0$$

Simplification of y_0 Formula

$$y_0 = \bar{x}_2 \bar{x}_1 x_0 + \bar{x}_2 x_1 x_0 + x_2 \bar{x}_1 x_0 + x_2 x_1 x_0$$

Simplification of y_0 Formula

$$y_0 = \overline{x}_2 \overline{x}_1 x_0 + \overline{x}_2 x_1 x_0 + x_2 \overline{x}_1 x_0 + x_2 x_1 x_0$$

Simplification of y_0 Formula

$$y_0 = \bar{x}_2 \bar{x}_1 x_0 + \bar{x}_2 x_1 x_0 + x_2 \bar{x}_1 x_0 + x_2 x_1 x_0$$

$$y_0 = \bar{x}_2 x_0 + x_2 \bar{x}_1 x_0 + x_2 x_1 x_0$$

Simplification of y_0 Formula

$$y_0 = \bar{x}_2 \bar{x}_1 x_0 + \bar{x}_2 x_1 x_0 + x_2 \bar{x}_1 x_0 + x_2 x_1 x_0$$

$$y_0 = \bar{x}_2 x_0 + x_2 \bar{x}_1 x_0 + x_2 x_1 x_0$$

Simplification of y_0 Formula

$$y_0 = \bar{x}_2 \bar{x}_1 x_0 + \bar{x}_2 x_1 x_0 + x_2 \bar{x}_1 x_0 + x_2 x_1 x_0$$

$$y_0 = \bar{x}_2 x_0 + x_2 \bar{x}_1 x_0 + x_2 x_1 x_0$$

$$y_0 = \bar{x}_2 x_0 + x_2 x_0$$

Simplification of y_0 Formula

$$y_0 = \bar{x}_2 \bar{x}_1 x_0 + \bar{x}_2 x_1 x_0 + x_2 \bar{x}_1 x_0 + x_2 x_1 x_0$$

$$y_0 = \bar{x}_2 x_0 + x_2 \bar{x}_1 x_0 + x_2 x_1 x_0$$

$$y_0 = \bar{x}_2 x_0 + x_2 x_0$$

Simplification of y_0 Formula

$$y_0 = \bar{x}_2 \bar{x}_1 x_0 + \bar{x}_2 x_1 x_0 + x_2 \bar{x}_1 x_0 + x_2 x_1 x_0$$

$$y_0 = \bar{x}_2 x_0 + x_2 \bar{x}_1 x_0 + x_2 x_1 x_0$$

$$y_0 = \bar{x}_2 x_0 + x_2 x_0$$

$$y_0 = x_0$$

Simplification of y_1 Formula

$$y_0 = x_0$$

$$y_1 = \bar{x}_2 \bar{x}_1 x_0 + \bar{x}_2 x_1 \bar{x}_0 + x_2 \bar{x}_1 x_0 + x_2 x_1 \bar{x}_0$$

$$y_2 = \bar{x}_2 \bar{x}_1 x_0 + \bar{x}_2 x_1 \bar{x}_0 + \bar{x}_2 x_1 x_0 + x_2 \bar{x}_1 \bar{x}_0$$

Simplification of y_1 Formula

$$y_1 = \bar{x}_2 \bar{x}_1 x_0 + \bar{x}_2 x_1 \bar{x}_0 + x_2 \bar{x}_1 x_0 + x_2 x_1 \bar{x}_0$$

Simplification of y_1 Formula

$$y_1 = \bar{x}_2 \bar{x}_1 x_0 + \bar{x}_2 x_1 \bar{x}_0 + x_2 \bar{x}_1 x_0 + x_2 x_1 \bar{x}_0$$

Simplification of y_1 Formula

$$y_1 = \bar{x}_2 \bar{x}_1 x_0 + \bar{x}_2 x_1 \bar{x}_0 + x_2 \bar{x}_1 x_0 + x_2 x_1 \bar{x}_0$$

$$y_1 = \bar{x}_1 x_0 + \bar{x}_2 x_1 \bar{x}_0 + x_2 x_1 \bar{x}_0$$

Simplification of y_1 Formula

$$y_1 = \bar{x}_2 \bar{x}_1 x_0 + \bar{x}_2 x_1 \bar{x}_0 + x_2 \bar{x}_1 x_0 + x_2 x_1 \bar{x}_0$$

$$y_1 = x_1 x_0 + \bar{x}_2 x_1 \bar{x}_0 + x_2 x_1 \bar{x}_0$$

Simplification of y_1 Formula

$$y_1 = \bar{x}_2 \bar{x}_1 x_0 + \bar{x}_2 x_1 \bar{x}_0 + x_2 \bar{x}_1 x_0 + x_2 x_1 \bar{x}_0$$

$$y_1 = \bar{x}_1 x_0 + \bar{x}_2 x_1 \bar{x}_0 + x_2 x_1 \bar{x}_0$$

$$y_1 = \bar{x}_1 x_0 + x_1 \bar{x}_0$$

Simplification of y_2 Formula

$$y_0 = x_0$$

$$y_1 = \bar{x}_1 x_0 + x_1 \bar{x}_0$$

$$y_2 = \bar{x}_2 \bar{x}_1 x_0 + \bar{x}_2 x_1 \bar{x}_0 + \bar{x}_2 x_1 x_0 + x_2 \bar{x}_1 \bar{x}_0$$

Simplification of y_2 Formula

$$y_2 = \bar{x}_2 \bar{x}_1 x_0 + \bar{x}_2 x_1 \bar{x}_0 + \bar{x}_2 x_1 x_0 + x_2 \bar{x}_1 \bar{x}_0$$

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$$y_2 = \bar{x}_2 \bar{x}_1 x_0 + \bar{x}_2 x_1 + x_2 \bar{x}_1 \bar{x}_0$$

Simplification of y_2 Formula

$$y_2 = \bar{x}_2 \bar{x}_1 x_0 + \bar{x}_2 x_1 \bar{x}_0 + \bar{x}_2 x_1 x_0 + x_2 \bar{x}_1 \bar{x}_0$$

$$y_2 = \bar{x}_2 \bar{x}_1 x_0 + \bar{x}_2 x_1 + x_2 \bar{x}_1 \bar{x}_0$$

$$y_2 = \bar{x}_2 \bar{x}_1 x_0 + \bar{x}_2 x_1 + \bar{x}_2 x_1 x_0 + x_2 \bar{x}_1 \bar{x}_0$$

Simplification of y_2 Formula

$$y_2 = \bar{x}_2 \bar{x}_1 x_0 + \bar{x}_2 x_1 \bar{x}_0 + \bar{x}_2 x_1 x_0 + x_2 \bar{x}_1 \bar{x}_0$$

$$y_2 = \bar{x}_2 \bar{x}_1 x_0 + \bar{x}_2 x_1 + x_2 \bar{x}_1 \bar{x}_0$$

$$y_2 = \bar{x}_2 \bar{x}_1 x_0 + \bar{x}_2 x_1 + \bar{x}_2 x_1 x_0 + x_2 \bar{x}_1 \bar{x}_0$$

$$y_2 = \bar{x}_2 \bar{x}_1 x_0 + \bar{x}_2 x_1 + \bar{x}_2 x_1 x_0 + x_2 \bar{x}_1 \bar{x}_0$$

Simplification of y_2 Formula

$$y_2 = \bar{x}_2 \bar{x}_1 x_0 + \bar{x}_2 x_1 \bar{x}_0 + \bar{x}_2 x_1 x_0 + x_2 \bar{x}_1 \bar{x}_0$$

$$y_2 = \bar{x}_2 \bar{x}_1 x_0 + \bar{x}_2 x_1 + x_2 \bar{x}_1 \bar{x}_0$$

$$y_2 = \bar{x}_2 \bar{x}_1 x_0 + \bar{x}_2 x_1 + \bar{x}_2 x_1 x_0 + x_2 \bar{x}_1 \bar{x}_0$$

$$y_2 = \bar{x}_2 \bar{x}_1 x_0 + \bar{x}_2 x_1 + \bar{x}_2 x_1 x_0 + x_2 \bar{x}_1 \bar{x}_0$$

$$y_2 = \bar{x}_2 x_0 + \bar{x}_2 x_1 + x_2 \bar{x}_1 \bar{x}_0$$

$$y_2 = \bar{x}_2 x_0 + \bar{x}_2 x_1 + x_2 \bar{x}_1 \bar{x}_0$$

$$y_2 = \bar{x}_2 \bar{x}_1 x_0 + \bar{x}_2 x_1 \bar{x}_0 + \bar{x}_2 x_1 x_0 + x_2 \bar{x}_1 \bar{x}_0$$

$$y_2 = \bar{x}_2 \bar{x}_1 x_0 + \bar{x}_2 x_1 + x_2 \bar{x}_1 \bar{x}_0$$

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$$y_2 = \bar{x}_2 \bar{x}_1 x_0 + \bar{x}_2 x_1 + \bar{x}_2 x_1 x_0 + x_2 \bar{x}_1 \bar{x}_0$$

$$y_2 = \bar{x}_2 x_0 + \bar{x}_2 x_1 + x_2 \bar{x}_1 \bar{x}_0$$

Simplified 2's Complement Formulas

$$\mathbf{y}_0 = \mathbf{x}_0$$

$$\mathbf{y}_1 = \overline{\mathbf{x}}_1 \mathbf{x}_0 + \mathbf{x}_1 \overline{\mathbf{x}}_0$$

$$\mathbf{y}_2 = \overline{\mathbf{x}}_2 \mathbf{x}_0 + \overline{\mathbf{x}}_2 \mathbf{x}_1 + \mathbf{x}_2 \overline{\mathbf{x}}_1 \overline{\mathbf{x}}_0$$

Combinational Circuit Design

Combinational circuit

Output determined by input

Design process

1. Specify semantics

Black Box input and output (informal semantics)

Truth Table (formal semantics)

2. Truth table \rightarrow Boolean formula

3. Minimize boolean formula (optional)

Boolean algebra

Karnaugh maps

4. Boolean formula \rightarrow combinational circuit

Circuit: $y_0 = x_0$

x_0 $\text{--} \textcircled{\times 1} y_0$

$\textcircled{\times 1} y_1$

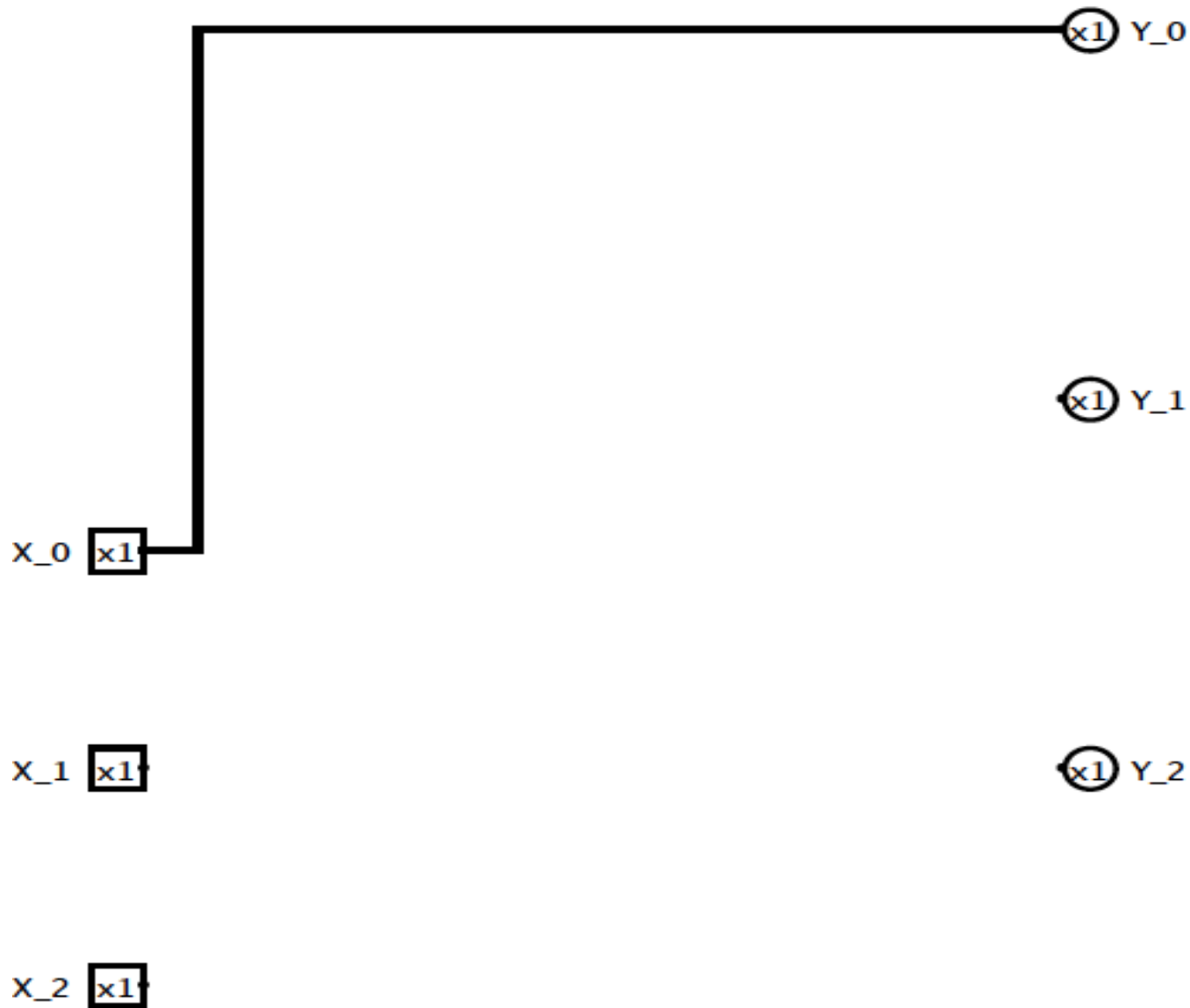
x_0 $\boxed{\times 1}$ --

x_1 $\boxed{\times 1}$

$\textcircled{\times 1} y_2$

x_2 $\boxed{\times 1}$

Circuit: $y_0 = x_0$



Circuit: $y_1 = \bar{x}_1 x_0 + x_1 \bar{x}_0$

$\oplus Y_0$

$\bar{x}_1 x_0 + x_1 \bar{x}_0 \oplus Y_1$

x_0 $\boxed{x1}$

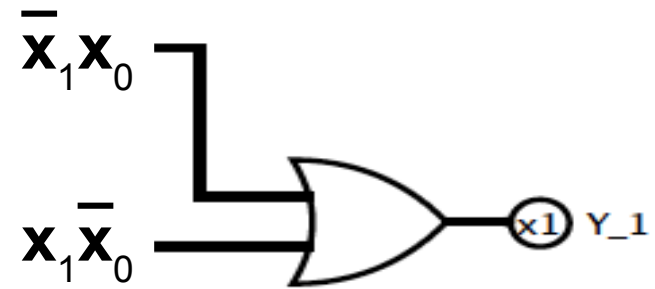
x_1 $\boxed{x1}$

$\oplus Y_2$

x_2 $\boxed{x1}$

Circuit: $y_1 = \bar{x}_1 x_0 + x_1 \bar{x}_0$

$\textcircled{x1} Y_0$



x_0 $\textcircled{x1}$

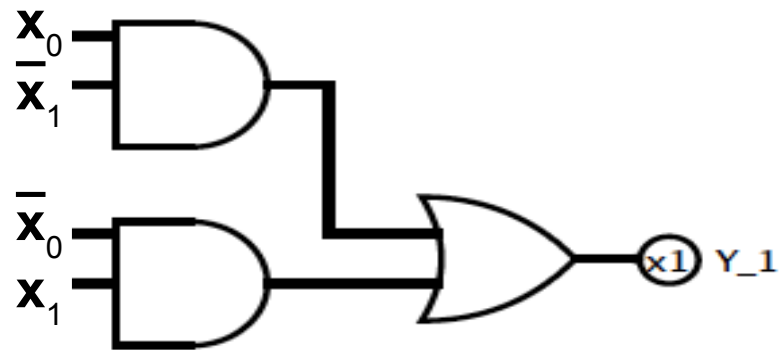
x_1 $\textcircled{x1}$

$\textcircled{x1} Y_2$

x_2 $\textcircled{x1}$

Circuit: $y_1 = \bar{x}_1 x_0 + x_1 \bar{x}_0$

$\textcircled{x1} Y_0$



x_0 $\textcircled{x1}$

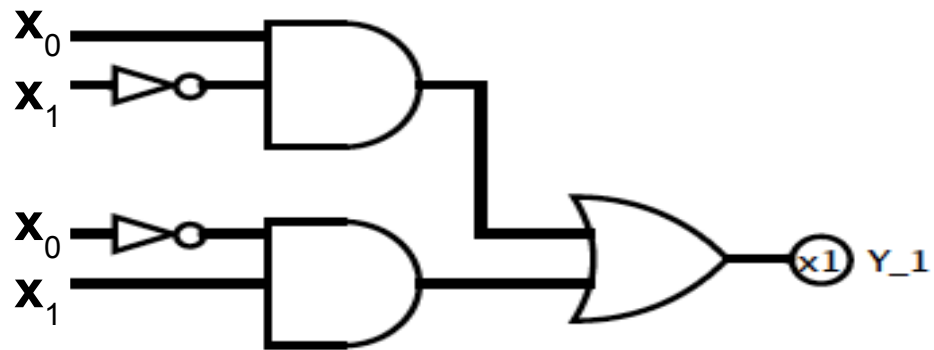
x_1 $\textcircled{x1}$

$\textcircled{x1} Y_2$

x_2 $\textcircled{x1}$

Circuit: $y_1 = \bar{x}_1 x_0 + x_1 \bar{x}_0$

$\textcircled{x1} Y_0$



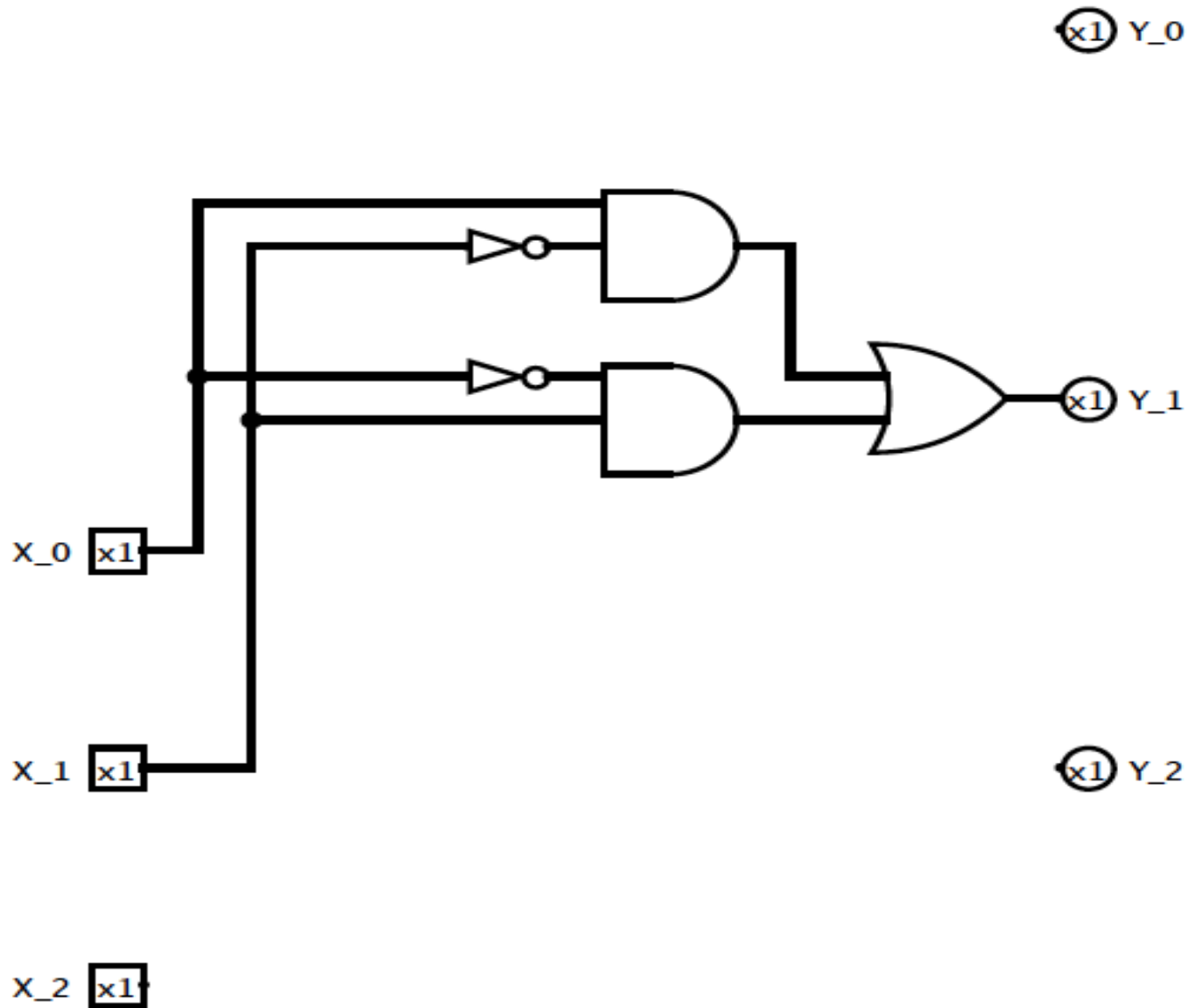
x_0 $\textcircled{x1}$

x_1 $\textcircled{x1}$

$\textcircled{x1} Y_2$

x_2 $\textcircled{x1}$

Circuit: $y_1 = \bar{x}_1 x_0 + x_1 \bar{x}_0$



Circuit: $y_2 = \bar{x}_2 x_0 + \bar{x}_2 x_1 + x_2 \bar{x}_1 \bar{x}_0$

$\oplus Y_0$

$\oplus Y_1$

x_0 $\boxed{x1}$

x_1 $\boxed{x1}$

x_2 $\boxed{x1}$

$\bar{x}_2 x_0 + \bar{x}_2 x_1 + x_2 \bar{x}_1 \bar{x}_0 \oplus Y_2$

Circuit: $y_2 = \bar{x}_2 x_0 + \bar{x}_2 x_1 + x_2 \bar{x}_1 \bar{x}_0$

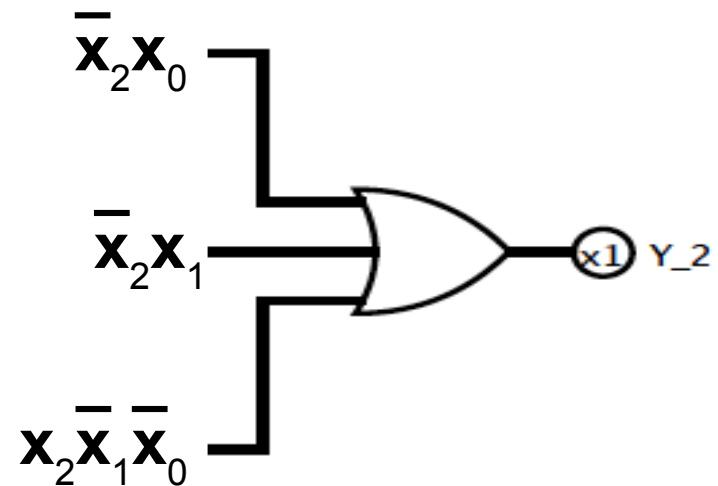
$\textcircled{x1} Y_0$

$\textcircled{x1} Y_1$

x_0 $\textcircled{x1}$

x_1 $\textcircled{x1}$

x_2 $\textcircled{x1}$



Circuit: $y_2 = \bar{x}_2 x_0 + \bar{x}_2 x_1 + x_2 \bar{x}_1 \bar{x}_0$

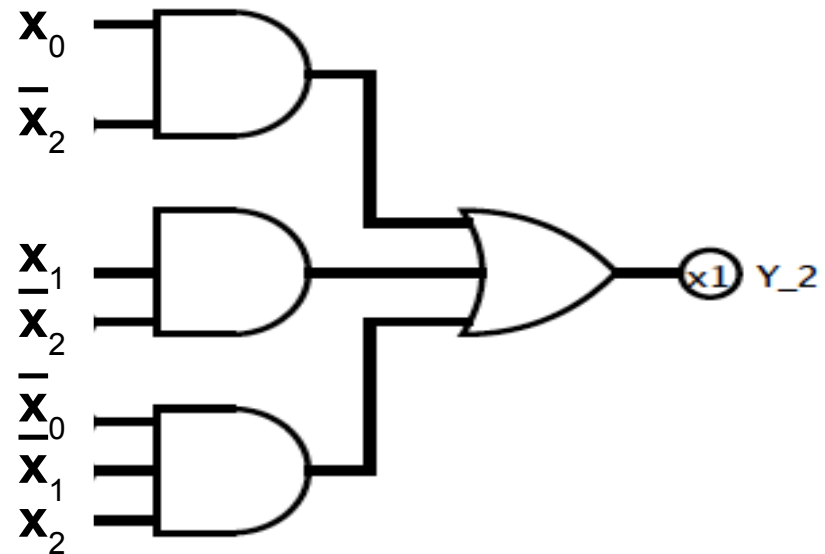
$\textcircled{x1} Y_0$

$\textcircled{x1} Y_1$

x_0 $\textcircled{x1}$

x_1 $\textcircled{x1}$

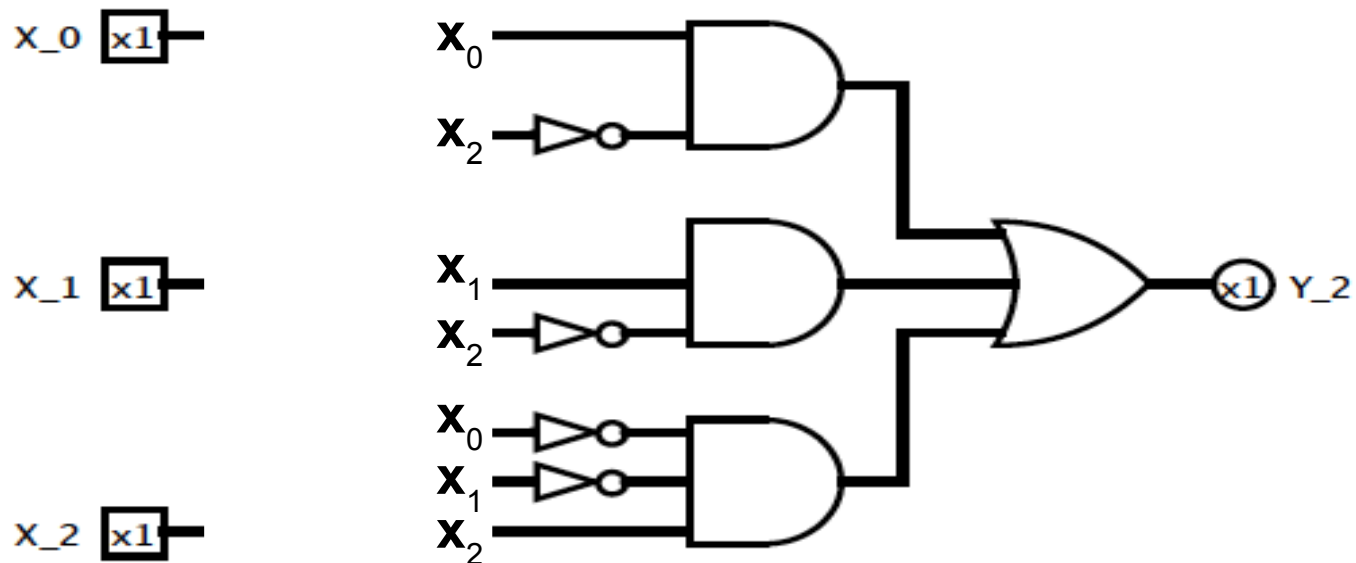
x_2 $\textcircled{x1}$



Circuit: $y_2 = \bar{x}_2 x_0 + \bar{x}_2 x_1 + x_2 \bar{x}_1 \bar{x}_0$

(x1) Y_0

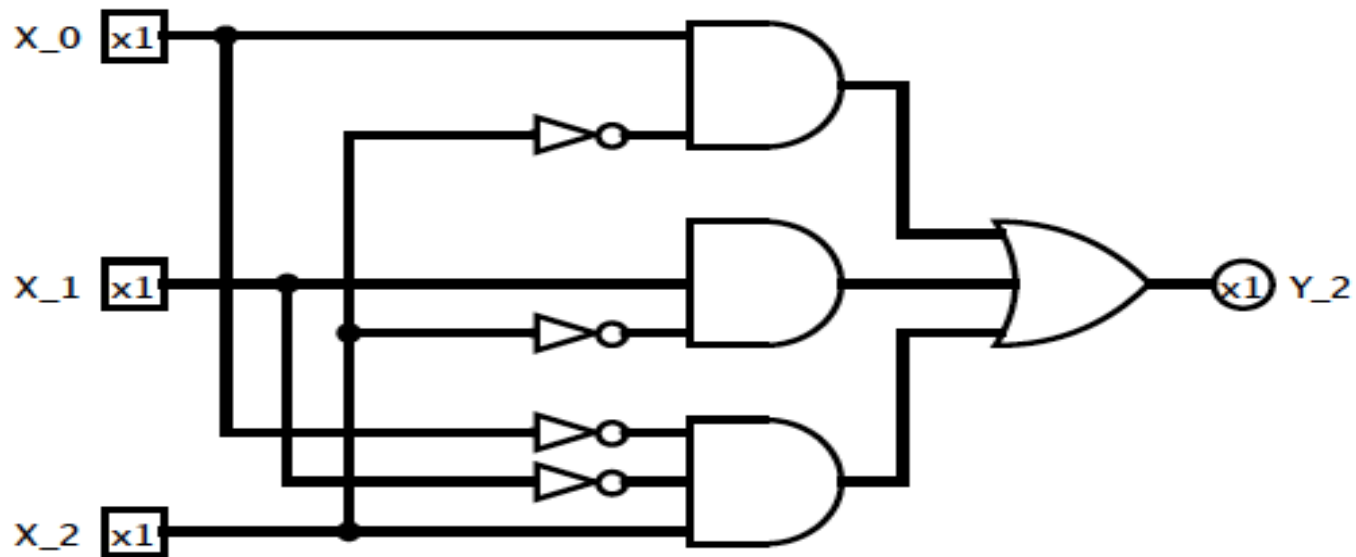
(x1) Y_1



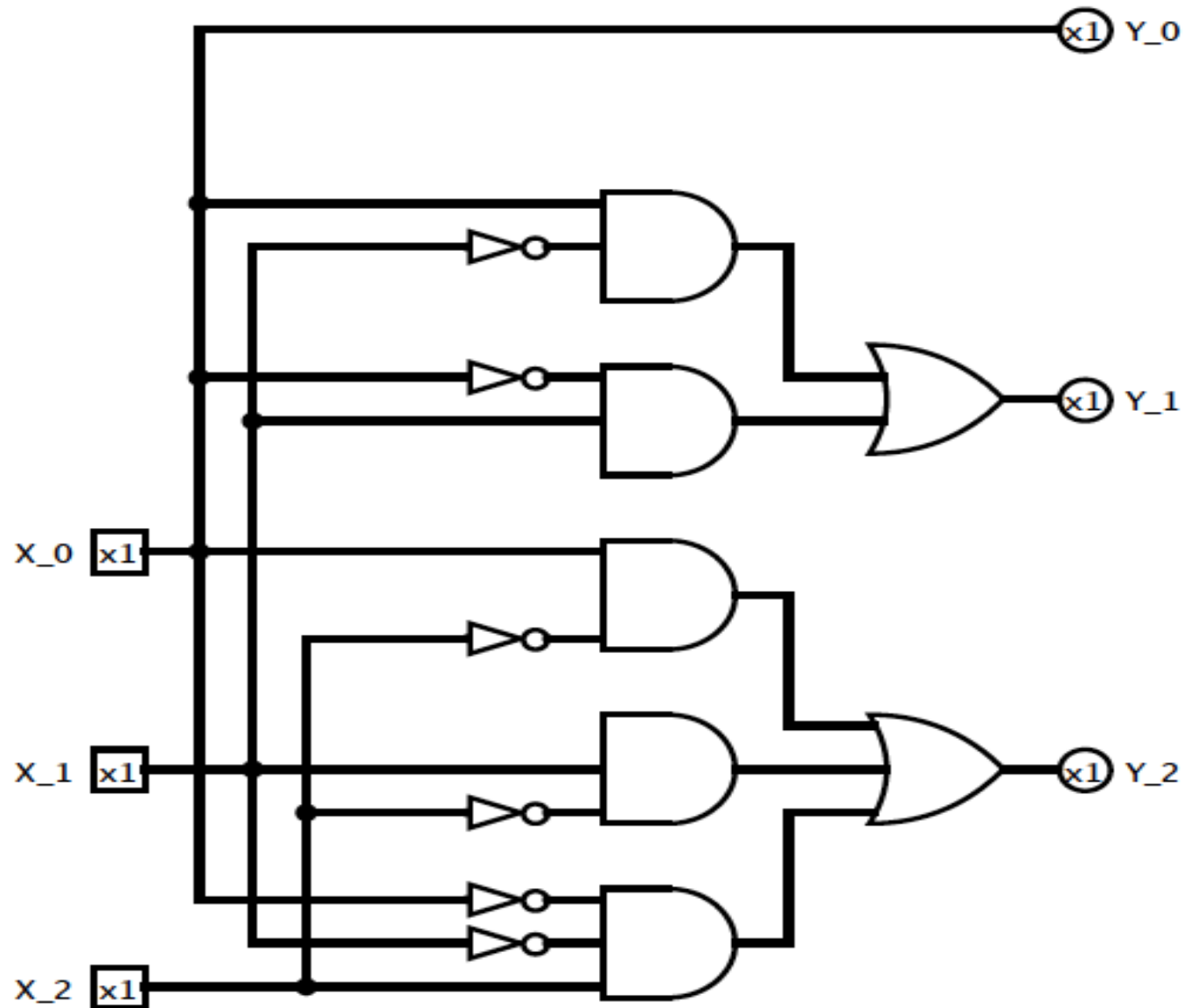
Circuit: $y_2 = \bar{x}_2 x_0 + \bar{x}_2 x_1 + x_2 \bar{x}_1 \bar{x}_0$

$\textcircled{x1} Y_0$

$\textcircled{x1} Y_1$



Two's Complement Circuits



Combinational Circuit Design

Combinational circuit

Output determined by input

Design process

1. Specify semantics

Black Box: *input* and *output* (informal semantics)

Truth Table (formal semantics)

2. Truth table \rightarrow Boolean formula

3. Minimize boolean formula (optional)

Boolean algebra

Karnaugh maps

4. Boolean formula \rightarrow combinational circuit

Inverters, Decoders, Multiplexer

Inverter: select data input or its negation

1 data input

1 selector input

1 output

Decoder: select unique output to be 1 (true)

N selector inputs

2^N outputs

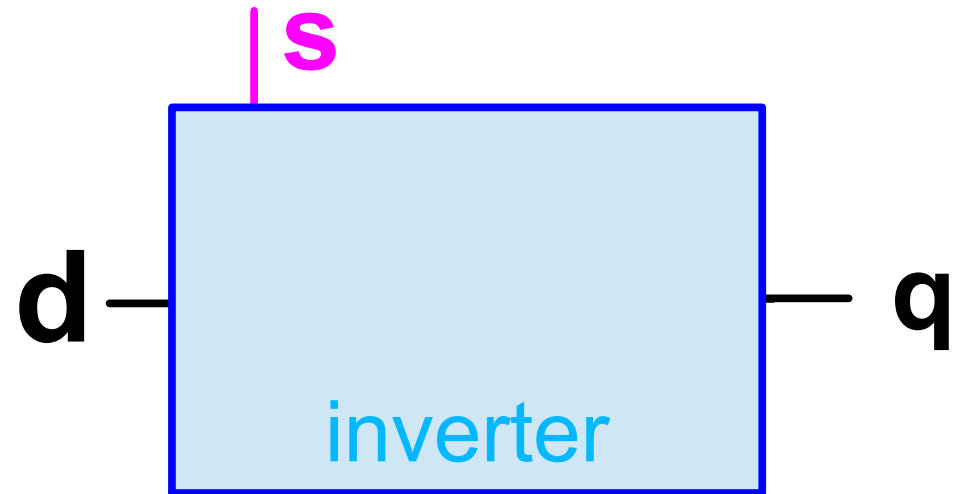
Multiplexer: select unique data input to be output

2^N data inputs

N selector inputs

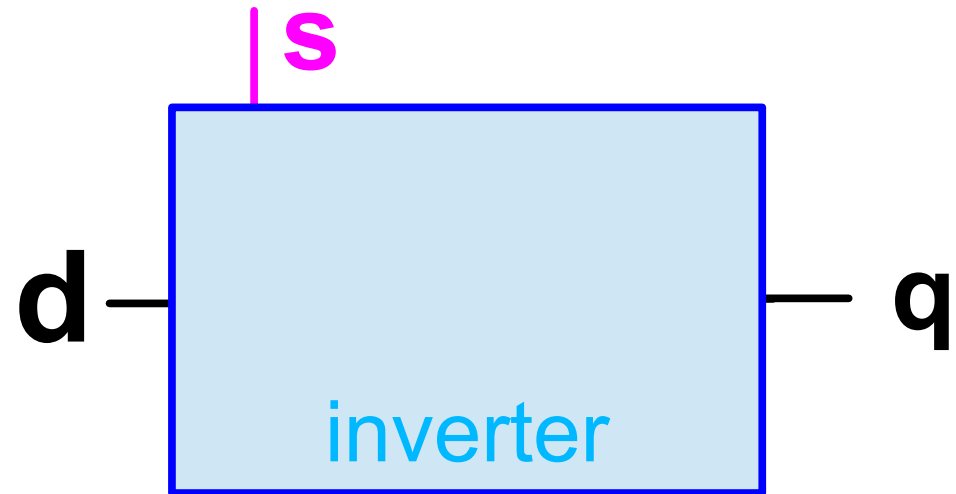
1 output

Inverter Black Box



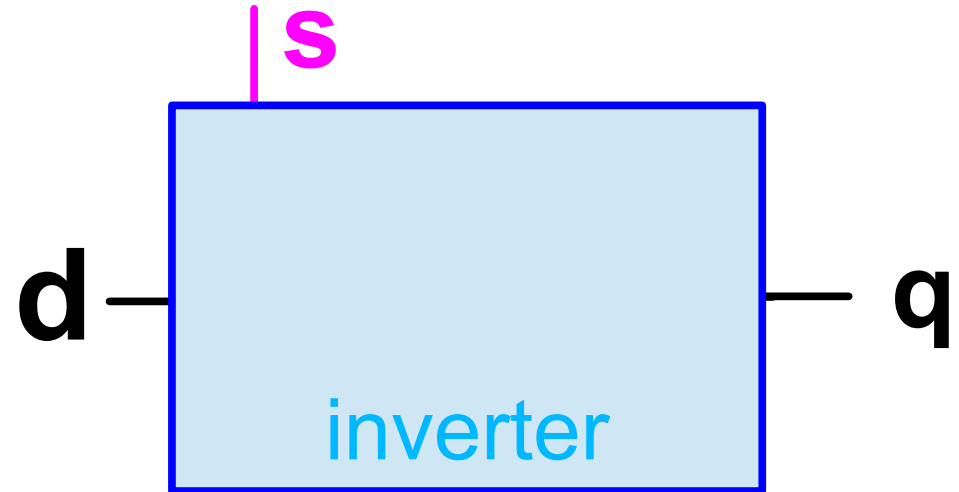
Inverter Truth Table

s	d	q
0	0	0
0	1	1
1	0	1
1	1	0



Inverter Formula

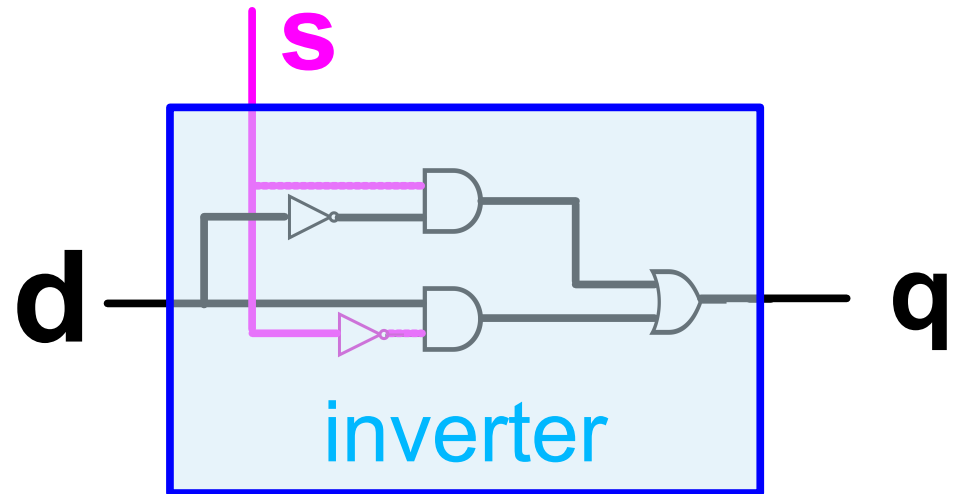
s	d	q
0	0	0
0	1	1
1	0	1
1	1	0



$$q = \bar{s}d + s\bar{d}$$

Inverter Circuit

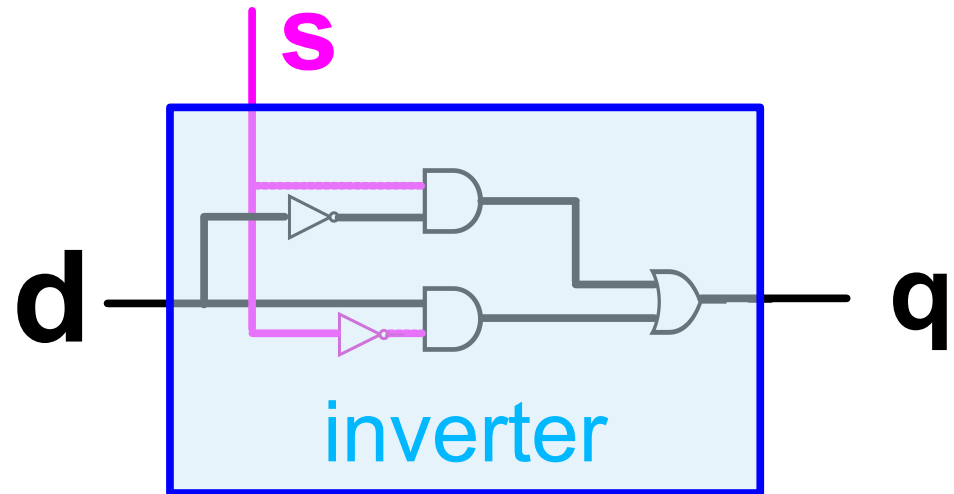
s	d	q
0	0	0
0	1	1
1	0	1
1	1	0



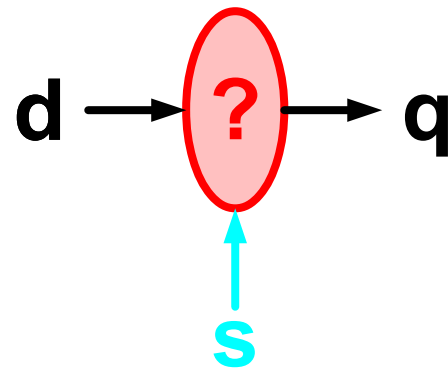
$$q = \bar{s}d + s\bar{d}$$

Inverter Component Icon

s	d	q
0	0	0
0	1	1
1	0	1
1	1	0



$$q = \bar{s}d + s\bar{d}$$



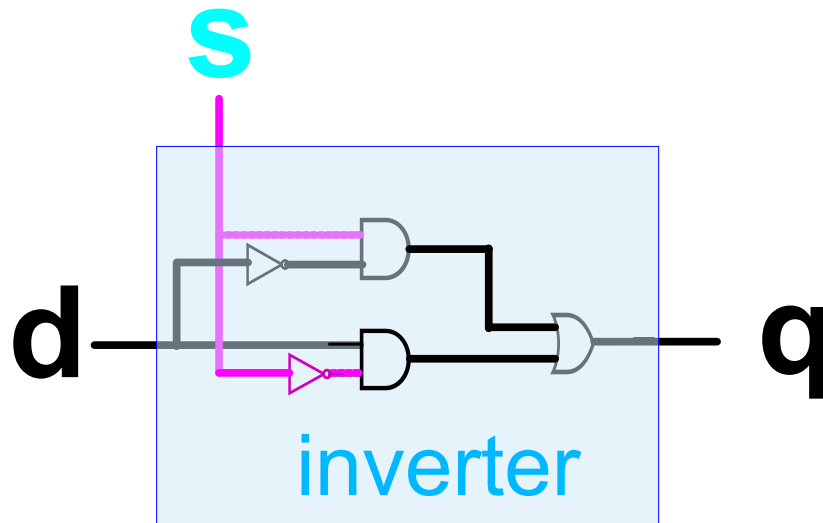
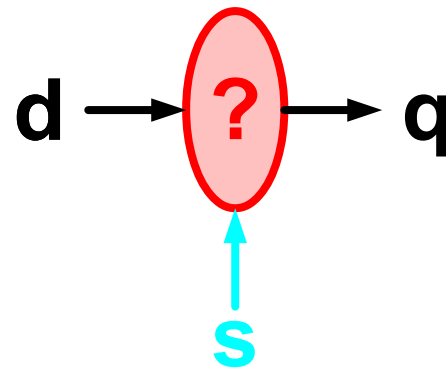
Inverter Summary

Inverter: select data input or its negation

1 data input

1 selector input

1 output



N-Bit Decoder

Each different combination of N input bits uniquely specifies one of 2^N outputs. An output is **1** if and only if the corresponding input combination is active (true). For any input, exactly one output is **1**.

N-Bit Decoder Truth Table:

- N input (**selector**) columns

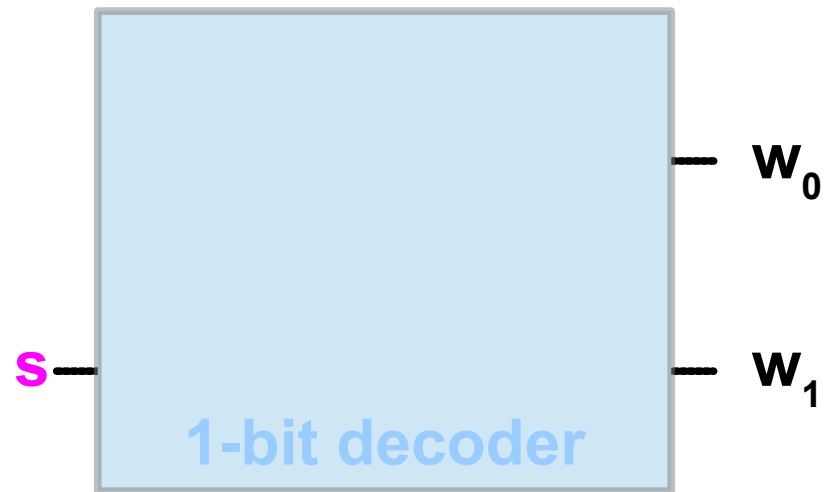
- 2^N output columns

- 2^N rows

- Exactly one 1 in each output column

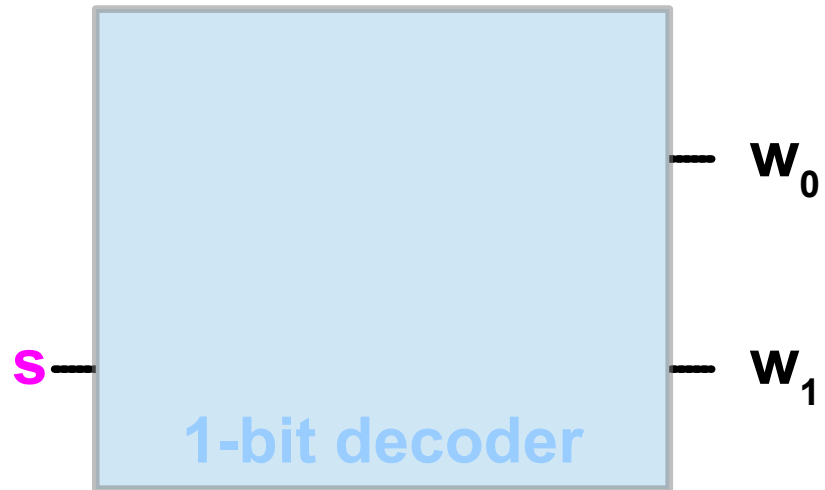
- Exactly one 1 in each output row

1-Bit Decoder Black Box



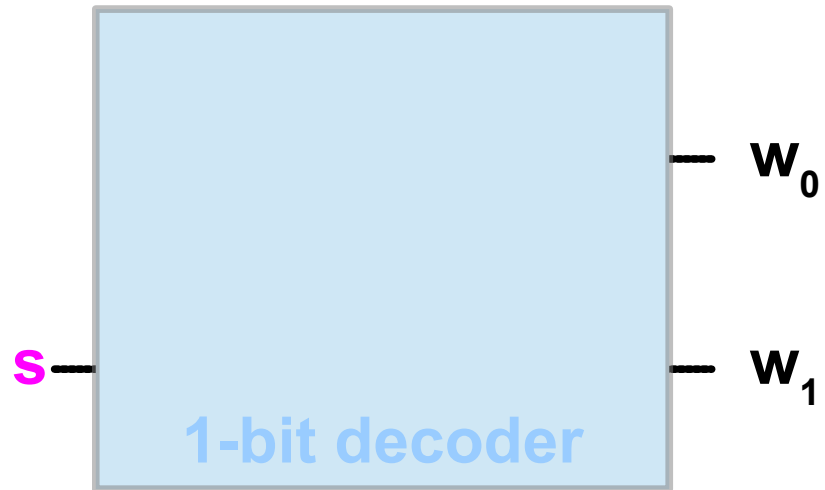
1-Bit Decoder Truth Table

s	w₀	w₁
0	1	0
1	0	1



1-Bit Decoder Formulas

s	w₀	w₁
0	1	0
1	0	1

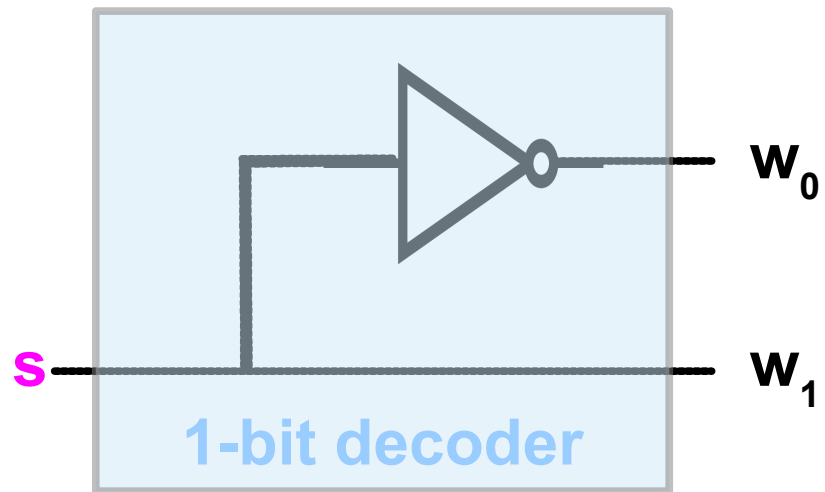


$$w_0 = \overline{s}$$

$$w_1 = s$$

1-Bit Decoder Circuit

s	w₀	w₁
0	1	0
1	0	1

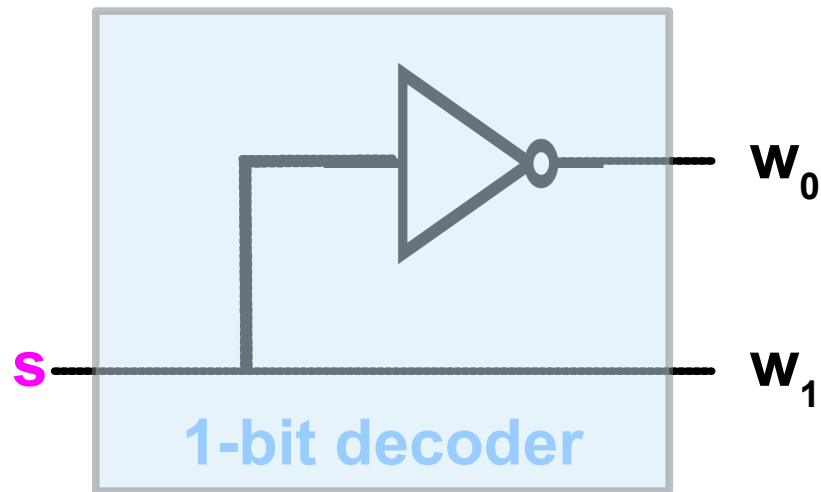


$$w_0 = \overline{s}$$

$$w_1 = s$$

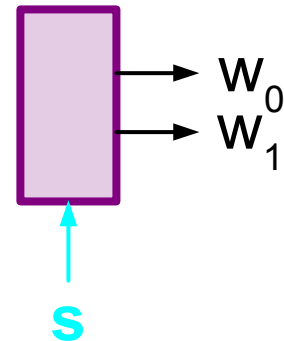
1-Bit Decoder Component Icon

s	w₀	w₁
0	1	0
1	0	1

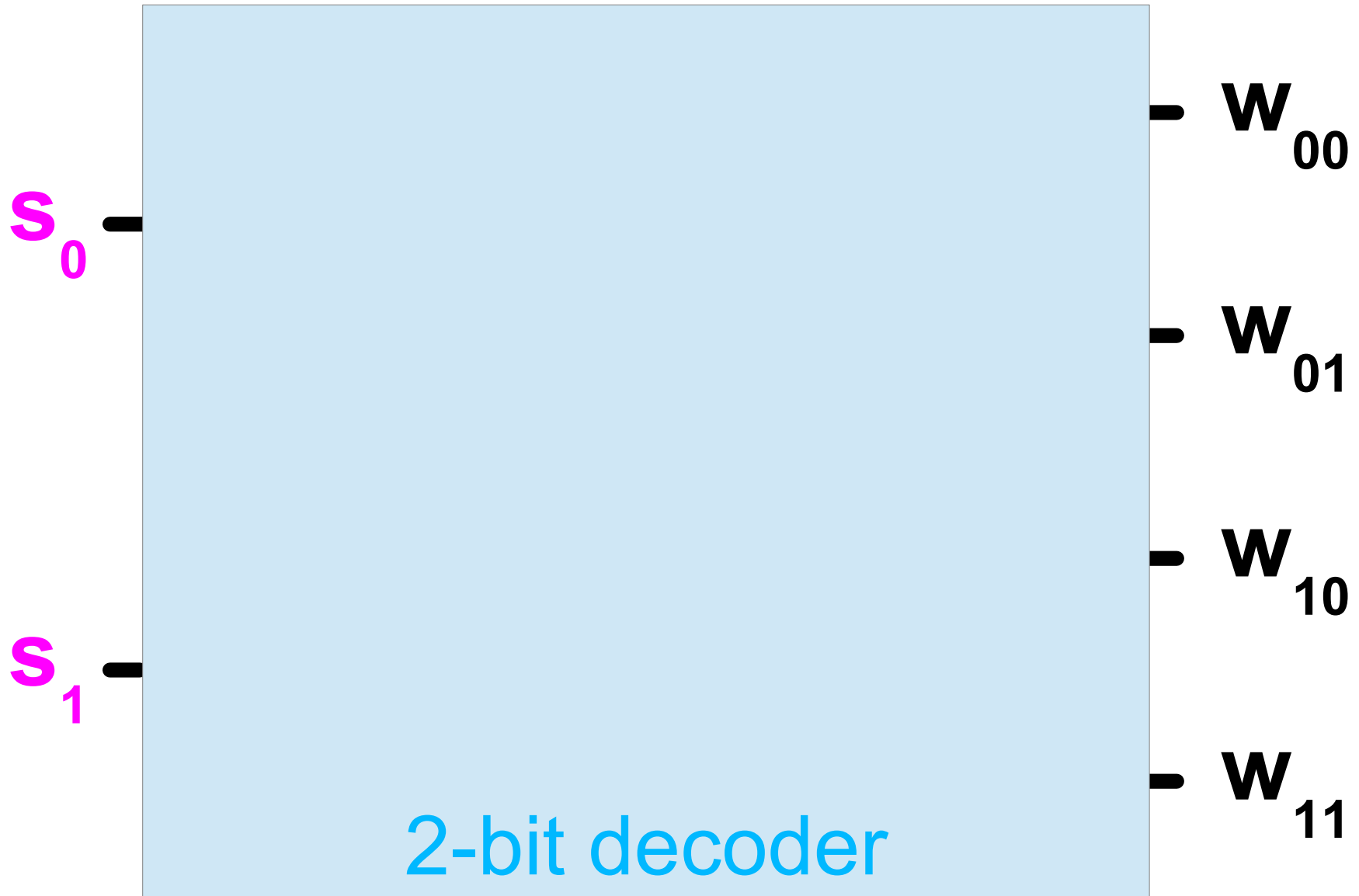


$$w_0 = \overline{s}$$

$$w_1 = s$$



2-Bit Decoder



2-bit Decoder Truth Table

#	s_1	s_0	w_{00}	w_{01}	w_{10}	w_{11}
0	0	0	1	0	0	0
1	0	1	0	1	0	0
2	1	0	0	0	1	0
3	1	1	0	0	0	1

2-bit Decoder Formulas

#	s_1	s_0	w_{00}	w_{01}	w_{10}	w_{11}
0	0	0	1	0	0	0
1	0	1	0	1	0	0
2	1	0	0	0	1	0
3	1	1	0	0	0	1

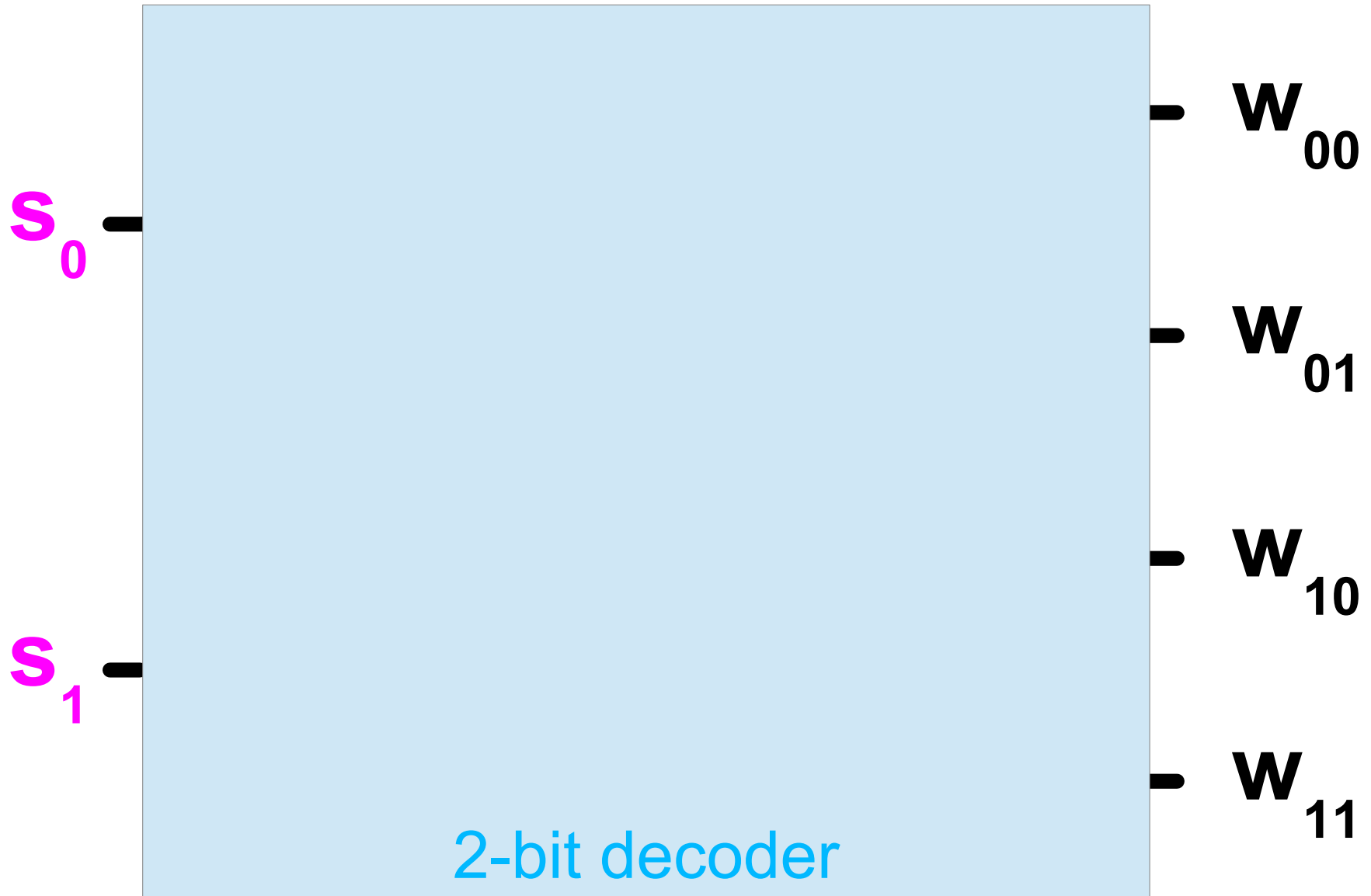
$$w_{11} = s_1 \overline{s_0}$$

$$w_{10} = \overline{s_1} s_0$$

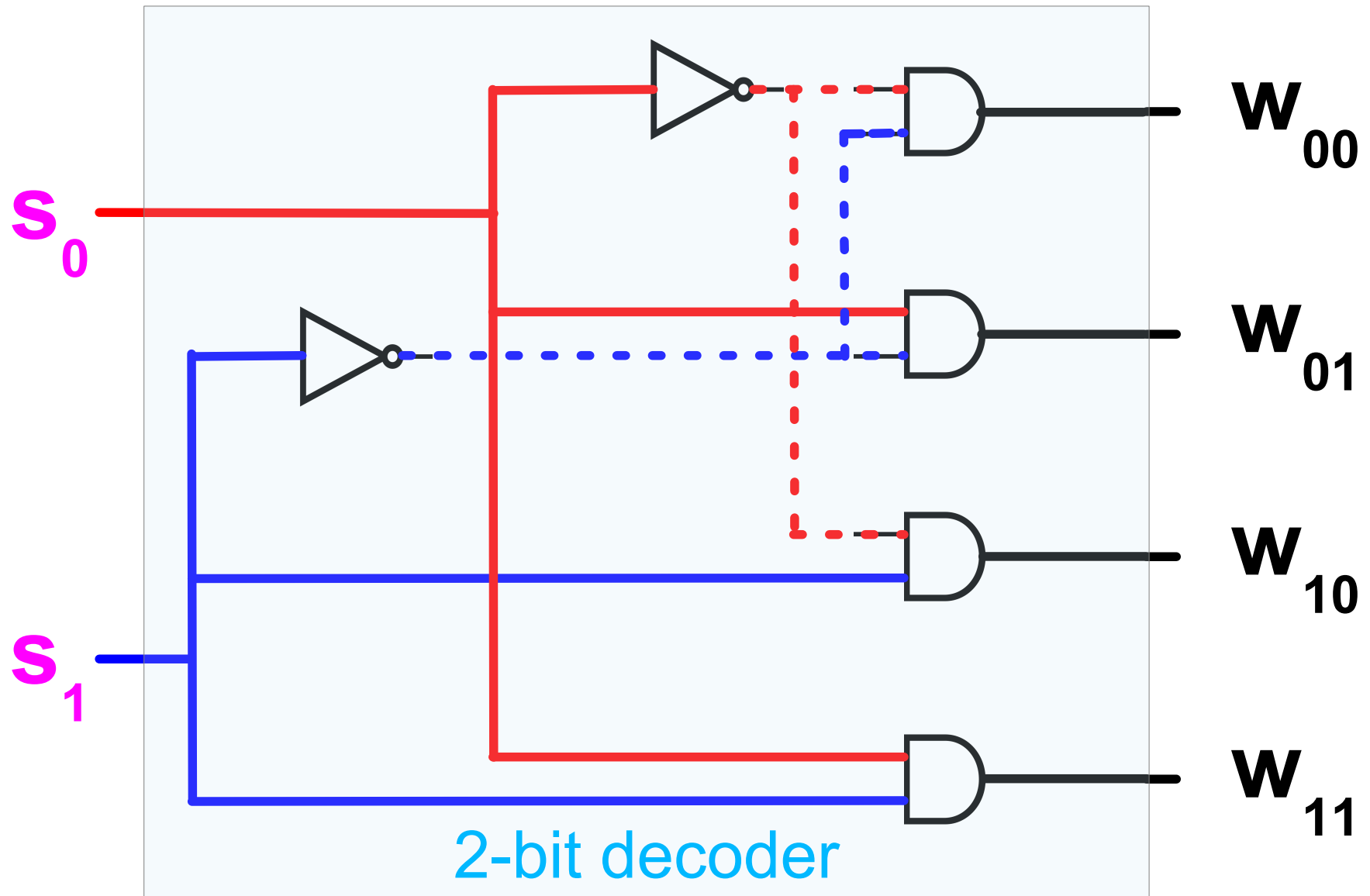
$$w_{01} = \overline{s_1} \overline{s_0}$$

$$w_{00} = s_1 s_0$$

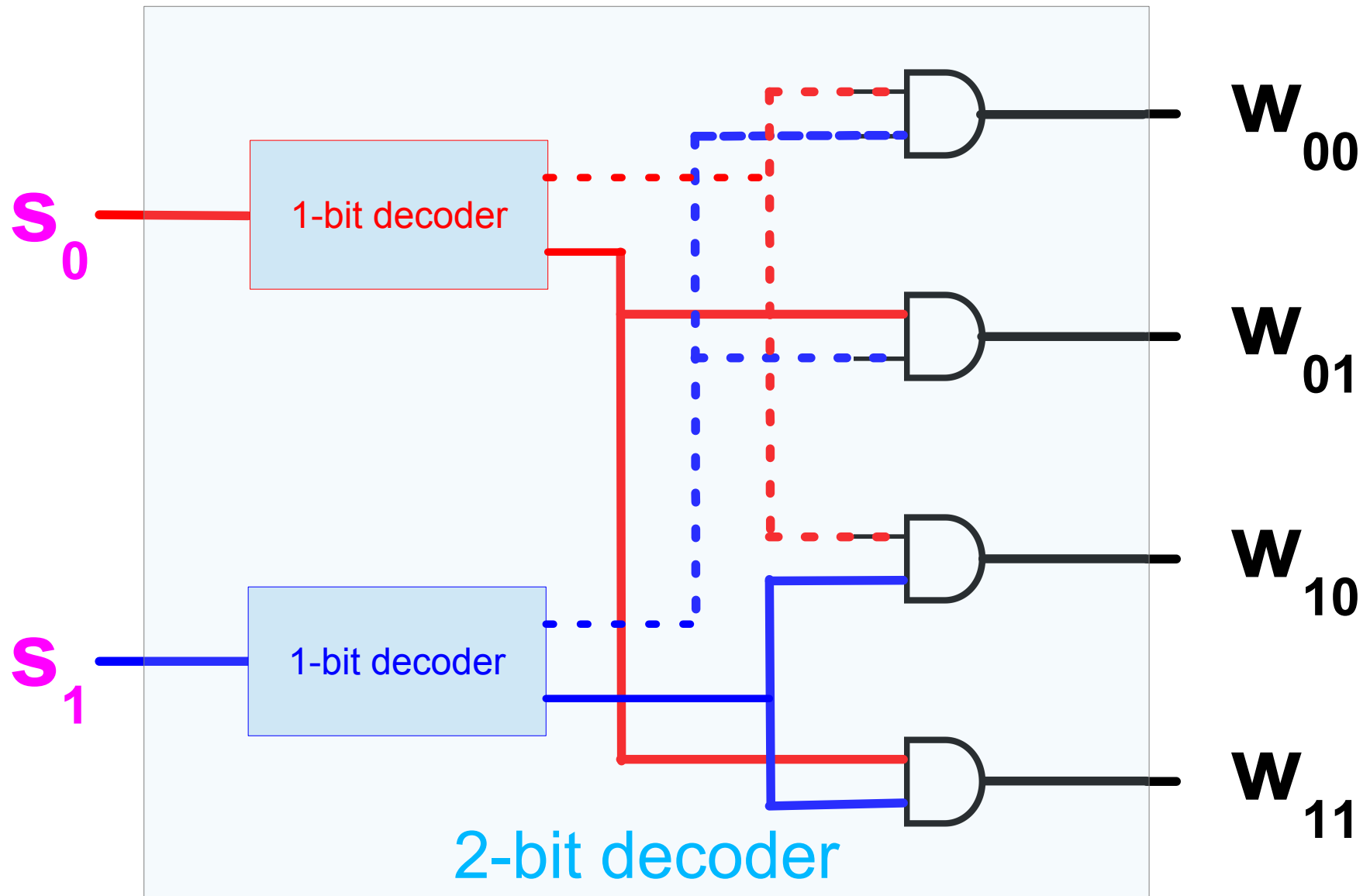
2-Bit Decoder Circuit



2-Bit Decoder Circuit



2-Bit Decoder Circuit

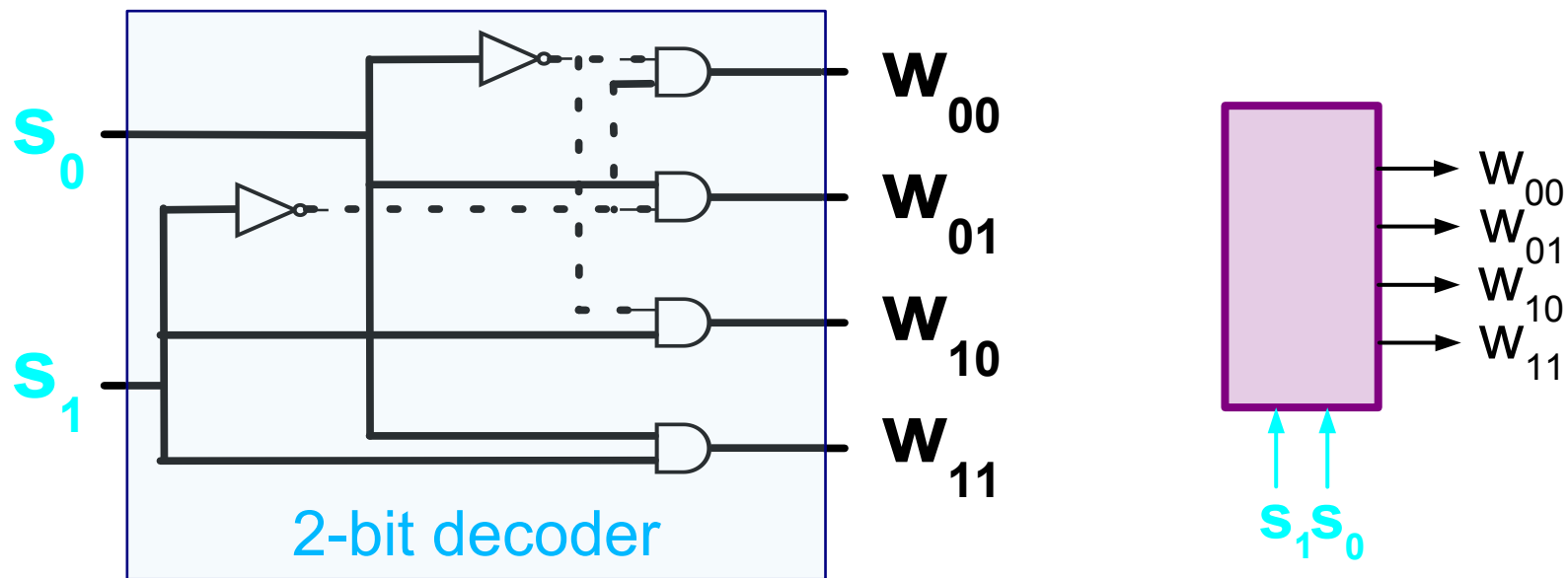


Decoder Summary

Decoder: *select* unique output to be 1 (true)

N **selector** inputs

2^N outputs



M-way Multiplexer ($M \equiv 2^N$)

N ($\lg M$) *selector* input bits

choose one of 2^N (M) *data* input bitss to output

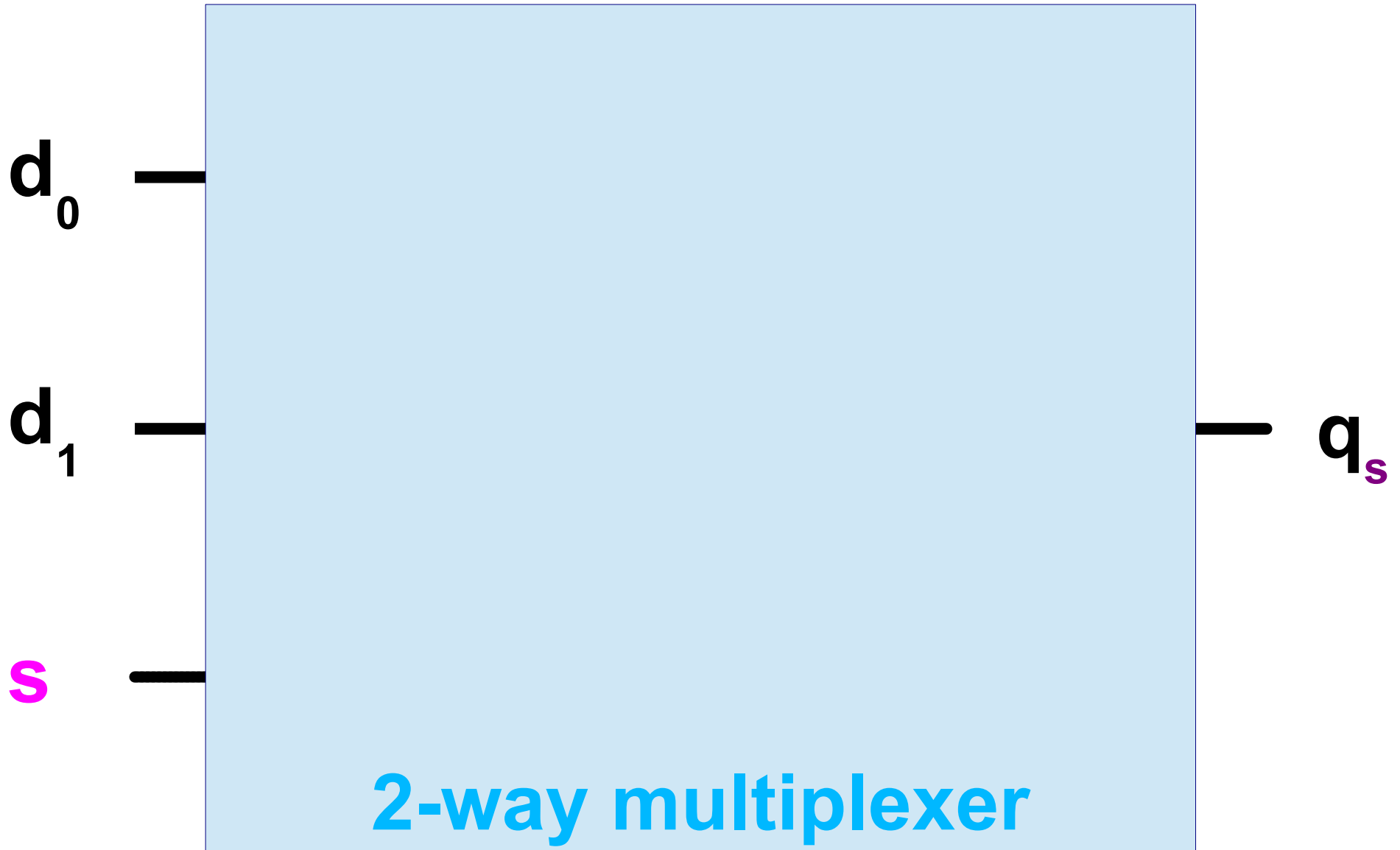
2^N -way multiplexer truth table:

2^N data input columns (\mathbf{d}_i $0 \leq i < 2^N$)

N selector input columns (\mathbf{s}_j $0 \leq j < N$, $0 \leq \mathbf{s} < 2^N$)

1 output column ($\mathbf{x} = \mathbf{d}_s$)

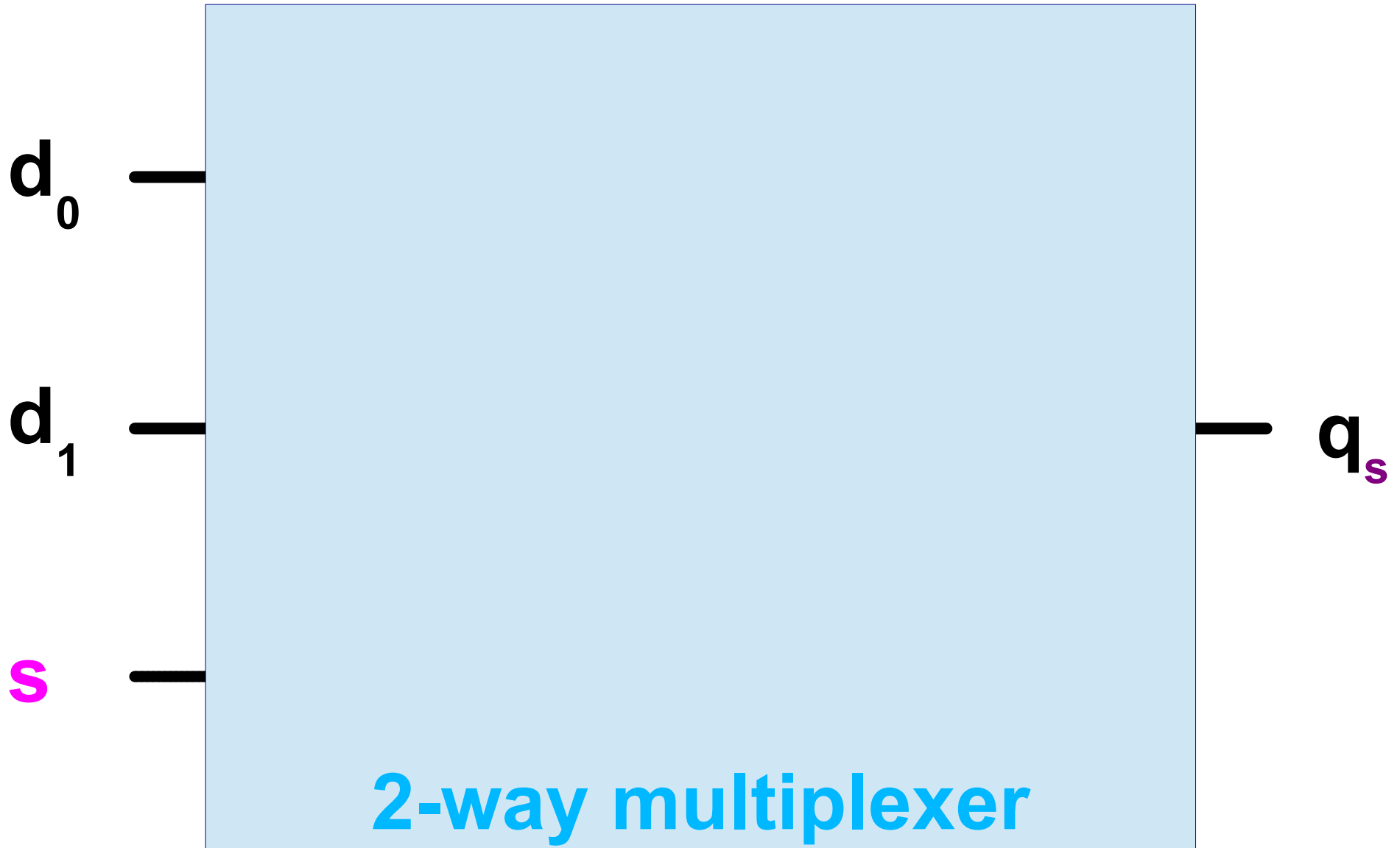
2-Way Multiplexer



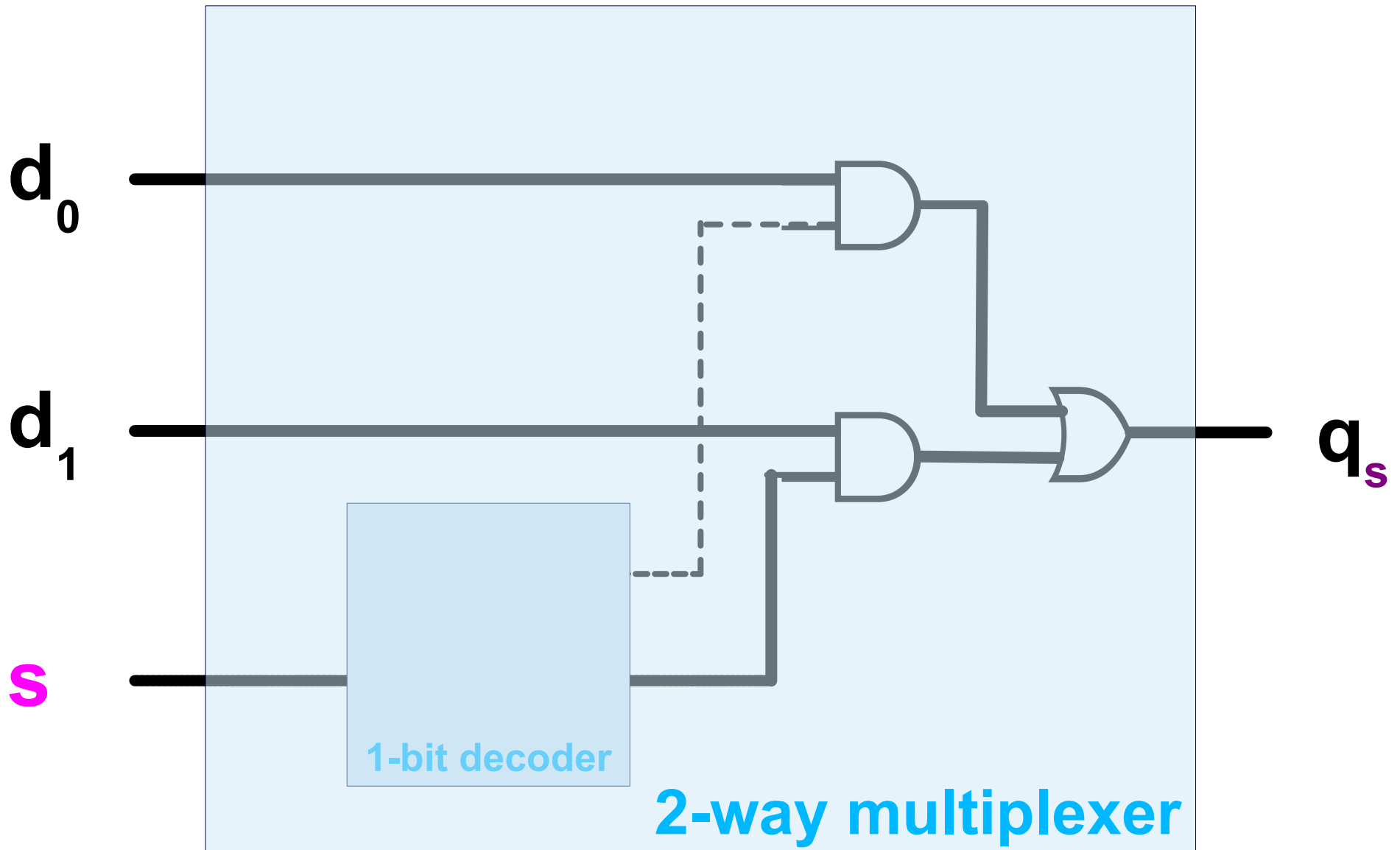
2-Way Multiplexer

#	s	d ₁	d ₀	q
0	0	0	0	0
1	0	0	1	1
2	0	1	0	0
3	0	1	1	1
4	1	0	0	0
5	1	0	1	0
6	1	1	0	1
7	1	1	1	1

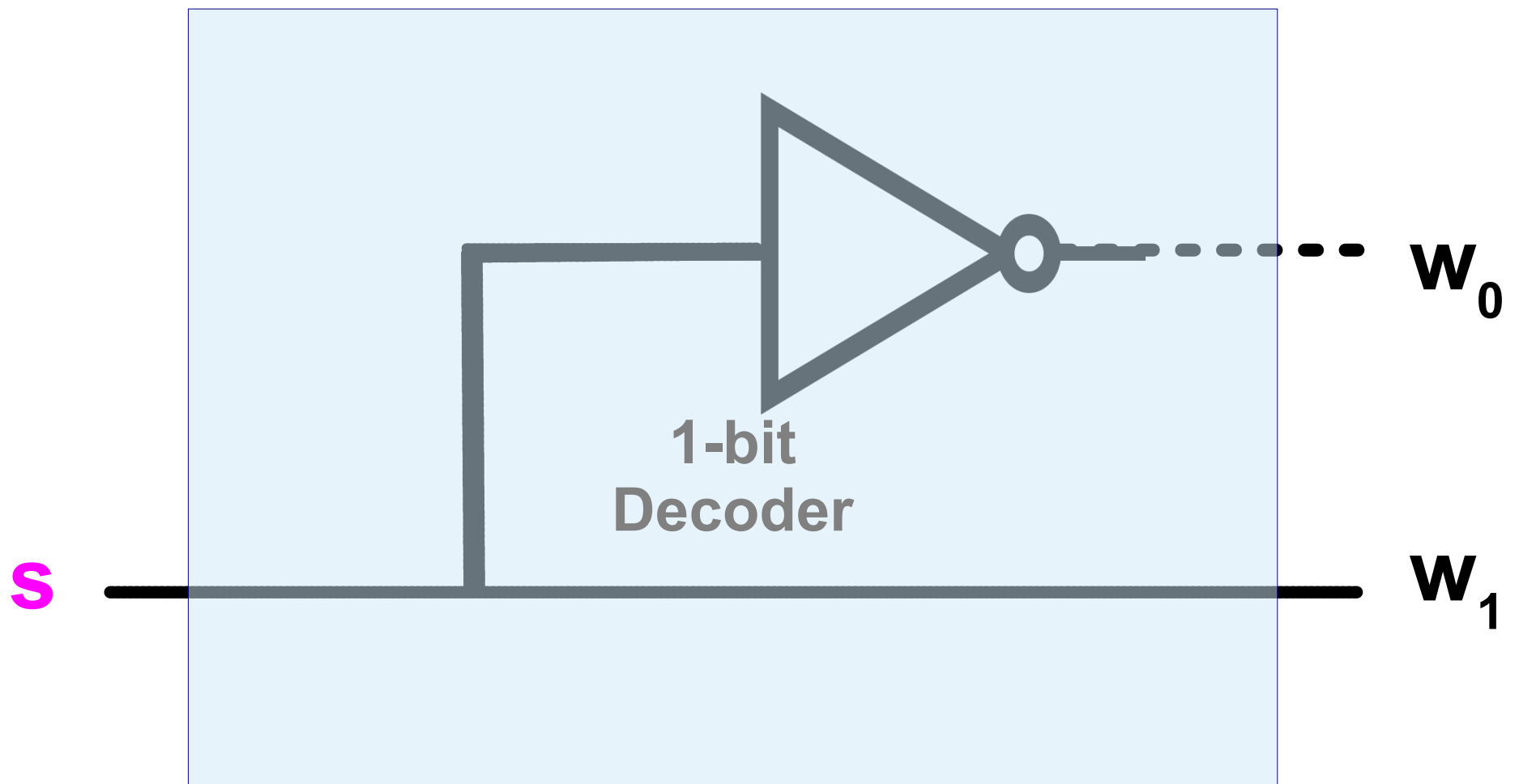
2-Way Multiplexer



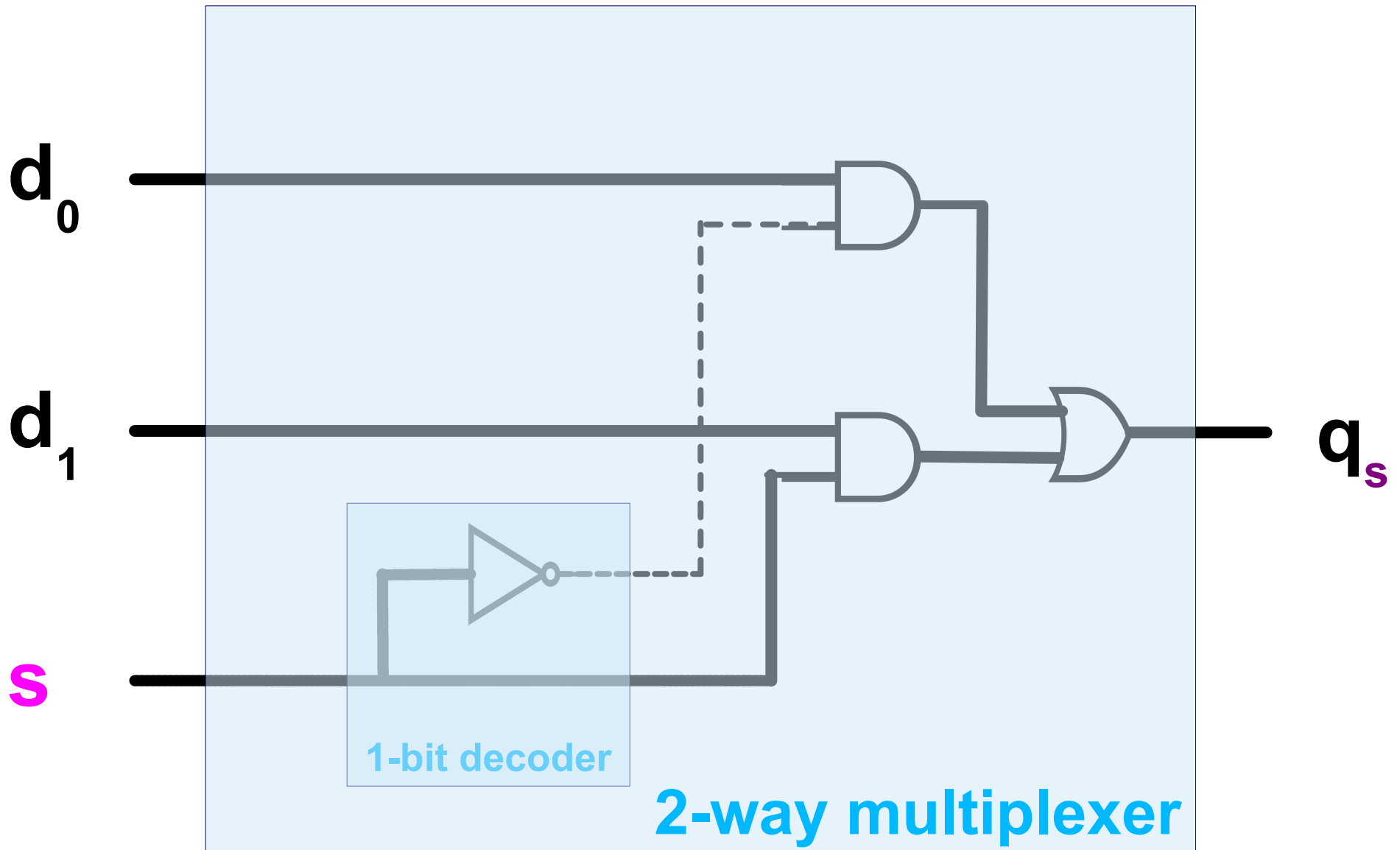
2-Way Multiplexer



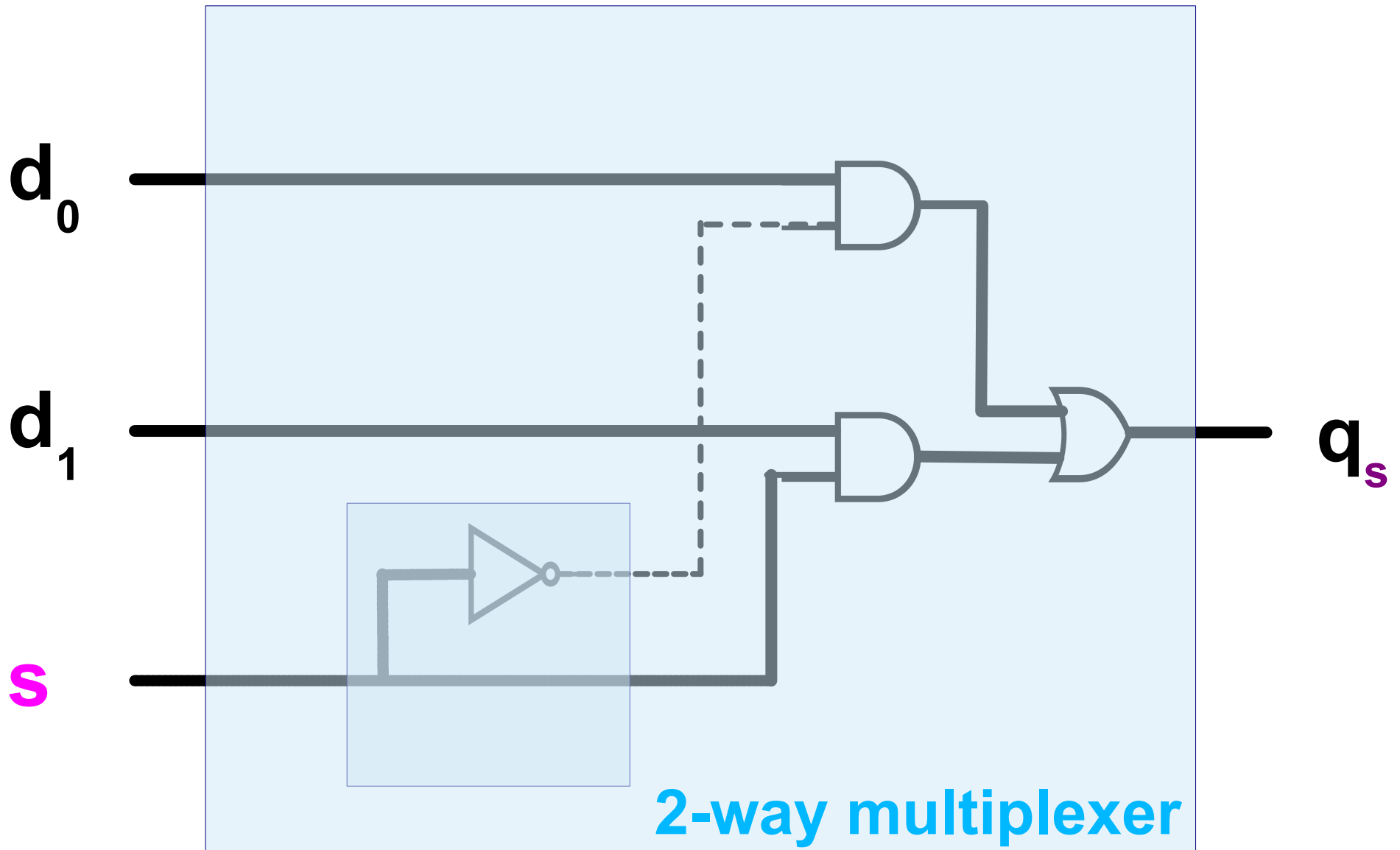
1-Bit Decoder



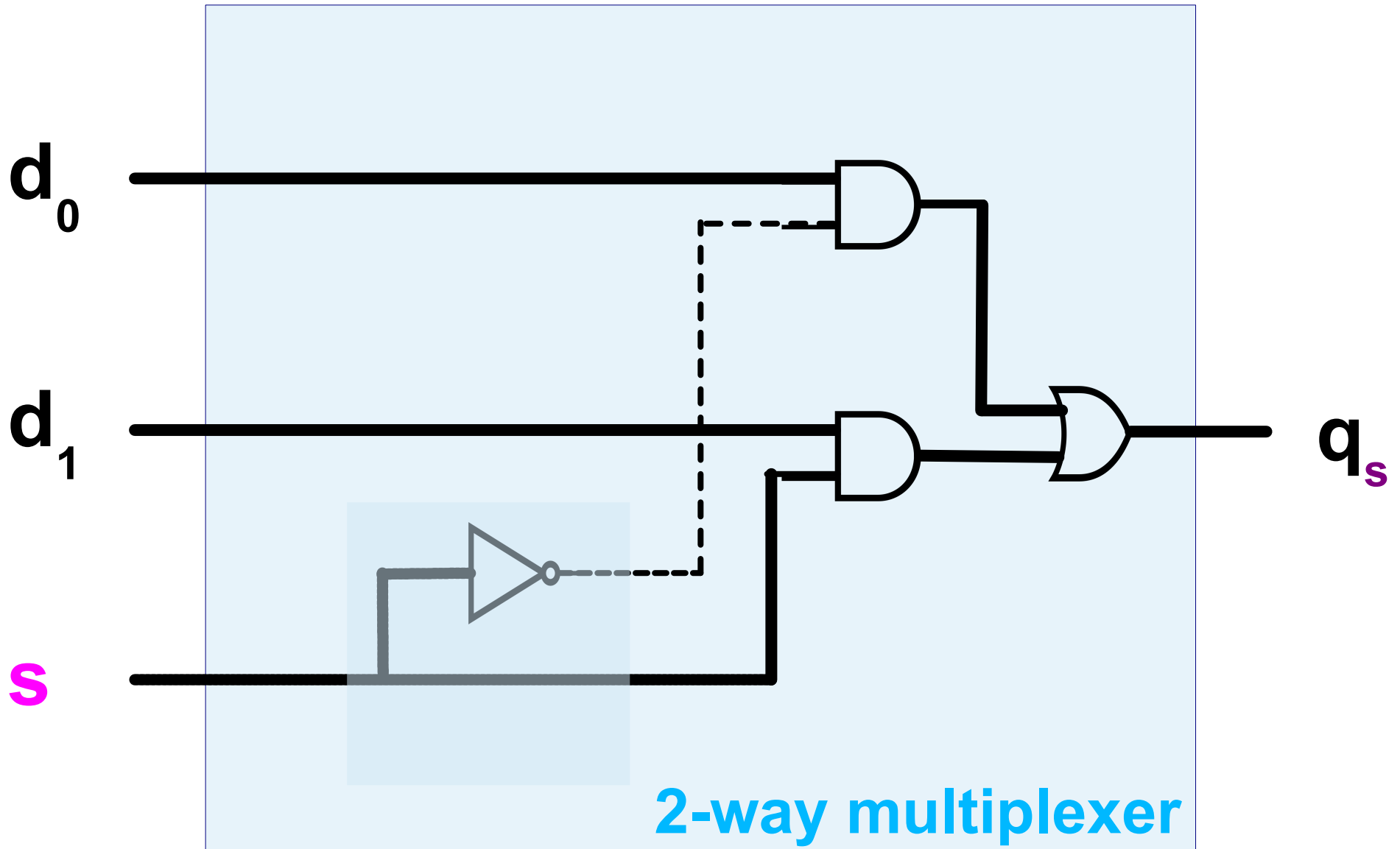
2-Way Multiplexer



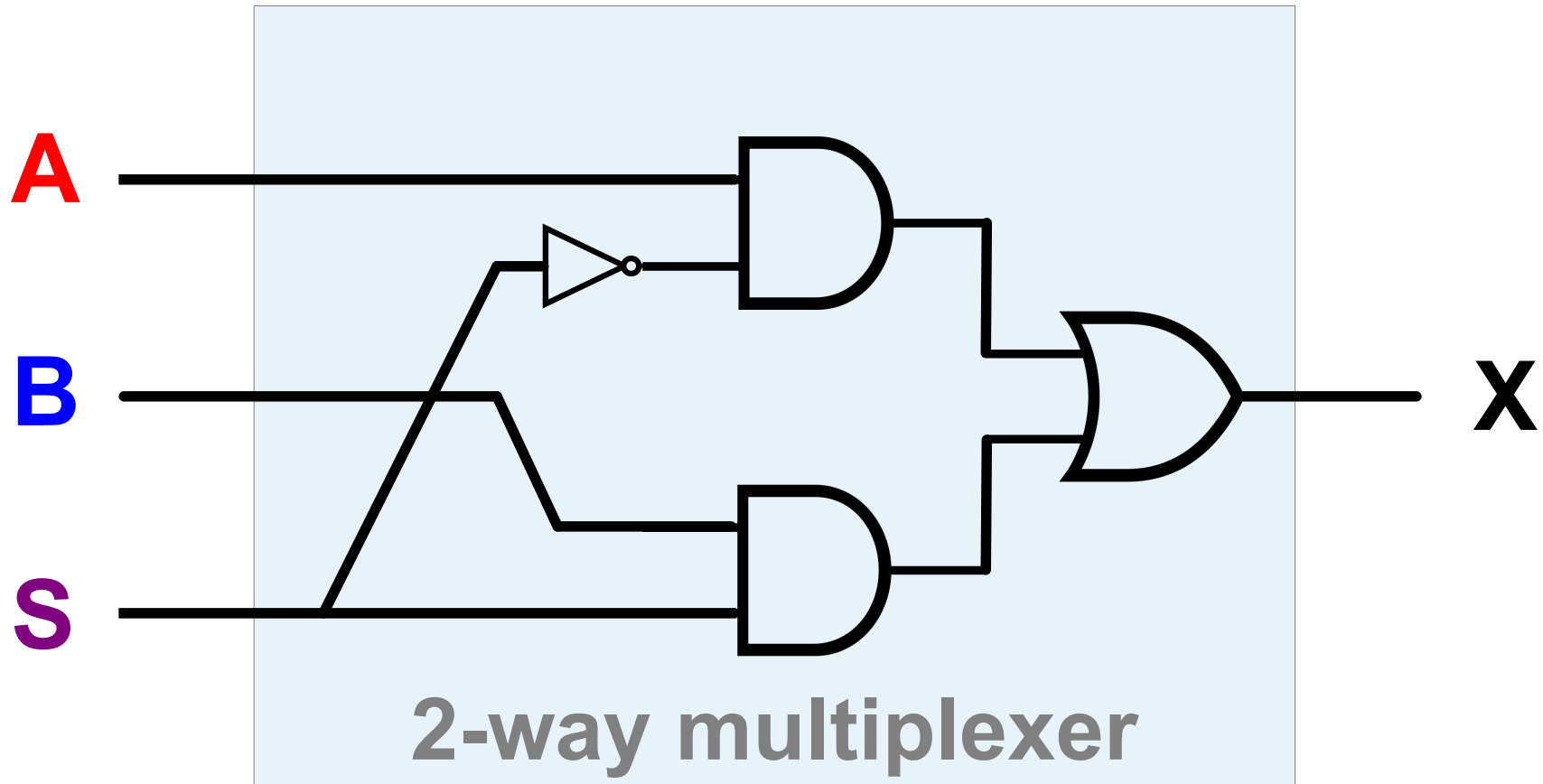
2-Way Multiplexer



2-Way Multiplexer



Two Way Multiplexer Circuit



$$X = \bar{S}A + SB$$

4-Way Multiplexer Black Box



4-Way Multiplexer

s_1	s_0	d_{11}	d_{10}	d_{01}	d_{00}	q
0	0	✓	✓	✓	0	0
0	0	✓	✓	✓	1	1
0	1	✓	✓	0	✓	0
0	1	✓	✓	1	✓	1
1	0	✓	0	✓	✓	0
1	0	✓	1	✓	✓	1
1	1	0	✓	✓	✓	0
1	1	1	✓	✓	✓	1

4-Way Multiplexer

s_1	s_0	d_{11}	d_{10}	d_{01}	d_{00}	q
0	0	✓	✓	✓	0	0
0	0	✓	✓	✓	1	1
0	1	✓	✓	0	✓	0
0	1	✓	✓	1	✓	1
1	0	✓	0	✓	✓	0
1	0	✓	1	✓	✓	1
1	1	0	✓	✓	✓	0
1	1	1	✓	✓	✓	1

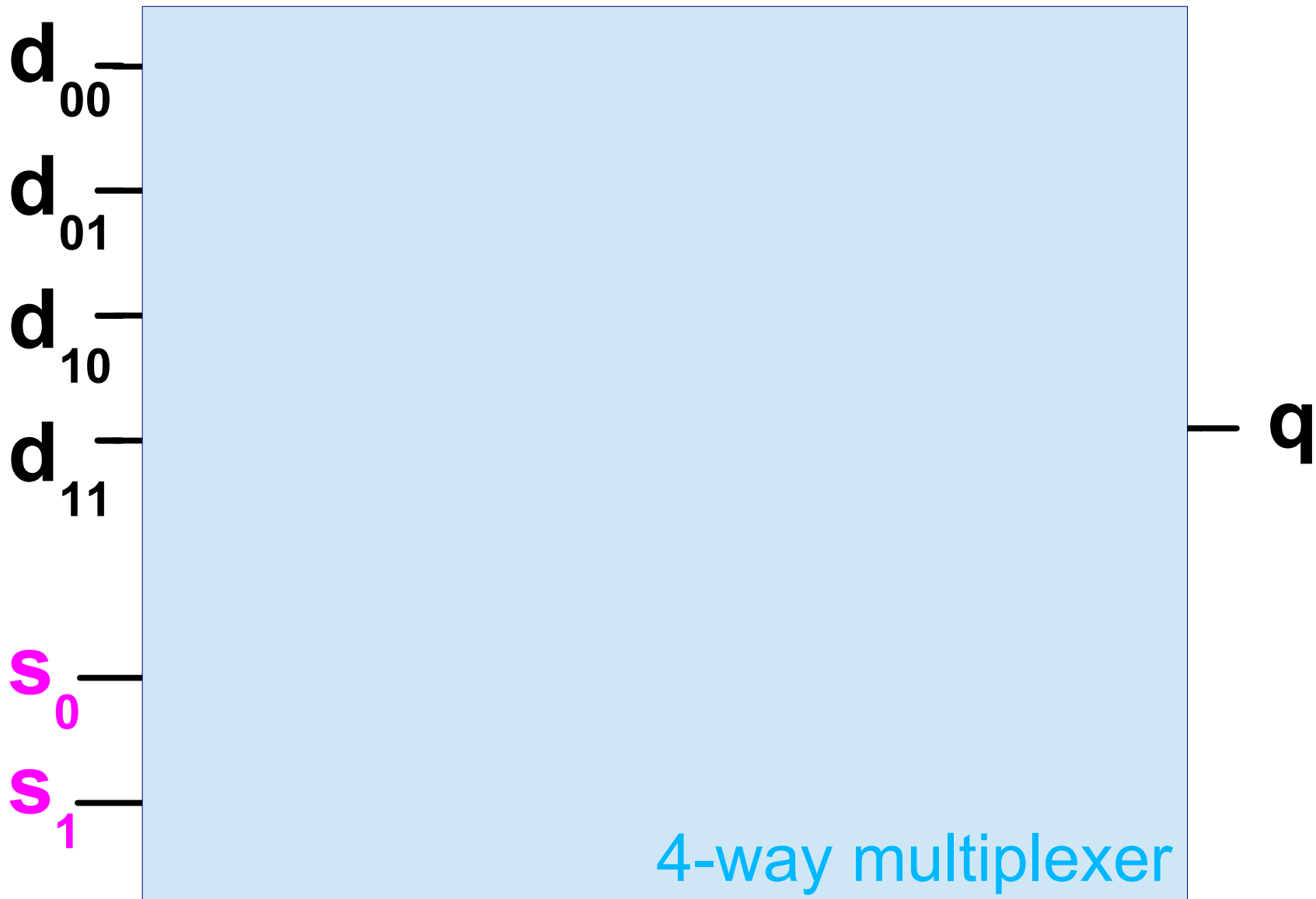
$$\bar{s}_1 \bar{s}_0 d_{00}$$

$$+ \bar{s}_1 s_0 d_{01}$$

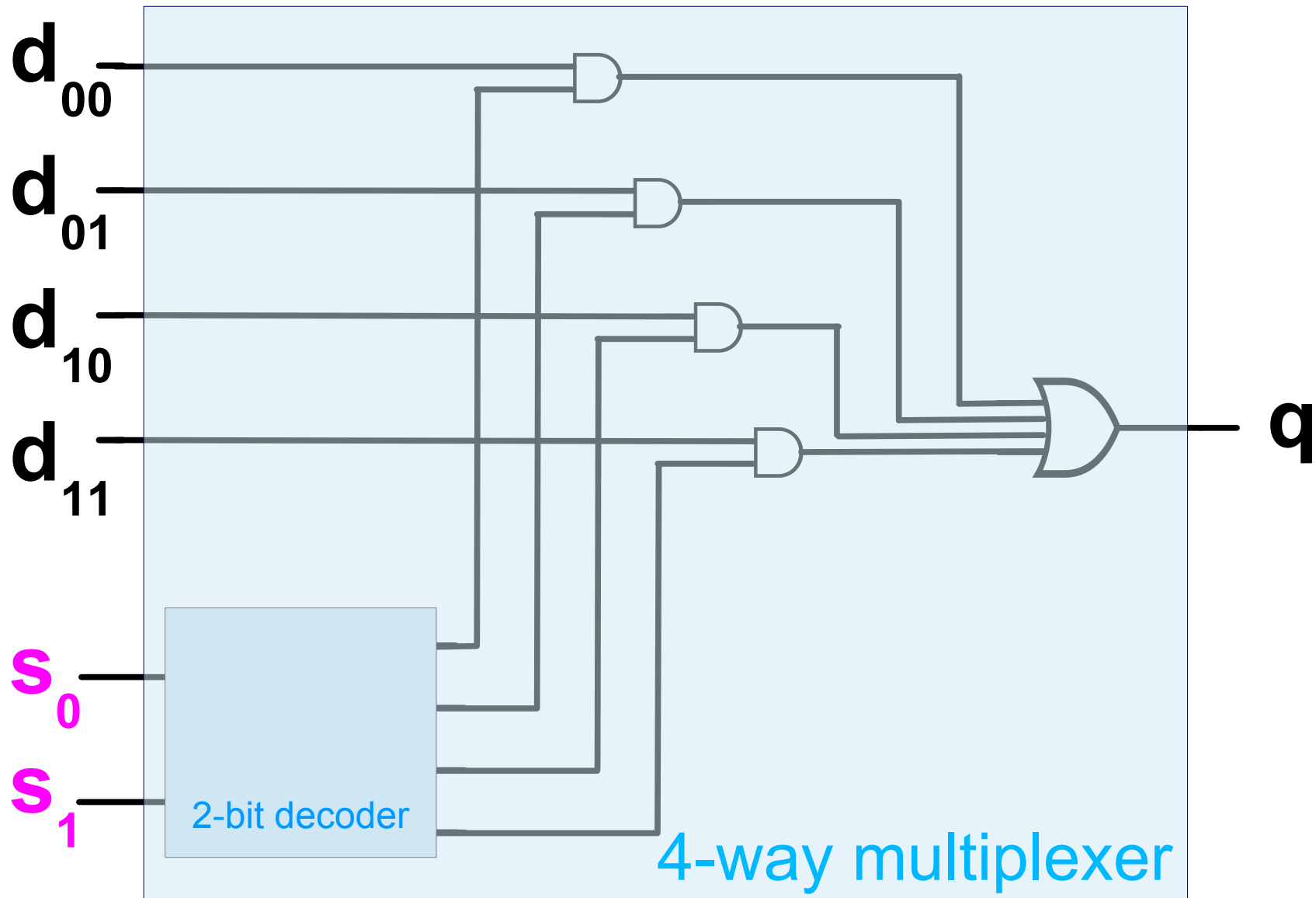
$$+ s_1 \bar{s}_0 d_{10}$$

$$+ s_1 s_0 d_{11}$$

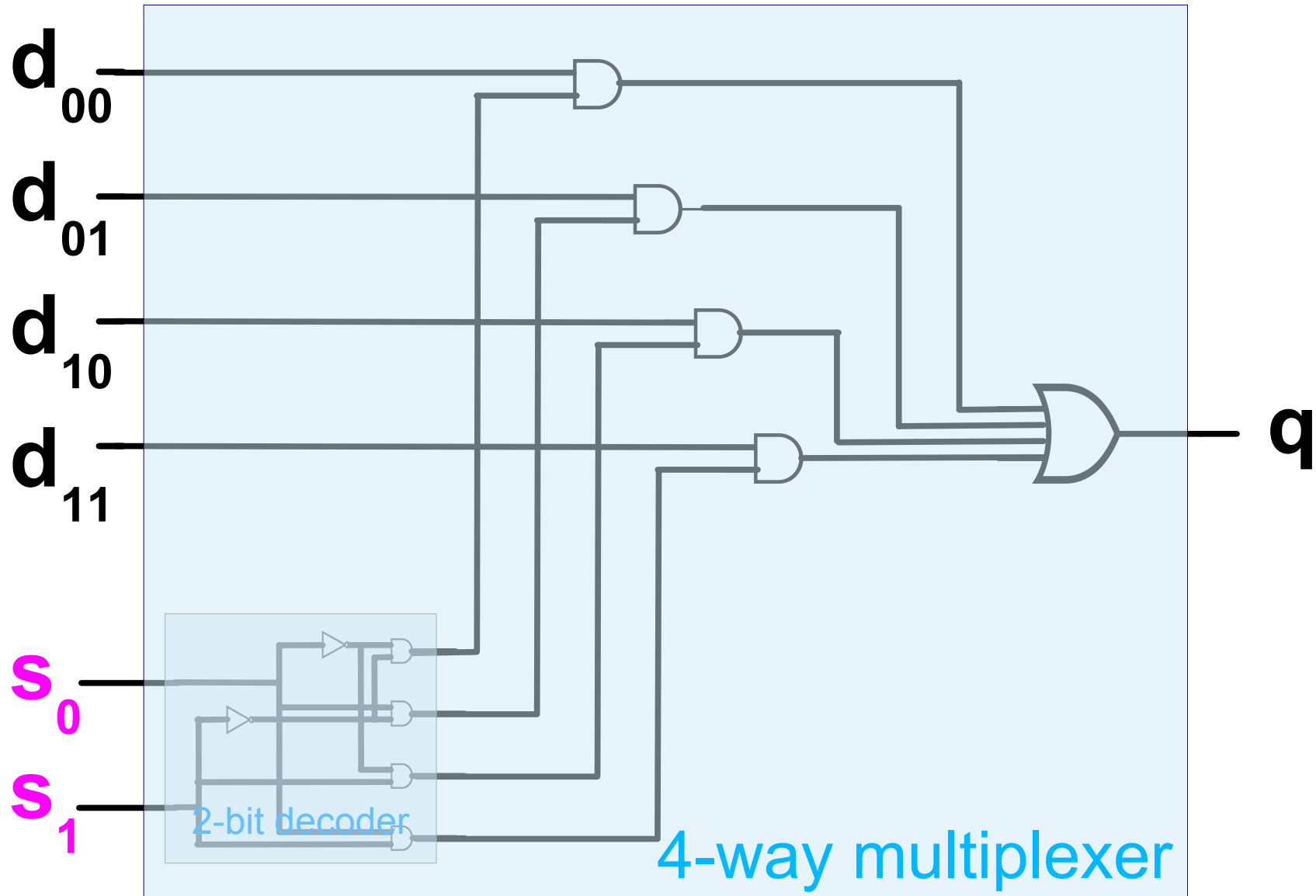
4-Way Multiplexer



4-Way Multiplexer



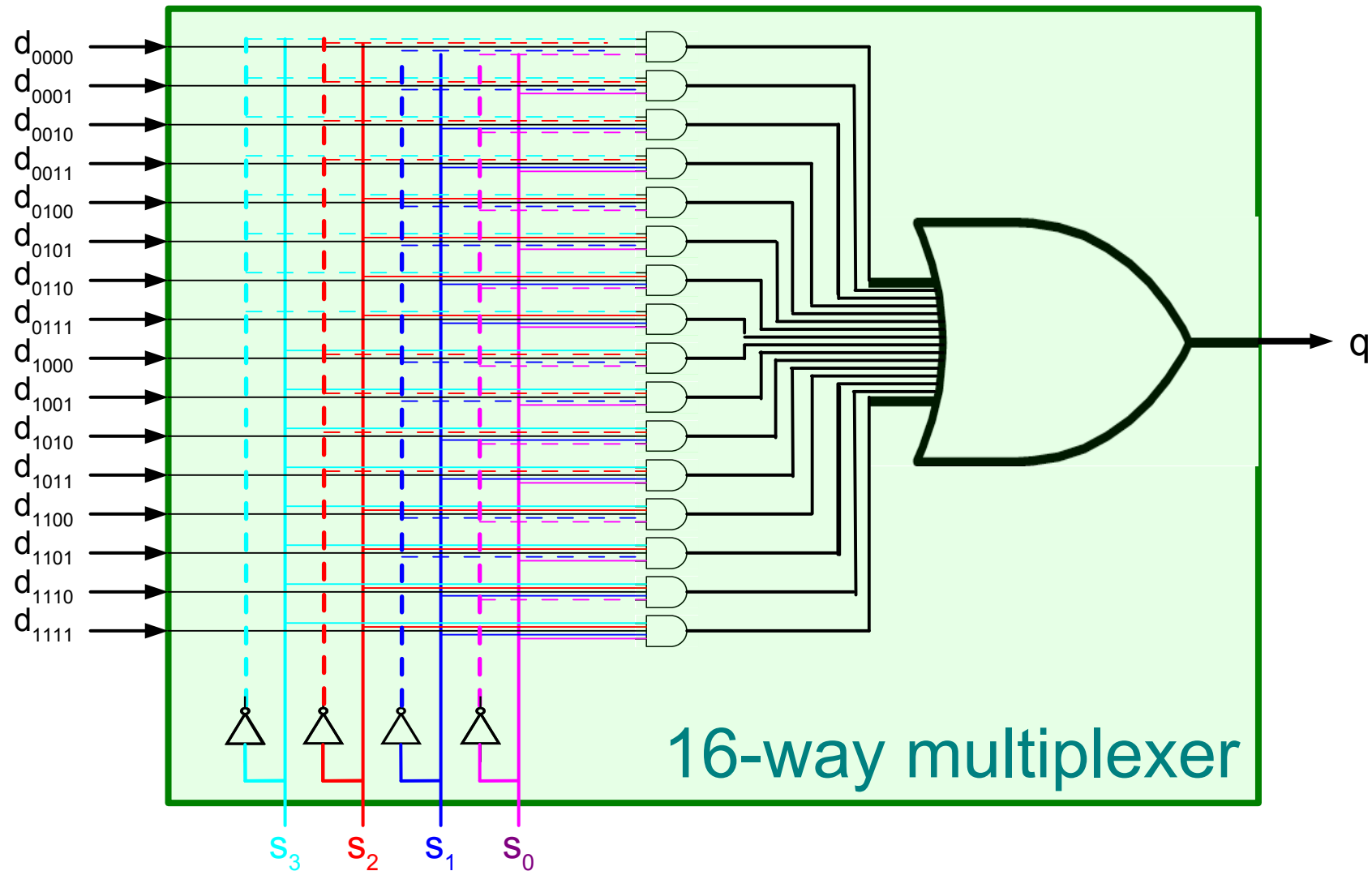
4-Way Multiplexer



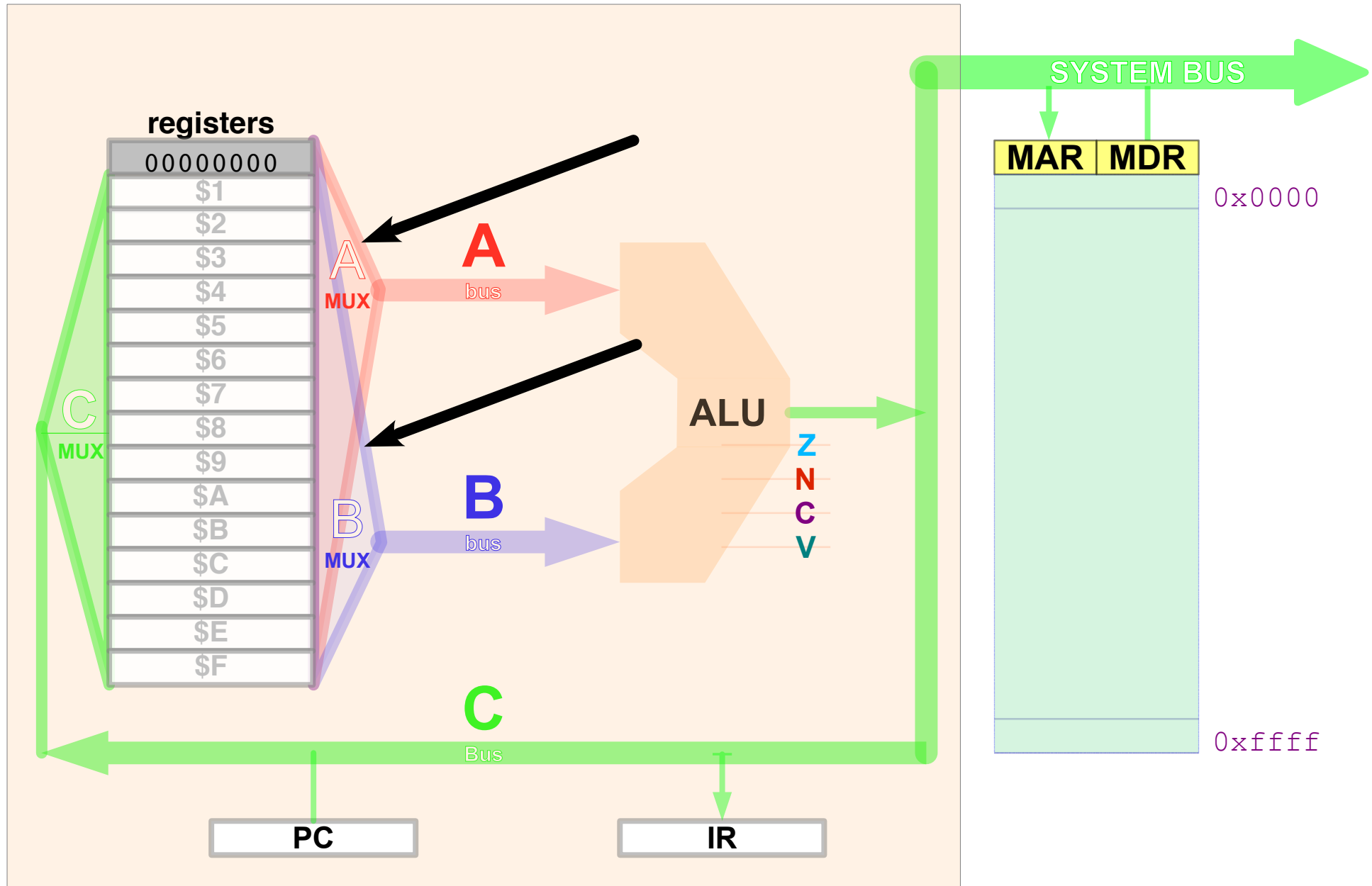
16-Way Multiplexer



16-Way Multiplexer

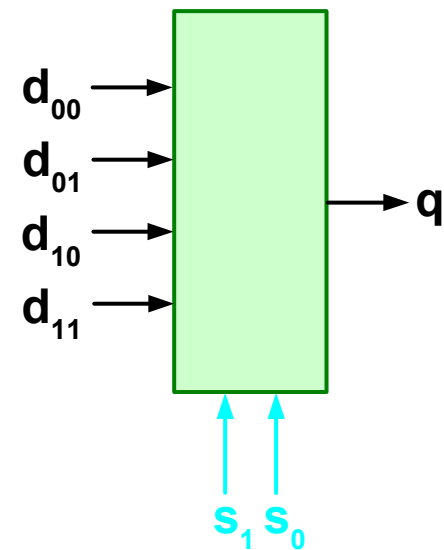
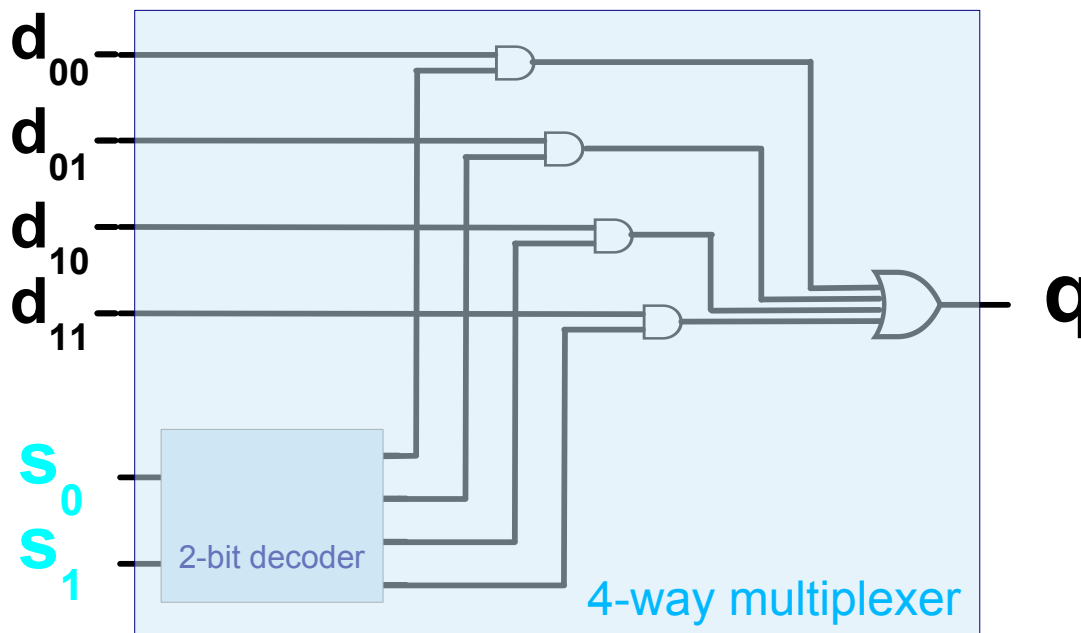


A and B Multiplexers



2^N -Way Multiplexer Summary

N **selector** inputs specify one of 2^N data inputs to output



n-bit Binary Numbers

Unsigned: $b_{n-1} b_{n-2} \dots b_1 b_0$ ($b_i = 0$ or $b_i = 1$)

value: $b_{n-1}2^{n-1} + b_{n-2}2^{n-2} + \dots + b_12^1 + b_02^0$

range: $[0 \dots 2^n - 1]$

n-bit sum: $A \overset{\dots}{+} B = A + B - c \cdot 2^n$

n-bit diff: $A \overset{\dots}{-} B \equiv A \overset{\dots}{+} (2^n - B) = A \overset{\dots}{+} \bar{B} + 1 = A - B + 2^n - c \cdot 2^n$

Signed: $b_{n-1} b_{n-2} \dots b_1 b_0$ ($b_i = 0$ or $b_i = 1$)

Value: $-b_{n-1}2^{n-1} + b_{n-2}2^{n-2} + \dots + b_12^1 + b_02^0$

Range: $[-2^{n-1} \dots 2^{n-1} - 1]$

n-bit sum: $A \overset{\dots}{+} B = A + B$ iff $v=0$

n-bit diff: $A \overset{\dots}{-} B = A - B$ iff $v=0$

Honesty Criteria

The n-bit result **r** of a binary operation on n-bit values **a** and **b** is **honest** (**deceptive**) if it is *the same as* (*different from*) the whole number result of the same operation on the same values.

(n-bit) unsigned addition is *honest* iff (**c** = 0)

Carry flag is not set

(n-bit) unsigned subtraction is *honest* iff (**c** = 1)

Carry flag is set

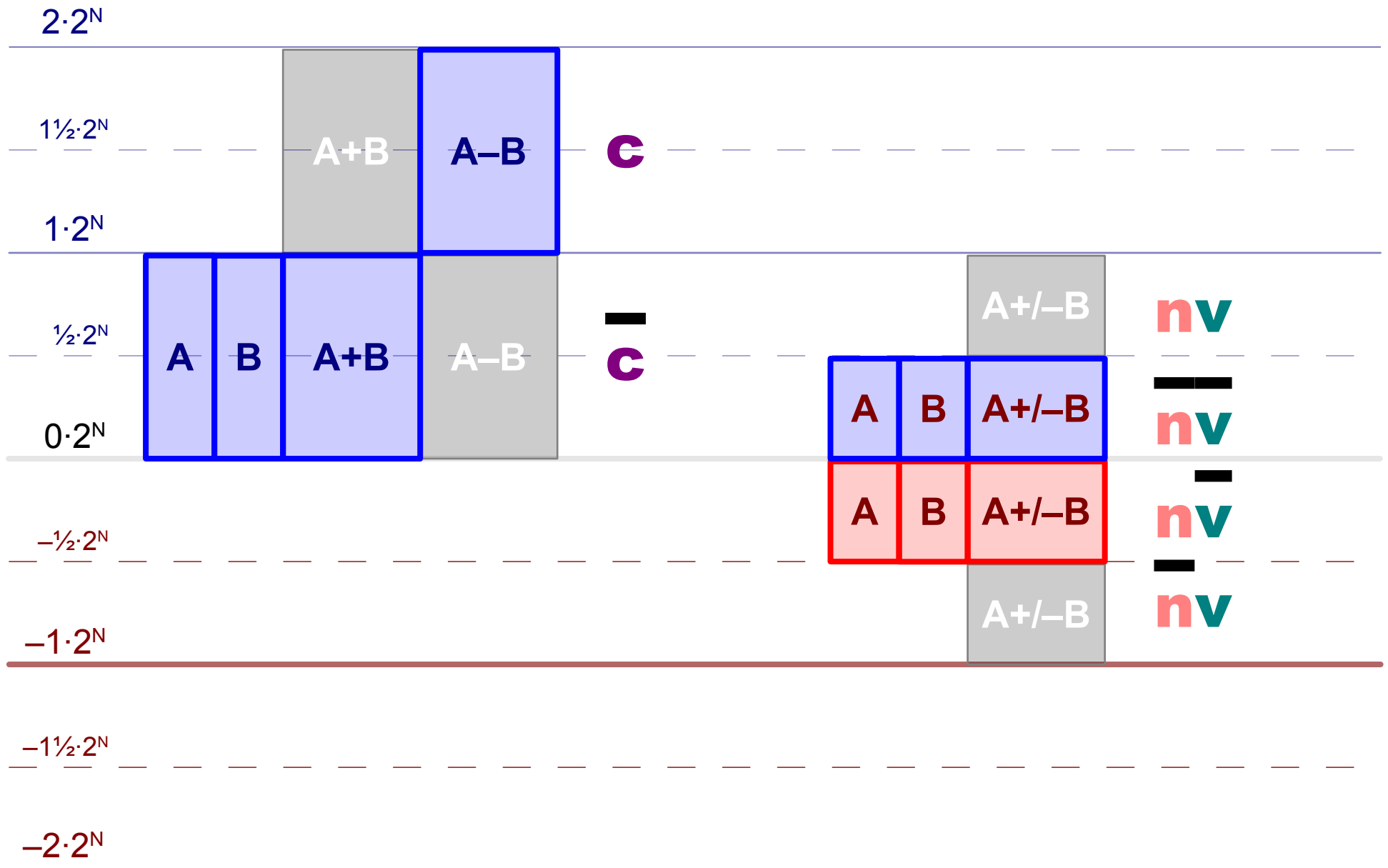
(n-bit) signed addition is *honest* iff (**v** = 0)

a and **b** have different signs or **a**, **b**, and **r** have same sign

(n-bit) signed subtraction is *honest* iff (**v** = 0)

a and **b** have same sign or **a** and **r** have same sign

Condition Flags



Comparisons and Condition Flags

A = **B** ??

A ≥ **B** ??

A ≥ **B**

unsigned

A = **B**

both

signed

Comparisons and Condition Flags

A = **B** if and only if **A** – **B** = 0

A ≥ **B** if and only if **A** – **B** ≥ 0

$$\mathbf{A-B \geq 0}$$

unsigned

$$\mathbf{A-B = 0}$$

both

signed

Comparisons and Condition Flags

A = **B** if and only if **A** – **B** = 0

A ≥ **B** if and only if **A** – **B** ≥ 0

$$\mathbf{A} - \mathbf{B} \geq 0$$

unsigned

$$\mathbf{A} - \mathbf{B} = 0 \quad \mathbf{Z}$$

both

signed

Comparisons and Condition Flags

A = **B** if and only if **A** – **B** = 0

A ≥ **B** if and only if **A** – **B** ≥ 0

A – **B** ≥ 0 **c**

unsigned

A – **B** = 0 **z**

both

signed

Comparisons and Condition Flags

$A \neq B$ if and only if $A - B \neq 0$

$A < B$ if and only if $A - B < 0$

unsigned $A - B \geq 0$ **c** $A - B < 0$

both $A - B = 0$ **z** $A - B \neq 0$

signed

Comparisons and Condition Flags

$A \neq B$ if and only if $A - B \neq 0$

$A < B$ if and only if $A - B < 0$

unsigned $A - B \geq 0$ c $A - B < 0$ \bar{c}

both $A - B = 0$ z $A - B \neq 0$ \bar{z}

signed

Comparisons and Condition Flags

$A \leq B$ if and only if $A - B \leq 0$

$A > B$ if and only if $A - B > 0$

unsigned

$A - B \geq 0$ c

$A - B < 0$ $\overline{\text{c}}$

$A - B \leq 0$

$A - B > 0$

both

$A - B = 0$ z

$A - B \neq 0$ $\overline{\text{z}}$

signed

Comparisons and Condition Flags

$A \leq B$ if and only if $A - B \leq 0$

$A > B$ if and only if $A - B > 0$

unsigned

$A - B \geq 0$ c

$A - B < 0$ \bar{c}

$A - B \leq 0$ $z + \bar{c}$

$A - B > 0$

both

$A - B = 0$ z

$A - B \neq 0$ \bar{z}

signed

Comparisons and Condition Flags

$A \leq B$ if and only if $A - B \leq 0$

$A > B$ if and only if $A - B > 0$

unsigned

$A - B \geq 0$ c

$A - B < 0$ \bar{c}

$A - B \leq 0$ $z + \bar{c}$

$A - B > 0$ $\bar{z}c$

both

$A - B = 0$ z

$A - B \neq 0$ \bar{z}

signed

Comparisons and Condition Flags

$A \geq B$ if and only if $A - B \geq 0$

$A < B$ if and only if $A - B < 0$

unsigned

$$A - B \geq 0 \quad \text{c}$$

$$A - B < 0 \quad \bar{\text{c}}$$

$$A - B \leq 0 \quad \text{z} + \bar{\text{c}}$$

$$A - B > 0 \quad \bar{\text{z}} \text{c}$$

both

$$A - B = 0 \quad \text{z}$$

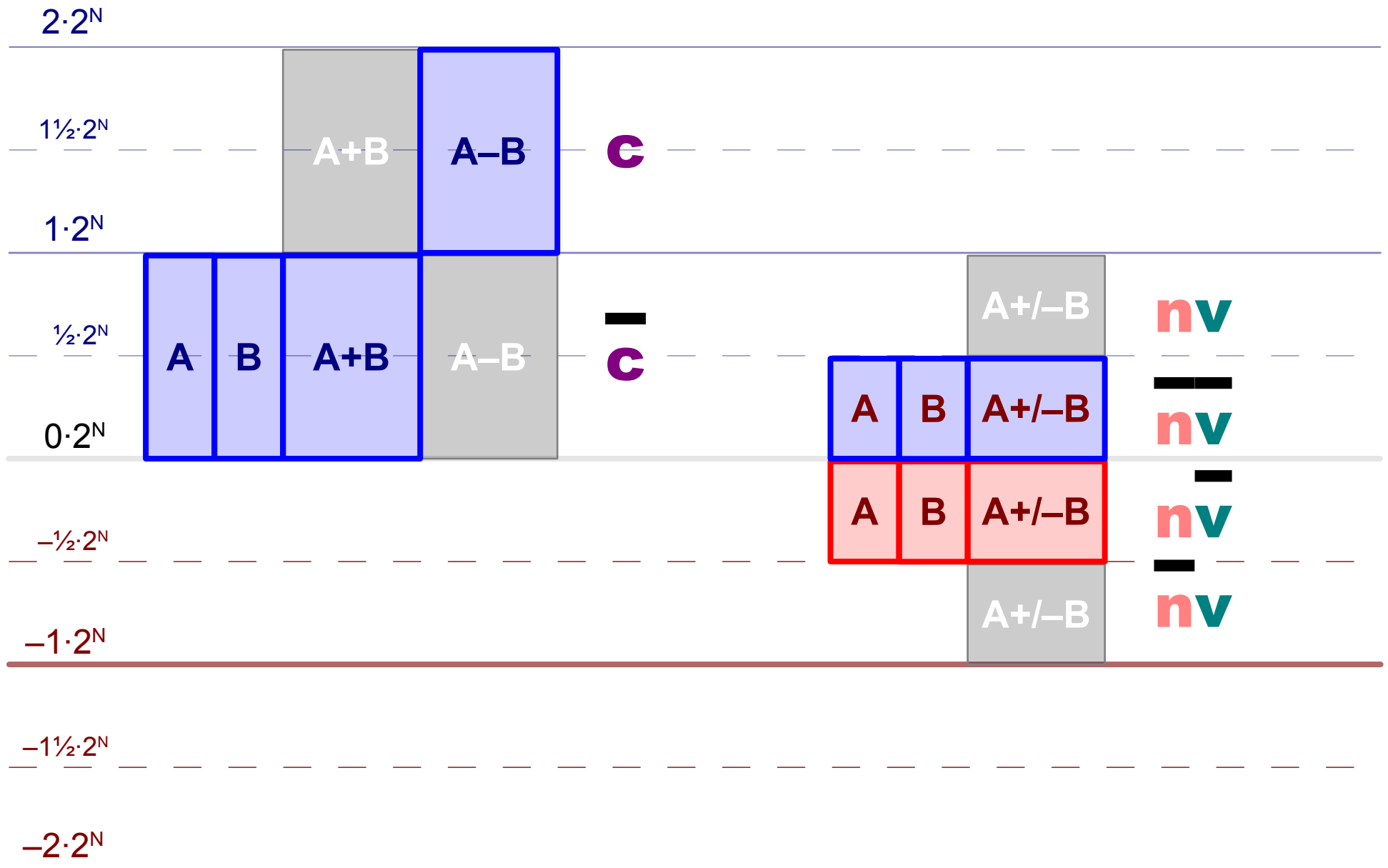
$$A - B \neq 0 \quad \bar{\text{z}}$$

signed

$$A - B \geq 0$$

$$A - B < 0$$

Condition Flags



Comparisons and Condition Flags

$A \geq B$ if and only if $A - B \geq 0$

$A < B$ if and only if $A - B < 0$

unsigned

$A - B \geq 0$ c

$A - B < 0$ \bar{c}

$A - B \leq 0$ $z + \bar{c}$

$A - B > 0$ $\bar{z}c$

both

$A - B = 0$ z

$A - B \neq 0$ \bar{z}

signed

$A - B \geq 0$ $\overline{nv + nv}$

$A - B < 0$

Comparisons and Condition Flags

$A \geq B$ if and only if $A - B \geq 0$

$A < B$ if and only if $A - B < 0$

unsigned

$$A - B \geq 0 \quad \mathbf{c}$$

$$A - B < 0 \quad \overline{\mathbf{c}}$$

$$A - B \leq 0 \quad \mathbf{z} + \overline{\mathbf{c}}$$

$$A - B > 0 \quad \overline{\mathbf{z}} \mathbf{c}$$

both

$$A - B = 0 \quad \mathbf{z}$$

$$A - B \neq 0 \quad \overline{\mathbf{z}}$$

signed

$$A - B \geq 0 \quad \overline{\mathbf{nv}} + \mathbf{nv}$$

$$A - B < 0 \quad \mathbf{nv} + \overline{\mathbf{nv}}$$

Comparisons and Condition Flags

$A \geq B$ if and only if $A - B \geq 0$

$A < B$ if and only if $A - B < 0$

unsigned

$A - B \geq 0$ c

$A - B < 0$ \bar{c}

$A - B \leq 0$ $z + \bar{c}$

$A - B > 0$ $\bar{z}c$

both

$A - B = 0$ z

$A - B \neq 0$ \bar{z}

signed

$A - B \geq 0$ $\overline{nv} + nv$

$A - B < 0$ $n\bar{v} + \bar{n}\bar{v}$

$A - B \leq 0$

$A - B > 0$

Comparisons and Condition Flags

$A \geq B$ if and only if $A - B \geq 0$

$A < B$ if and only if $A - B < 0$

unsigned

$$A - B \geq 0 \quad \text{c}$$

$$A - B < 0 \quad \overline{\text{c}}$$

$$A - B \leq 0 \quad \text{z} + \overline{\text{c}}$$

$$A - B > 0 \quad \overline{\text{z}} \text{c}$$

both

$$A - B = 0 \quad \text{z}$$

$$A - B \neq 0 \quad \overline{\text{z}}$$

signed

$$A - B \geq 0 \quad \overline{\text{nv}} + \text{nv}$$

$$A - B < 0 \quad \text{nv} + \overline{\text{nv}}$$

$$A - B \leq 0 \quad \text{z} + \text{nv} + \overline{\text{nv}}$$

$$A - B > 0$$

Comparisons and Condition Flags

$A \geq B$ if and only if $A - B \geq 0$

$A < B$ if and only if $A - B < 0$

unsigned

$$A - B \geq 0 \quad c$$

$$A - B < 0 \quad \bar{c}$$

$$A - B \leq 0 \quad z + \bar{c}$$

$$A - B > 0 \quad \bar{z}c$$

both

$$A - B = 0 \quad z$$

$$A - B \neq 0 \quad \bar{z}$$

$$A - B \geq 0 \quad \overline{nv + nv}$$

$$A - B < 0 \quad nv + \bar{nv}$$

signed

$$A - B \leq 0 \quad z + \bar{nv} + \bar{nv}$$

$$A - B > 0 \quad \overline{znv + nv}$$

Comparisons and Condition Flags

unsigned $A \geq B$ c $A < B$ \bar{c}

$A \leq B$ $z + \bar{c}$ $A > B$ $\bar{z}c$

both $A = B$ z $A \neq B$ \bar{z}

signed $A \geq B$ $\overline{nv + nv}$ $A < B$ $nv + \overline{nv}$

$A \leq B$ $z + \overline{nv} + \overline{nv}$ $A > B$ $\overline{znv + nv}$

HW 10: Condition Flags

For each line in the table on the following page indicate whether the assertion would be true (T), false (F), or unknown (U) if the condition flags obtained their indicated values after the ALU performed the indicated operation.

operation		flags	assertion	T / F / ?
$\overset{\dots}{A} - \overset{\dots}{B}$	unsigned	$\overline{Z}N\overline{C}V$	result is honest	
$\overset{\dots}{A} + \overset{\dots}{B}$	signed	$\overline{Z}NCV$	result is honest	
$\overset{\dots}{A} + \overset{\dots}{B}$	unsigned	$\overline{Z}N\overline{C}V$	result is honest	
$\overset{\dots}{A} - \overset{\dots}{B}$	signed	$\overline{Z}N\overline{C}V$	result is honest	
$\overset{\dots}{A} + \overset{\dots}{B}$	unsigned	$\overline{Z}NCV$	result is honest	
$\overset{\dots}{A} + \overset{\dots}{B}$	signed	$\overline{Z}N\overline{C}\overline{V}$	result is honest	
$\overset{\dots}{A} - \overset{\dots}{B}$	unsigned	$\overline{Z}N\overline{C}V$	$\overset{\dots}{A} > \overset{\dots}{B}$	
$\overset{\dots}{A} - \overset{\dots}{B}$	signed	$\overline{Z}N\overline{C}V$	$\overset{\dots}{A} = \overset{\dots}{B}$	
$\overset{\dots}{A} - \overset{\dots}{B}$	unsigned	$\overline{Z}N\overline{C}V$	$\overset{\dots}{A} < \overset{\dots}{B}$	
$\overset{\dots}{A} + \overset{\dots}{B}$	signed	$\overline{Z}N\overline{C}V$	$\overset{\dots}{A} \leq \overset{\dots}{B}$	
$\overset{\dots}{A} - \overset{\dots}{B}$	unsigned	$\overline{Z}N\overline{C}\overline{V}$	$\overset{\dots}{A} > \overset{\dots}{B}$	
$\overset{\dots}{A} - \overset{\dots}{B}$	signed	$\overline{Z}NCV$	$\overset{\dots}{A} \leq \overset{\dots}{B}$	
$\overset{\dots}{A} + \overset{\dots}{B}$	unsigned	$\overline{Z}N\overline{C}V$	$\overset{\dots}{A} < \overset{\dots}{B}$	
$\overset{\dots}{A} - \overset{\dots}{B}$	signed	$\overline{Z}N\overline{C}V$	$\overset{\dots}{A} \geq \overset{\dots}{B}$	
$\overset{\dots}{A} - \overset{\dots}{B}$	unsigned	$\overline{Z}N\overline{C}\overline{V}$	$\overset{\dots}{A} \leq \overset{\dots}{B}$	
$\overset{\dots}{A} - \overset{\dots}{B}$	signed	$\overline{Z}NCV$	$\overset{\dots}{A} \geq \overset{\dots}{B}$	

Honesty Criteria

The n-bit result **r** of a binary operation on n-bit values **a** and **b** is **honest** (**deceptive**) if it is *the same as* (*different from*) the whole number result of the same operation on the same values.

(n-bit) unsigned addition is *honest* iff (**c** = 0)

Carry flag is not set

(n-bit) unsigned subtraction is *honest* iff (**c** = 1)

Carry flag is set

(n-bit) signed addition is *honest* iff (**v** = 0)

a and **b** have different signs or **a**, **b**, and **r** have same sign

(n-bit) signed subtraction is *honest* iff (**v** = 0)

a and **b** have same sign or **a** and **r** have same sign

HW 9: Signed Binary Arithmetic

For each of the $\langle X, Y \rangle$ pairs in the table below:

- Convert X and $Y \rightarrow$ binary
- Compute $X + Y$ (the 8-bit **sum**)
- Compute \bar{Y} (the 2's complement of Y)
- Compute $X - Y \equiv X + \bar{Y}$ (the 8-bit **difference**)
- Indicate the signs of X , Y , $X + Y$, \bar{Y} , and $X - Y$
- Convert $X + Y$, \bar{Y} , and $X - Y \rightarrow$ hexadecimal
- Indicate condition flag (**z**, **n**, **c**, **v**) values for $X + Y$, $X - Y$
- Is $X + Y$ honest? is $X - Y$ honest?

Where $\langle X, Y \rangle =$

- | | |
|---------------------------------|---------------------------------|
| 1) $\langle 0x4F, 0x6D \rangle$ | 2) $\langle 0xB3, 0x17 \rangle$ |
| 3) $\langle 0xA3, 0x95 \rangle$ | 4) $\langle 0x6E, 0x3A \rangle$ |

Signed Arithmetic Example: X4

X

0x5A

Y

0x5A

X + **Y**

~**Y**

X - **Y**

Signed Arithmetic: X4 a)

X

Y

X + Y

~Y

X - Y

0x5A

0x5A

01011010

01011010

Signed Arithmetic: X4 b)

X

Y

X + Y

~Y

X - Y

0x5A

0x5A

01011010

01011010

01011010

01011010

0 1 0 1 1 0 1 0

010110100

Signed Arithmetic: X4 c)

X

Y

X + Y

~Y

X - Y

0x5A

0x5A

01011010

01011010

01011010

01011010

01011010

10100101

0 1 0 1 1 0 1 0

00000001

010110100

10100110

Signed Arithmetic: X4 d)

X	Y	X + Y	~Y	X - Y
0x5A	0x5A			
01011010	01011010	01011010 <u>01011010</u>	<u>01011010</u> 10100101	01011010 10100110
		0 1 0 1 1 0 1 0	<u>00000001</u>	1 1 1 1 1 1 1 0
		0 10110100	10100110	1 00000000

Signed Arithmetic: X4 e)

X	Y	X + Y	~Y	X - Y
0x5A	0x5A			
01011010	01011010	01011010 <u>01011010</u>	<u>01011010</u> 10100101	01011010
		0 1 0 1 1 0 1 0	<u>00000001</u>	1 1 1 1 1 1 1 0
		010110100	10100110	100000000

Signed Arithmetic: X4 f)

X	Y	X + Y	~Y	X - Y
0x5A	0x5A			
01011010	01011010	01011010 <u>01011010</u>	<u>01011010</u> 10100101 <u>00000001</u>	01011010 10100110
		0 1 0 1 1 0 1 0 010110100	10100110	1 1 1 1 1 1 1 0 100000000
		0xB4	0xA6	0x00

Signed Arithmetic: X4

X	Y	X + Y	~Y	X - Y
0x5A	0x5A			
01011010	01011010	<div>01011010 01011010 ----- 010110100</div>	<div>01011010 10100101 ----- 10100110</div>	<div>01011010 10100110 ----- 10000000</div>
		0 1 0 1 1 0 1 0	0 0 0 0 0 0 0 1	1 1 1 1 1 1 1 0
		0xB4	0xA6	0x00
		oVerflow		no oVerflow
		— —		— —

Signed Arithmetic: X4

X	Y	X + Y	~Y	X - Y
0x5A	0x5A			
01011010	01011010	01011010 <u>01011010</u> 0 1 0 1 1 0 1 0 010110100	<u>01011010</u> 10100101 00000001 10100110	01011010 10100110 1 1 1 1 1 1 0 100000000
		0xB4	0xA6	0x00
		oVerflow		no oVerflow

Signed Arithmetic: X4

X	Y	X + Y	~Y	X - Y
0x5A	0x5A			
01011010	01011010	<div>01011010 01011010 ----- 010110100</div>	<div>01011010 10100101 00000001 ----- 10100110</div>	<div>01011010 10100110 ----- 100000000</div>
		0xB4	0xA6	0x00
		oVerflow		no oVerflow

Signed Arithmetic: X4 g)

X	Y	X + Y	~Y	X - Y
0x5A	0x5A			
01011010	01011010	<div>01011010</div> <div>01011010</div> <hr/> <div>01011010</div> <div>01011010</div> <hr/> <div>01011010</div>	<div>01011010</div> <div>10100101</div> <hr/> <div>00000001</div> <div>10100110</div> <hr/> <div>10100110</div>	<div>01011010</div> <div>10100110</div> <hr/> <div>11111100</div> <div>10000000</div> <hr/> <div>10000000</div>
		0xB4	0xA6	0x00
		zncv		zncv

Signed Arithmetic: X4 h)

X	Y	X + Y	~Y	X - Y
0x5A	0x5A			
01011010	01011010	<div>01011010</div> <div>01011010</div> <hr/> <div>01011010</div> <div>01011010</div> <hr/> <div>01011010</div>	<div>01011010</div> <div>10100101</div> <hr/> <div>00000001</div> <div>10100110</div> <hr/> <div>10100110</div>	<div>01011010</div> <div>10100110</div> <hr/> <div>10000000</div> <div>10000000</div> <hr/> <div>10000000</div>
		0xB4	0xA6	0x00
		zncv	zncv	zncv
		(s) deceptive		(s) honest