

VHDL Testing Platform

BY FELIX G. BRAUN AND PHILIPP-S. VOGT

Advisor: Dr. Nima TaheriNejad

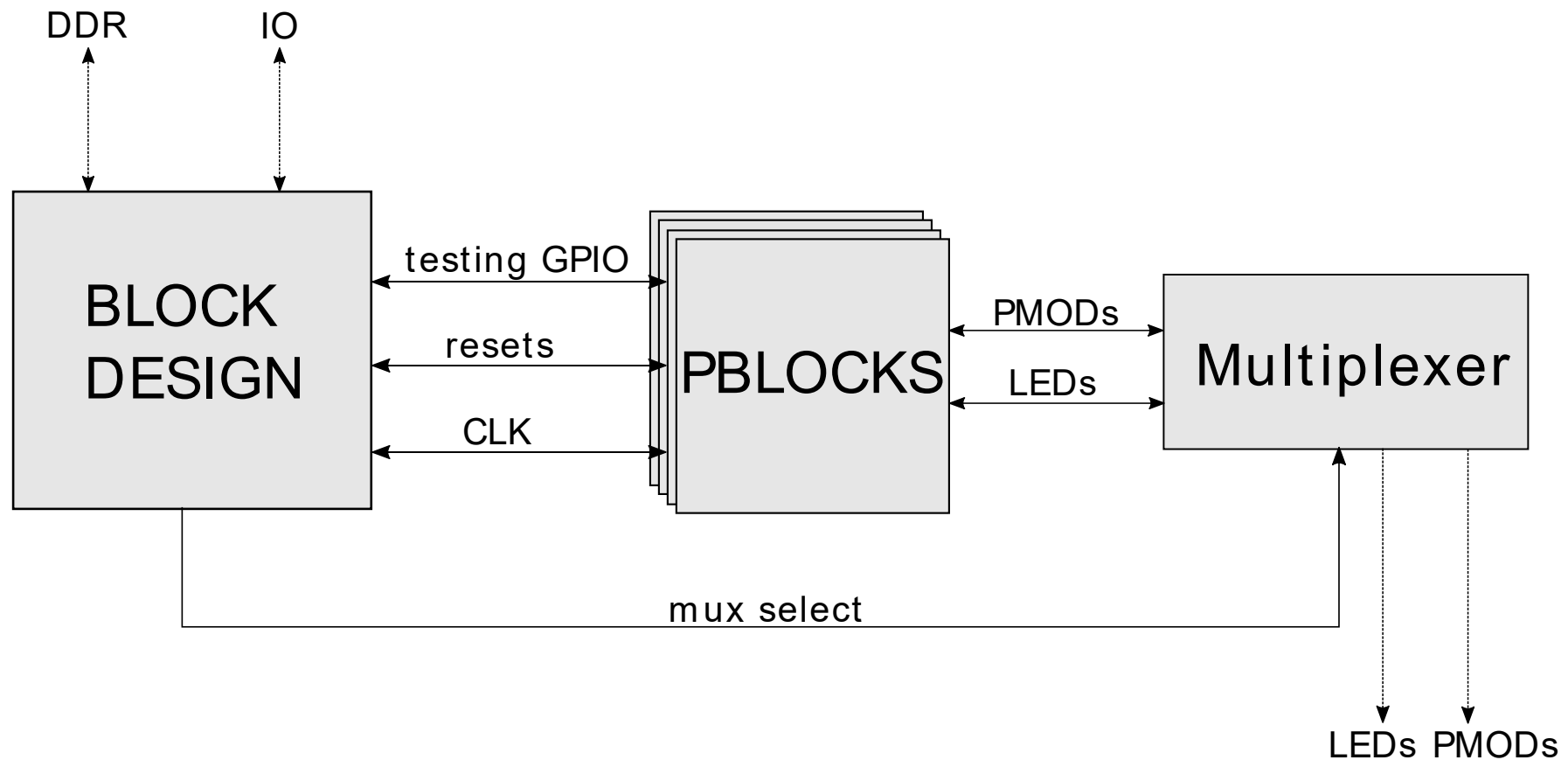
Motivation

1. More People to learn VHDL
2. Easy to use for beginners
3. Better usage of resources
4. Improved lecture at TU Wien

Vivado Automation

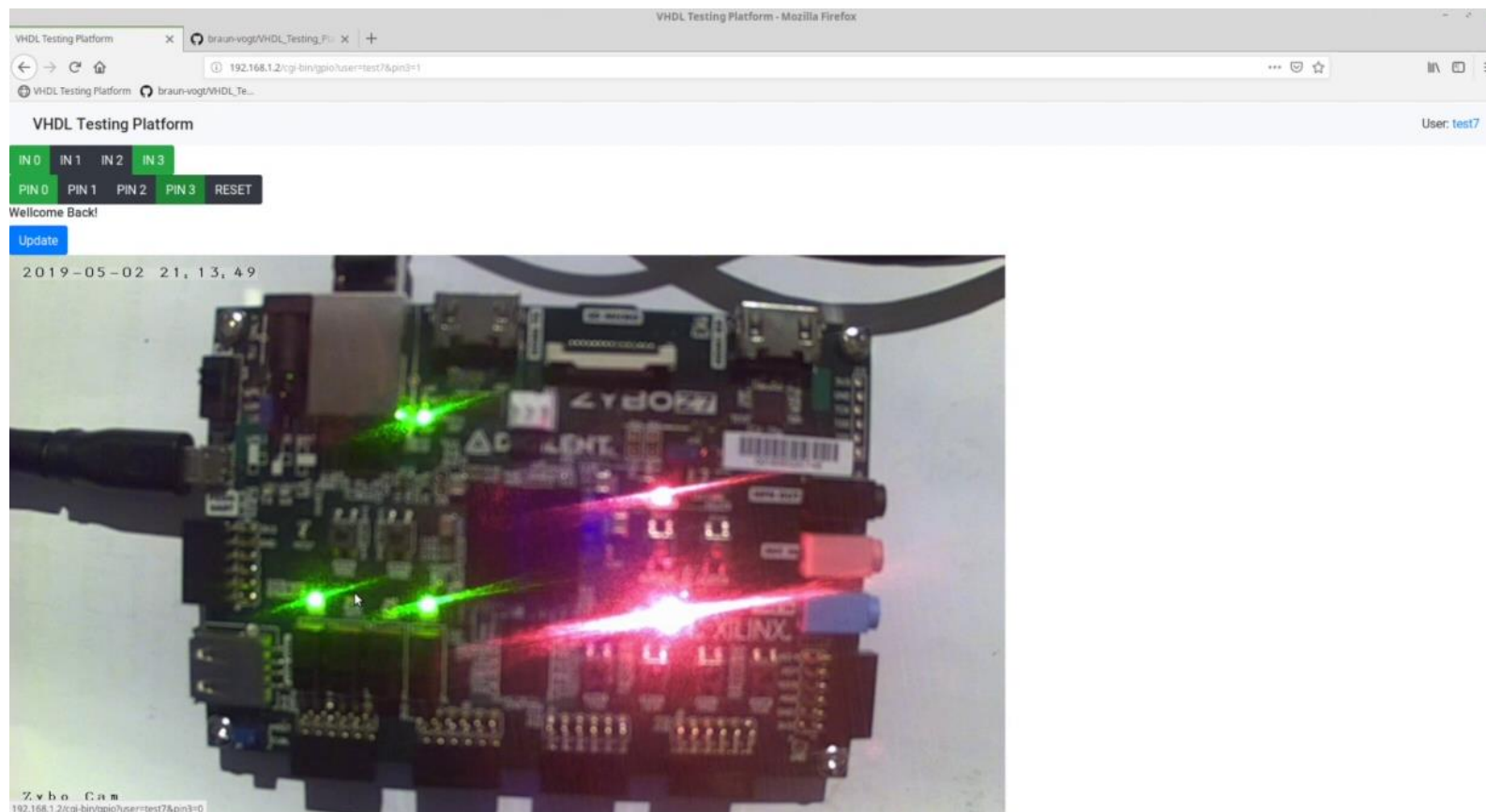
1. Automated TCL file creation
2. Written in C
3. Remove complexity

Vivado Overview



Webinterface

1. Easy to use
2. Simplistic
3. Expandable



Lecture at TU Wien

1. Improved VELs
2. Live Feedback
3. Enhanced understanding

Summary

1. Three major parts
 1. Vivado Automation
 2. Small VHDL Design
 3. Webinterface

Future Plans

1. Automated Testing
2. Online Demo
3. IP Blocks in the Cloud

Demo

