



**FRANCISCO RAFAEL  
FLORES DE MARIA Y CAMPOS**

**ELECTRONICS AND  
TELECOMMUNICATIONS ENGINEER**

## PROFILE

Electronics and Telecommunications Engineer with experience on digital design and embedded systems applications.

## CONTACT

PHONE:  
477-580-1476

LOCATION:  
San Pedro Tlaquepaque, JAL

E-MAIL  
frfdmyc@gmail.com

## EDUCATION

### **Master of science - ITESO**

#### **Master of electronic design**

August 2023 – Currently Studying

Digital design, Microprocessor Architecture and Embedded Systems.

### **Bachelor's – Universidad De La Salle Bajío**

#### **Electronics and Telecommunication Engineering**

August 2017 – December 2021

Graduated with excellence and Honorable Mention. Participant as judge in WER State Tournament for robotics in 2018 and as Staff member for the Electronics Engineering National Week in 2017.

## WORKING EXPERIENCE

### **ITESO | Half-time Professor**

September 2022 - Now

- Teacher in Secure Development Module for Security for Automotive Embedded Systems Course.
- Digital Systems Fundamentals subject teacher.

### **NXP Semiconductors | Automotive Systems and Applications Eng.**

April 2022 - Now

Tier 1 customer support on Microcontroller Application Development. Customer Enablement. New Product Introduction process active participant from the Product Concept stage to Customer Applications.

### **Electrinet | Jr. Embedded System Designer**

February 2021 – March 2022

Design, development, documentation and prototype of electronic solutions for Industrial, Automotive, Research and Warehouses. Design, test, manufacturing and maintenance of PCB in different applications using RFID, WiFi, Bluetooth, LoRA.

## SKILLS

### **Tools**

- C/C++
- Verilog
- Python
- Assembler ARM and RISC-V

### **Languages**

- Spanish (native)
- English (advanced)