

Summary:

Vidya Bhanu is a digital design engineer with about 2.5 years of experience in front-end and back-end design. She has a Master's degree in Computer Engineering from Arizona State University. She has worked with NXP Semiconductors Inc. and Intel Corporation. Her skills include RTL design, digital design, and operating systems including Windows, Linux.

Name: Vidya Bhanu

Email: vidya.bhanu96@gmail.com

Phone: (602) 702-8370

Location: Austin, Texas

Country: USA

Position Category: Design: Logic design

Desired Job Role: RTL Design Engineer for Instruction Fetch, Out-of-order Execution and Memory Blocks

University: Arizona State University

Degree: Master of Science, Computer Engineering (Electrical Engineering)

Graduation Year: 2022

Years Experience: 2.5

Notable Companies: NXP Semiconductors Inc., Intel Corporation

Top Skills: RTL design, Digital design, Front-end and back-end digital design workflows, Operating systems including Windows, Linux

Job Intention: full-time

Visa Status: --

Able: no

Subject: Digital/logic design engineer - Vidya Bhanu

Date Sent: 02-18-2025

Resume: Yes