Summary:

Seasoned VLSI professional with expertise in synthesis, P&R, signoff flows and methodology. Have held various technical and managerial roles in Intel (USA, Malaysia), Qualcomm (India) and Synopsys (USA). Have experience in CAD tool development, PD implementation, Flow and methodology development, DFx - DV execution and RTL to GDS implementation. Worked on implementation of multiple CPU, Chipset and Mobile SOCs across process nodes from TSMC, SEC and INTC foundries.

Name: Balaji Velikandanathan

Email: znath@yahoo.com

Phone: +1-(425)-770-7186

Location: Austin, TX 78717

Country: USA

Position Category: Design: Logic design, Design: Physical Design

Desired Job Role: Senior Solutions Architect

University: Syracuse University, NY, Guindy Engineering College, Anna University, India

Degree: M.S., in Computer Science, B.E., in Electronics and Communication Engineering

Graduation Year: --

Years Experience: 23+

Notable Companies: Intel, Qualcomm, Synopsys

Top Skills: EDA Tools, C, C++, Tcl, System Verilog, Perl

Job Intention: full-time

Visa Status: --

Able: no

Subject: Resume

Date Sent: 03-20-2025

Resume: Yes