

## Summary:

Sai Surendra Reddy Yaraballi is a Master's graduate in Computer Engineering from Texas A&M University, with a Bachelor's degree in Electronics and Communication Engineering from the Gandhi Institute of Technology and Management. He has 2 years of experience, including an internship as an RTL Design intern and a role as an Assistant System Engineer at Tata Consultancy Services. He has strong skills in Verilog, System Verilog, UVM, SVA, Covergroups, C, C++, Python, and Tcl.

Name: Sai Surendra Reddy Yaraballi

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Phone: (979)-739-5255

Location: College Station, TX

Country: USA

Position Category: Verification: Formal verification

Desired Job Role: Senior Formal Verification Engineer

University: Texas A&M University, Gandhi Institute of Technology and Management

Degree: Master of Science in Computer Engineering, Bachelor of Technology in Electronics and Communication Engineering

Graduation Year: 2024, 2020

Years Experience: 2

Notable Companies: Tata Consultancy Services, Centre for Development of Advanced Computing

Top Skills: Verilog, System Verilog, Universal Verification Methodology (UVM), System Verilog Assertions (SVA), Covergroups, C, C++, Python, Tcl

Job Intention: full-time

Visa Status: --

Able: no

Subject: Working at AheadComputing Application for Senior Formal Verification Engineer role

Date Sent: 03-11-2025

Resume: Yes