

Hi team,

I'm a MS-CE graduate from NC State, worked as DV Engineer at Renesas Electronics until recently.

I also have some relevant previous experience in design. My skills include Computer Architecture, ASIC design and verification with proficiency in Verilog, System Verilog, UVM methodology, python scripting, regex, SVN, C++ programming. I'm looking for immediate positions and open to relocate.

I'm interested in the Senior CPU Microarchitecture & Logic Design Engineer position at Ahead Computing

and I believe my skills align with what you're looking for. It would be great to have an opportunity to discuss/interview for the role.

I would also like to be considered for other Design/Verification roles at your company. I've attached my resume with this email.

Regards,

Mathura