

Dear Sir/Madam,

Greetings ! My name is Nilesh Karnik.

I am herewith submitting my job application for "Staff Physical Design Engineer CPU PPA Optimization" position posted on AheadComputing Career website, for your due consideration and review.

Summary of Work-Experience/Skills:

As a Sr. ASIC Design Engineer with 23+ years of experience, I have demonstrated proven technical leadership at INTEL CORP covering various roles and responsibilities including Physical Design - Planning, Implementation, Block and SOC Design, Construction, Implementation Integration, Pre-Si STA/SI Convergence, Quality/Reliability and Signoff, IP Logic Design (RTL) & Formal Verification, Soft-IP (SIP) Qualification, PPA Estimation, IP/SOC Handoff, Design Tools (EDA) -Flows-Methodology development/maintenance/support and technical Managment in CPU/Server/IP organizations respectively.

My aforementioned work-experience is backed by the necessary Education, Skills/Expertise, Research and Strengths, that are also available in my attached resume herewith, in requirement of this position.

I look forward to further discuss my credentials and interest in this position with your Talent Advisor/Recruitment team if given the opportunity.

Thank you again for your time and consideration!

Sincerely,

Nilesh Karnik