



Master's Degree - Academic Record Decree Translation

1723 6976 0605

Zurich, 31 January 2025

Eike HIMSTEDT

born on 24 December 1997, matriculation number 22-942-957

has passed the following examinations

at the Department of Information Technology and Electrical Engineering

according to the regulations of 31 October 2017

for the Master's Degree in Electrical Engineering and Information Technology

	Grade/Result	Credits (ECTS)
Core Courses		
Electromagnetic Waves: Materials, Effects, and Antennas	5.00	6
Nano-Optics	6.00	6
Solid State Electronics and Optics	6.00	6
VLSI 2: From Netlist to Complete System on Chip	5.25	6
Specialisation Courses		
Embedded MEMS Lab	5.50	5
Integrated Systems Seminar	passed	1
Machine Learning on Microcontrollers	6.00	6
Modeling, Characterization and Reliability of Power Semiconductors	5.00	6
Physical Modelling and Simulation	5.75	6
Power Market I - Portfolio and Risk Management	6.00	6
Quantum Measurements and Optomechanics	5.25	4
Solar Cells	5.50	4
Technology and Innovation Management	5.50	3
Semester Projects		
Semester Project (Nr 1)	5.50	12
Industrial Internship		
Internship in Industry	passed	12

Director of Studies
Prof. Dr. Christian Franck



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	Grade/Result	Credits (ECTS)
Science in Perspective		
International Environmental Politics	5.75	3
Master's Thesis		
Master's Thesis	6.00	30
Overall Grade Point Average (Weight=Credits)	5.69	
Total Credits		122

Grading scale: 6 is the highest, 1 is the lowest grade; passmark is 4. Ungraded performance assessments are marked with either passed or failed. An ECTS credit corresponds to a workload of 30 hours.

It is decreed:

1. The candidate is awarded the Master's Degree in Electrical Engineering and Information Technology.
2. The candidate is awarded the title Master of Science ETH in Electrical Engineering and Information Technology.
Short titles: MSc ETH EEIT or MSc ETH.

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ETH ZÜRICH
FEDERATION OF ETH INSTITUTES
IN THE GERMAN-SPEAKING COUNTRIES

Addendum Translation

Zurich, 31 January 2025

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Further achievements in the Master's Degree Programme in Electrical Engineering and Information Technology

	Grade/Result	Credits (ECTS)
Español A2: Inicial / Sprachenzentrum der UZH und der ETH Zurich	5.00	2
Español B1: Inicial / Sprachenzentrum der UZH und der ETH Zurich	4.75	2
VLSI 1: HDL Based Design for FPGAs	4.75	6



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Certified Translation
ETH Zurich, 3 February 2025



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