

Summary:

Mathura Swaminathan is a MS-CE graduate with 4 years of experience in design and verification. Proficient in Verilog, System Verilog, UVM methodology, python scripting, and C++ programming. Seeking immediate positions in Design: Physical Design.

Name: Mathura Swaminathan

Email: mathura.swaminathan@gmail.com

Phone: 984-895-9340

Location: Morrisville, NC

Country: USA

Position Category: Design: Physical Design

Desired Job Role: Senior CPU Microarchitecture & Logic Design Engineer

University: North Carolina State University, Raleigh, NC, USA

Degree: Master of Science in Computer Engineering

Graduation Year: 2023

Years Experience: 4

Notable Companies: Renesas Electronics America Inc., DANFOSS, DATA PATTERNS Ltd.

Top Skills: Verilog, System Verilog, UVM methodology, python scripting, C++ programming

Job Intention: full-time

Visa Status: --

Able: no

Subject: Working at AheadComputing

Date Sent: 04-07-2025

Resume: Yes