

Summary:

Rudakiya Mayank is a semiconductor professional with over 3 years of hands-on experience in RTL Design of various protocols. He has worked with notable companies like Cisma Consultants, Cadence, and SiFive. His top skills include RTL Design, Verilog, System Verilog, Chisel, Python, C++, Tcl, Formal Verification, and UVM.

Name: Rudakiya Mayank

Email: rudakiyamayank355@gmail.com

Phone: +919824817093

Location: Bangalore

Country: India

Position Category: Design: Logic design

Desired Job Role: CPU Design Engineer

University: The Maharaja Sayajirao University of Baroda

Degree: Bachelor of Engineering in Electronics

Graduation Year: 2021

Years Experience: 3

Notable Companies: Cisma Consultants, Cadence, SiFive

Top Skills: RTL Design, Verilog, System Verilog, Chisel, Python, C++, Tcl, Formal Verification, UVM

Job Intention: full-time

Visa Status: --

Able: no

Subject: Application for CPU Design Engineer

Date Sent: 03-05-2025

Resume: Yes