Summary:

Hari Shashank Singh Thakur is a Design Verification Engineer with 1.5 years of experience, currently seeking full-time opportunities. He holds a Master of Science in Electrical and Computer Engineering from Portland State University and a Bachelor of Engineering in Electronics and Communication from Osmania University. He has worked at Onsemi as a Digital Verification Engineer and intern. He is proficient in System Verilog, Verilog, C, UVM/UVM Framework, and comfortable with Python.

Name: Hari Shashank Singh Thakur

Email: harishashanksinghthakur@gmail.com

Phone: (971)-427-9428

Location: Corvallis, Oregon

Country: USA

Position Category: Verification: Design verification

Desired Job Role: Design Verification and Validation Engineer

University: Portland State University, Osmania University

Degree: Master of Science in Electrical and Computer Engineering, Bachelor of Engineering in Electronics and Communication

Graduation Year: 2024, 2022

Years Experience: 1.5

Notable Companies: Onsemi

Top Skills: System Verilog, Verilog, C, UVM/UVM Framework, Python, Windows, Linux, MESI, AHB, APB, AXI, I2C, SPI, UART, DDR

Job Intention: full-time

Visa Status: H1B

Able: yes

Subject: Looking for Full Time Opportunities starting Immediately

Date Sent: 03-24-2025

Resume: Yes