

Summary:

Roshan Samuel has 23 years of experience as SOC, FPGA and Microcontroller Architect and Team Lead, with 7 years of experience as a designer with Verilog and PERL scripting. He has 17 issued patents and 1 pending. He has a proven track record of translating rough or vague ideas into detailed, designable specifications and presenting ideas to a wide audience.

Name: Roshan Samuel

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Phone: (480) 326 3085

Location: Chandler, AZ

Country: USA

Position Category: Architecture

Desired Job Role: Silicon Chip Architect

University: Syracuse University, Manipal Institute of Technology

Degree: Masters Degree in Computer Science, Bachelors Degree in Electronics and telecommunications Engineering

Graduation Year: 1995, 1993

Years Experience: 30

Notable Companies: Microchip Technology Inc., Texas Instruments

Top Skills: Verilog, C, PERL, architecture expertise in ADC-based touch interfaces, I2C, SPI, FPGA fabric, AXI bus interface, embedded regulators, UARTs, programmable CRC calculators

Job Intention: full-time

Visa Status: --

Able: no

Subject: Resume

Date Sent: 03-09-2025

Resume: Yes