Dear Hiring Team,

I am writing to express my interest in the

Senior Formal Verification Engineer

position at

Ahead Computing

.

I hold a

Ph.D. in Electrical Engineering

, where my research focused on applying

Formal Methods to verify Latency-Insensitive Circuits

. I bring over

9 years of industry experience

, including nearly

4 years leading Formal Verification projects

for complex

coherency agents in server SoCs

.

Prior to that, I spent over

5 years as a Digital Design Engineer

at a startup, where I was involved in the

entire front-end design flow

. My role also extended into

firmware development

for the chip support package, and I had the opportunity to

study neural networks

and

implement kernel functions

for deploying neural nets using the

DSP subsystem

.

I would welcome the opportunity to bring my deep technical expertise and leadership experience to

the team at Ahead Computing.

Thank you for your time and consideration.

Best regards,

Vidura

Wijayasekara

(701) 566-4089