

Summary:

Adwyck Gupta is an MS EE student at Columbia University with hands-on experience in formal verification flow and tools. He has successfully verified a VGA monitor-based game controller on FPGA and is currently working on a RISC-V-based GPU vortex. He has prior experience working at Bhabha Atomic Research Centre, Maven Silicon Softech Ltd, and Computational Intelligence Laboratory, IISc.

Name: Adwyck Gupta

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Phone: +1 (332) 265-9048

Location: New York City

Country: USA

Position Category: Verification: Formal verification

Desired Job Role: Formal Verification Engineer

University: Columbia University, Vellore Institute of Technology

Degree: M.S. in Electrical Engineering, B.E. in Electrical and Electronics Engineering

Graduation Year: 2025

Years Experience: 2

Notable Companies: Bhabha Atomic Research Centre, Maven Silicon Softech Ltd, Computational Intelligence Laboratory, IISc

Top Skills: Verilog/ SystemVerilog, SVA, VHDL, Perl, Python, C/C++, System C, tcl, MATLAB, R

Job Intention: full-time

Visa Status: --

Able: no

Subject: Working at AheadComputing

Date Sent: 03-10-2025

Resume: Yes