Summary:

Suhrud Kelkar is a Design and Verification Engineer currently working at Intel, Oregon. He has a Masters degree in Electrical & Electronics Engineering from California State University, Sacramento. He has experience working with Intel Corporation and Analog Devices. His top skills include RTL Logic design & Verification, Python, Perl, C, LabVIEW among others. He is currently looking for new opportunities in the field of design verification.

Name: Suhrud Kelkar

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Phone: +1(916 796 7832)

Location: Hillsboro, OR

Country: USA

Position Category: Verification: Design verification

Desired Job Role: Formal Verification Engineer

University: California State University, Sacramento

Degree: Masters in Electrical & Electronics Engineering

Graduation Year: 2019

Years Experience: 3

Notable Companies: Intel Corporation, Analog Devices

Top Skills: RTL Logic design & Verification, Python, Perl, C, LabVIEW, DVT, DVE, Verdi, Modelsim, VCS, Conformal, DesignVision, Vivado, Visio, Virtuoso, VCLINT, VCLP, UPF

Job Intention: full-time

Visa Status: --

Able: yes

Subject: Job opportunities at ahead computing.

Date Sent: 02-23-2025

Resume: Yes