Hello,

My name is Mark Zakharov and I have interest in the

"

Senior CPU Microarchitecture & Logic Design Engineer, Execution Unit

" posting found on your website.

For context, I have been working on RISC-V ASIC development and research for the last 4 years, with 2 years spent doing a Masters and 2 years spent working at Esperanto Technologies, a RISC-V startup where I did Emulation and RTL work on our VPU/FPU designs, among other things.

Attached is my resume, and if my qualifications are of interest I would love to set up a call to discuss the position further!

Thank you for your time,

Mark Zakharov