

Dear AheadComputing Team, Dear Debbie Marr,

I recently finished my Studies in EE at ETH Zurich. I have learned the basics of VLSI Design at IIS from Luca Benini and Frank. I have no dedicated work experience in designing the chips you are aiming to build, but I am learning fast and deliver results as requested and beyond.

I want to learn (from) the best, and the best is imho RISC-V. I would be delighted to be considered for a Junior CPU Microarchitecture & Logic Design Engineer or an Internship.

Attached you find my ETH Certificate and Grade as well as my academic CV.

Cheers

Eike