SRIJAN YENDLURI

Email: yendlurisrijan@gmail.com
Phone: +1 (945) 257 6990
Address: 84 Church Avenue, Brooklyn, NY
Nationality: American

Motivated student working towards a computer engineering degree. Hardworking and resourceful individual commended for first-rate collaboration, organizational and time management abilities. Committed to developing a career path and expanding work experience.

EDUCATION

- *Bits Pilani (B.E. hons Electronics and communications engineering) – 2024*
- *New York University (M.S. Computer Engineering) - 2026 (expected)*

Relevant coursework: VLSI Design, Analog design, Digital design, Microprocessor and interfacing, Electronic devices, Embedded Systems, Computer Architecture

PROJECTS

- **AI for hardware – with Prof. Siddharth Garg** ***(ongoing)***
  - ➢ Leveraging OpenAI and DeepSeek APIs to generate and optimize HDL code for efficient hardware design.Exploring AI-assisted synthesis for RTL-to-GDS automation.
- **CNN on FPGA** ***(ongoing)***
  - ➢ Implemented a low-bit quantized CNN (INT4/INT8) on the Cora Z7 FPGA (Zynq-7000 SoC), optimizing for inference speed and energy efficiency; used SystemVerilog and HLS for hardware design, with ARM Cortex-A9 core for control and Vivado for synthesis and co-simulation.
- **Cache Performance Simulation:**
  - ➢ Developed a cache simulator to analyze L1/L2 performance, hit/miss rates, and replacement policies.Simulated direct-mapped, set-associative, and fully associative caches to optimize memory hierarchy. Automated performance analysis and visualization using Python & R
- **Five stage pipeline processor**
  - ➢ Designed a five-stage pipelined 16-bit RISC-V processor in Verilog with static branch prediction, hazard detection, and data forwarding; verified functionality through simulation and improved instruction throughput by minimizing pipeline stalls.
- **Generation of Lecture notes**
  - ➢ Automatically generate technical lecture notes by leveraging knowledge bases, unstructured/semi-structured data through NLP, utilizing Python, JavaScript, and web technologies.

INTERNSHIPS

- **OSI Digital**
  - ➢ Developed an AI application using OCR to extract data from invoices and store it in database objects. Integrated an NLP-based semantic comparison feature for accurate data matching and validation. Automated the process using cloud-based machine learning with a Python stack.
- **Swecha Voice Modules**
  - ➢ Built an OCR extraction engine using python. Used GIT for version control and documented the entire project to assist the development team.
- **Sagility Health**
  - ➢ Designed a Python script utilizing AWS Transcribe APIs for call recording transcription and leveraged OpenAI LLMs for automated medical insurance form completion. Made user friendly UI for same feature and integrated into AWS Stack

TECHNICAL SKILLS: Verilog HDL, IoT using Arduino, Xilinx, LTSpice, Wireshark, Cadence Virtuoso, C, C++, Python, MATLAB, Perl,TCL.