Hi,

I hope my email finds you well. My name is Sanket Patil, and I am actively seeking full-time opportunities in

Design Verification

. I recently completed my Masters degree in

Electrical and Computer Engineering

at

Portland State University

(June 2024) with a GPA of 3.96.

I also gained hands-on industry experience during my internship at

AMD

as a

CPU Post-Silicon Validation

Intern, where I focused on automation and validation processes.

I have strong academic foundation in

Computer Architecture, System Verilog,

Assertion-Based Verification, Formal Verification, and advanced methodologies like UVM,

RTL Design and ASIC workflows, STA, and coding languages like SV, C/C++, Python.

I have attached my resume for your review and would greatly appreciate it if you could share any relevant opportunities or insights about roles that align with my skills. Thank you very much for your time and consideration, and I look forward to hearing from you.

Best regards,

Sanket Patil

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