

## Summary:

Dedicated and knowledgeable Sr. Systems Engineer with over a decade of experience in Hardware design, FPGA design, and embedded systems. With a solid background in Python, I've effectively used my skills for automation and streamlining design processes for 10 years. My career includes impactful roles at Intel Corporation and Plexus Corp, where I focused on hardware design, system architecture, and collaborative team efforts.

Name: Jose David Bolaos Rodriguez

Email: ing.jdavidbr@gmail.com

Phone: +52 3332707494

Location: Guadalajara, Mexico

Country: Mexico

Position Category: Design: Logic design

Desired Job Role: Senior Systems Engineer & FPGA Design Specialist

University: University of Colima

Degree: Bachelor of Engineering. Electronics and Communication

Graduation Year: 2013

Years Experience: 10

Notable Companies: Intel Corporation, Plexus Corp, A2e technologies

Top Skills: Systems Engineering, FPGA Design, SoC Architecture, Embedded Systems, C/C++ Programming, Hardware Design, PCB Layout, Signal Integrity Simulation, Schematic Capture, Power Sequencing, Cross-functional Collaboration, IoT Connectivity, Mechatronics, Sensor Integration, Post-Silicon Validation

Job Intention: full-time

Visa Status: --

Able: yes

Subject: Working at AheadComputing -Senior Design Enablement Engineer

Date Sent: 02-19-2025

Resume: Yes