

Summary:

Shreesha T P is a B.Tech student in Electronics and Communication Engineering from India. He has experience as an intern at Samsung R&D and has worked on several projects related to RISC-V CPU design. He has a strong interest in semiconductor design and innovation.

Name: Shreesha T P

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Phone: 9483882803

Location: Bangalore

Country: India

Position Category: Internship

Desired Job Role: RISC-V CPU Design and Verification Engineer

University: Ramaiah Institute of Technology, Indian Institute of Technology Madras

Degree: B.E in Electronics and Communication, B.S. Data science and Programming

Graduation Year: 2025

Years Experience: 1

Notable Companies: Samsung R&D

Top Skills: Verilog, SystemVerilog, Python, MATLAB, C/C++, Vivado, Cadence, EDA Playground, Proteus, IAR Workbench, Keil, MATLAB, Simulink, Git, GitHub

Job Intention: Internship

Visa Status: --

Able: No

Subject: Internship Opportunities at Ahead Computing

Date Sent: 02-24-2025

Resume: Yes