Summary:

Miguelangel Miranda is a senior Electrical Engineering student at Arizona State University with over three years of hands-on experience, including an internship as a Process Engineer at Intel Corporation. He has built a strong foundation in system design, automation, and hardware-software integration, working with MATLAB, Python, VHDL, Verilog, and C, along with designing and testing wireless communication frameworks.

Name: Miguelangel Miranda

Email: mtmiran1@asu.edu

Phone: 6026223079

Location: Mesa, AZ 85209

Country: USA

Position Category: Design: Logic design

Desired Job Role: Process Engineer

University: Arizona State University

Degree: BS, ELECTRICAL ENGINEERING: Electrical Engineering

Graduation Year: 2025

Years Experience: 3

Notable Companies: Intel Corporation, Arizona State University

Top Skills: C, Python, HTML, Java, Verilog, SystemVerilog, VHDL, Microsoft Office, SQL, MATLAB, SOLIDWORKS

Job Intention: full-time

Visa Status: --

Able: no

Subject: Inquiry About Career Opportunities at Ahead Computing

Date Sent: 03-03-2025

Resume: Yes