

Summary:

Eike Himstedt is a recent graduate from ETH Zurich with a Masters in Information Technology and Electrical Engineering. He has a background in semiconductor research, machine learning, and next-generation communications. He has expressed interest in a Junior CPU Microarchitecture & Logic Design Engineer position or an Internship.

Name: Eike Himstedt

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Location: Witikonstrasse 399, 8053 Zurich

Country: Switzerland

Position Category: Design: Logic design

Desired Job Role: Junior CPU Microarchitecture & Logic Design Engineer

University: ETH Zurich, University of Duisburg-Essen

Degree: MSc Information Technology and Electrical Engineering, BSc Electrical Engineering and Information Technology

Graduation Year: 2024, 2022

Years Experience: 3

Notable Companies: Siemens, ETH - IEF, ETH - PBL, ETH - Neuroinformatics Lab, ETH - PMS, University of Duisburg Essen

Top Skills: Python, Matlab, C, System Verilog, Cadence Innovus, Vivado, Questasim

Job Intention: full-time

Visa Status: --

Able: no

Subject: Working at AheadComputing

Date Sent: 02-19-2025

Resume: Yes