Summary:

Experienced VLSI professional with expertise in synthesis, P&R, signoff flows, and methodology. Worked in leadership roles at Intel, Qualcomm, and Synopsys.

Name: Balaji Velikandanathan

Email: znath@yahoo.com

Phone: +1-(425)-770-7186

Location: Austin, TX 78717

Country: USA

Position Category: --

Desired Job Role: Senior VLSI Design Engineer

University: Syracuse University, Guindy Engineering College, Anna University

Degree: M.S. in Computer Science, B.E. in Electronics and Communication Engineering

Graduation Year: --

Years Experience: --

Notable Companies: Intel, Qualcomm, Synopsys

Top Skills: VLSI design, RTL2GDS implementation, DFx verification, EDA tools

Job Intention: --

Visa Status: --

Able: no

Subject: Resume

Date Sent: 03-20-2025

Resume: Yes