Hadi Mirzaee

Senior Electronic Engineer

DoB: 1981-12-01

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📍 Tehran, Iran

Skills

- RTL Design
- Synopsys Design Compiler
- GaAs Digital Integrated Circuits
- Cadence Virtuoso Spectre
- Cadence Virtuoso Layout Suite
- FPGA: Zynq7xxx
- Analog: A2D , PLL , CDR , OPAMP , Gm-C , SC Circuits
- DSP: Ti TMS320 Family, Freescale DSP56xx
- Keysight ADS
- The MathWorks MATLAB
- Texas Instruments Code Composer Studio
- Xilinx Vivado
- Microcontroller: MSP430 , ARM ,...

Language

English

Reading	▰▰▰▰▱
Writing	▰▰▰▰▱
Speaking	▰▰▰▱▱
Listening	▰▰▰▰▱

Senior Electronic Engineer

Currently focused on Digital ASIC and RTL Design, I am a highly skilled electronic design engineer with over 19 years of experience in both digital and analog domains. I excel in designing, simulating, and laying out analog circuits such as A2D converters, PLLs, OP-AMPs, and CDRs in CMOS, as well as digital circuits in GaAs. Additionally, I am proficient in developing and implementing digital algorithms and protocols using DSPs, ARM processors, and FPGAs, with experience in SDR Communication Systems.

Education

PhD in Electronic Engineering

Branch: Analog Integrated Circuits

Institute/University: Tarbiat Modares University

Tehran, Iran

2021 - Present

GPA: 18.7 /20

Master of Electronic Engineering

Branch: Analog Integrated Circuits

Institute/University: Shahid Beheshti University

Tehran, Iran

2019 - 2021

GPA: 19.2 /20

Bachelor of Electrical Engineering

Branch: Electronic

Institute/University: Ferdowsi university of Mashad

Mashhad, Razavi Khorasan, Iran

2000 - 2004

Work Experience

Senior Electronic Engineer

Iran Semiconductor Industry

Tehran, Iran

February 2019 - May 2024

Senior Electronic Engineer

Iran Sama Co.

Tehran, Iran

May 2010 - January 2019

German

Reading	▪▪▪▫▫
Writing	▪▪▫▫▫
Speaking	▪▪▪▫▫
Listening	▪▪▫▫▫

Social Network

in **linkedin.com/in/hadi-mirzaee-413aa3158**

Electronic Engineer

Iran FAVA Co.

Tehran, Iran

September 2005 - April 2010

Projects

Collaboration in RISC-V Processor ASIC Design

For: University of Tehran

Collaboration in the ASIC Design of a General-Purpose microcontroller

For: Sharif University of Technology

Design and Implementation of Algorithms and Protocols for Electronic and Communication Systems

For: Tarbiat Modares University

Design and implementation of algorithms by TI and Motorola DSPs, ARM Processors and Xilinx FPGAs for SDR Projects

Design and post layout simulation of Analog Circuits: A2D (Flash,Pipline,DSM), PLL, CDR, OP-AMP and Comparator

For: Tarbiat Modares University

SPI Module Design by GaAs Technology

For: Tarbiat Modares University

configuration of a phase shifter and an attenuator in a C-band core chip

Personal interests

AI Processor

Digital and Analog Circuit Design - ASIC

Design and Implementation of Algorithms and protocols on DSPs,FPGAs and Processors.

Design of Analog Integrated Circuits in CMOS technology

MMIC and RFIC

Mixed-Signal Design