Hi Hiring Manager,

I am Ashish Ajayakumar, a Computer Engineering graduate from NC State University specializing in ASIC Design Verification and Computer Architecture.

I wanted to reach out to you regarding Senior CPU Microarchitecture & Logic Design Engineer, Execution Unit, and Design Verification Engineer opportunities at Ahead Computing

Highlights of my skills:

 Proficient in writing automation test scripts in Tcl and Python.

 Developed test plans for validating different Wi-Fi features as part of post-silicon validation.

 Expert Verilog design for complex digital circuits and processors.

 Application of computer architecture principles for performance optimization.

 Creation of accurate cache simulators in C/C++ for system behavior analysis.

 Development of robust UVM/SystemVerilog testbenches for design quality assurance.

I have attached my resume for your reference.

Please let me know the next steps in the process.

Thanks and regards

Ashish