

Summary:

Harry Hsiao is a first-year Computer Engineering masters student at North Carolina State University with a GPA of 4.0. He has a Bachelor of Science in Electrical Engineering from National Taipei University. He is skilled in Verilog, SystemVerilog, C++, ModelSim, QuestaSim, Vivado, Xilinx ISE, Klayout, Design Compiler/Vision, Innovus, and mflowgen. He is seeking an internship to apply his expertise in architecture, design, and verification.

Name: Harry Hsiao

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Phone: +1 984 379 1874

Location: Raleigh, NC

Country: USA

Position Category: Internship

Desired Job Role: Computer Architecture, Design, and Verification Intern

University: North Carolina State University, National Taipei University

Degree: Master's in Computer Engineering, Bachelor of Science in Electrical Engineering

Graduation Year: 2026

Years Experience: 1

Notable Companies:

Top Skills: Verilog, SystemVerilog, C++, ModelSim, QuestaSim, Vivado, Xilinx ISE, Klayout, Design Compiler/Vision, Innovus, mflowgen

Job Intention: Internship

Visa Status: --

Able: No

Subject: Inquiry About Volunteer Internship Opportunity

Date Sent: 04-19-2025

Resume: Yes