

Summary:

Pranava Krishnamurthy Bhat is a graduate student at Texas A&M University, pursuing a Masters of Science in Electrical Engineering. He has a combined experience of 3 years working as an FPGA and Software engineer. He has worked on RISC-V in a project and is interested in roles in VLSI Design and Verification.

Name: Pranava Krishnamurthy Bhat

Email: bpranavak@gmail.com

Phone: +1 979-344-6831

Location: College Station, Texas

Country: USA

Position Category: Internship

Desired Job Role: VLSI Design and Verification Engineer

University: Texas A&M University, Visveswaraya Technological University

Degree: Masters of Science, Electrical Engineering, Bachelors of Engineering

Graduation Year: 2024

Years Experience: 3

Notable Companies: Mphasis India Ltd, Senpronics PVT Ltd

Top Skills: HDL and Design, Programming, Tools and Technologies

Job Intention: Internship

Visa Status: --

Able: No

Subject: Enquiring about potential Internship opportunities for Summer 2025

Date Sent: 03-22-2025

Resume: Yes