Summary:

Mathura Swaminathan is a MS-CE graduate from NC State with experience as a DV Engineer at Renesas Electronics. She has skills in Computer Architecture, ASIC design and verification, and is proficient in Verilog, System Verilog, UVM methodology, python scripting, regex, SVN, and C++ programming. She is looking for immediate positions and is open to relocation.

Name: Mathura Swaminathan

Email: mathura.swaminathan@gmail.com

Phone: 984-895-9340

Location: Raleigh, NC, USA

Country: USA

Position Category: Design: Logic design, Verification: Design verification

Desired Job Role: Senior CPU Microarchitecture & Logic Design Engineer

University: North Carolina State University, Anna University

Degree: Master of Science in Computer Engineering, Bachelor of Engineering in Electronics and Communication

Graduation Year: 2023

Years Experience: 7

Notable Companies: Renesas Electronics America Inc., DANFOSS, DATA PATTERNS Ltd.

Top Skills: Computer Architecture, ASIC design, verification, Verilog, System Verilog, UVM methodology, python scripting, regex, SVN, C++ programming

Job Intention: full-time

Visa Status: --

Able: no

Subject: Working at AheadComputing

Date Sent: 04-07-2025

Resume: Yes