

I hope you're doing well.

Im writing to express my interest in the CPU Performance Architect position in Portland, OR. With a Masters in Computer Engineering and a solid foundation in CPU microarchitecture, performance modeling, and RTL design, Im excited by the opportunity to contribute to your team.

Through hands-on experience at DRDL and Silicon Engineers, Ive worked extensively with SystemVerilog, UVM, and Verilog HDL, focusing on performance verification and digital system design. My recent academic projects, such as RISC-V core functional verification and PPA-optimized DSP algorithm implementation, have deepened my expertise in CPU simulation, performance analysis, and power-area trade-offs.

Ive also used Python scripting for automation and performance data analysis, and have familiarity with the RISC-V ISA, making me confident in my ability to contribute meaningfully to pre-silicon performance investigations and architectural refinements.

Please find my resume attached. Id be grateful for the opportunity to discuss how my background and passion for performance architecture align with your goals.

Looking forward to hearingfromyou.

Regards,

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LinkedIn

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GitHub