

Dear Ahead Computing Team,

I hope youre doing great!

My name is Harshith Reddy Surakanti, and Im reaching out to express my enthusiasm for the groundbreaking work AheadComputing is doing. With over three years of experience spanning RTL to GDSII, FPGA/ASIC development, and workflow automation, the opportunity to be part of a team thats building RISC-V application processors.

Heres a quick snapshot of what I bring:

Hands-on accelerator designfor edge inference and image processing using FPGAs and SoCs

RTL and physical design expertise, including processor tapeouts (RISC-V & MIPS) and IP development

Automation with Python/TCLfor full-chip flows and physical signoff

Familiarity with the evolving RISC-V ecosystem and reconfigurable computing paradigms

Ive attached my resume for your review. Whether or not theres a current role thats an exact fit, Id love to explore ways I can contribute to Aheads mission.

Thank you for your timeand Im excited at the possibility of building the future with you.

Best,

Harshith Reddy