

Summary:

Alejandro Iglesias is a Verification Engineer with a passion for self-learning and improvement. He has experience working at Intel Corporation and has skills in UVM, SystemVerilog, Python, C, and C++. He has degrees from the University of Alberta and the Autonomous University of Guadalajara.

Name: Alejandro Iglesias

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Location: Guadalajara, Jalisco

Country: Mexico

Position Category: Verification: Design verification

Desired Job Role: Senior CPU Verification Engineer

University: University of Alberta, Autonomous University of Guadalajara

Degree: Electrical and Computer Engineering Exchange Student, Electrical and Biomedical Engineering

Graduation Year: 2021

Years Experience: 2

Notable Companies: Intel Corporation

Top Skills: UVM, SystemVerilog, Python, C, C++

Job Intention: full-time

Visa Status: --

Able: yes

Subject: Working at AheadComputing - Senior CPU Verification Engineer - Guadalajara, Mexico

Date Sent: 02-17-2025

Resume: Yes