

Hi,

I came across the Formal Verification position at Ahead Computing and wanted to express my interest. With my experience in formal verification at AMD, where I worked extensively on SystemVerilog Assertions, load-store unit verification, and formal property checking using VC Formal/Jasper, I believe I would be a strong fit for this role.

I'd love the opportunity to learn more about the position and discuss how my skills align with your team's needs. Looking forward to your response.

Regards,

Pavan Kotla