

Summary:

Rohit Joshi is a Master's student in Electrical and Computer Engineering at the University of Michigan, Ann Arbor with a strong foundation in VLSI design and computer architecture. He has experience in digital design, verification, and processor architecture. He has worked on projects such as designing a reservation station, reorder buffer, and store queue for a superscalar MIPS R-10K style out-of-order processor, and implementing Winograd transformation and ReLU units for a CNN accelerator.

Name: Rohit Shriram Joshi

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Location: Ann Arbor

Country: USA

Position Category: Design: Logic design, Verification: Design verification, Architecture

Desired Job Role: RTL Design Engineer

University: University of Michigan, Ann Arbor, University of Mumbai

Degree: Masters of Science in Electrical and Computer Engineering, Bachelor of Technology in Electronics Engineering

Graduation Year: 2025, 2023

Years Experience: 2

Notable Companies: University of Michigan, SR Systems, Meshnet Electronics, Indian Institute of Technology, Bombay

Top Skills: System Verilog, Verilog, VHDL, MATLAB, C, TCL, Python, Assembly Language Programming

Job Intention: full-time

Visa Status: --

Able: no

Subject: Application for CPU RTL Design Role

Date Sent: 03-04-2025

Resume: Yes