Hi,

I hope you're doing well. My name is Raghul, and I am a recent Computer Engineering graduate with expertise in computer architecture, digital ASIC design, and design verification. A company-wide restructuring recently impacted me, and I am now actively seeking new opportunities in computer architecture.

Ive worked on various projects, including:

Building 9-stage out-of-order processor simulator in C++.

Designing a fully synthesizable multi-stage neural network accelerator using Verilog.

Implementing coherence protocols (MSI, MESI) for distributed systems with Snoop filter.

Implementing Check Point Processing Recovery (CPR) to establish a wider instruction window with smaller Physical Registers.

I have seen below 4 roles that are relevant to myskillsets.

Senior CPU Microarchitecture & Logic Design Engineer, Memory Execution

Senior CPU Microarchitecture & Logic Design Engineer, Fetch Decode

Senior CPU Microarchitecture & Logic Design Engineer, Out-of-Order Execution

Senior CPU Microarchitecture & Logic Design Engineer, Execution Unit

I would love the opportunity to discuss how my skills and background could be a good fit for any open roles at AheadComputing Inc.

Ive attached my resume for your reference.

Thank you for your time and consideration. I look forward to hearing from you.

Regards

Raghul Srinivasan

LinkedIn

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GitHub