Summary:

Raghul Srinivasan, a recent graduate with a degree in Computer Engineering, has a strong background in computer architecture, RTL design, and verification. He has experience working at Analog Devices and Syrma SGS Technology Ltd. He is skilled in several programming languages, design and verification, and using various tools and technologies.

Name: Raghul Srinivasan

Email: rsriniv7@ncsu.edu

Phone: 860-962-1742

Location: Wilmington, MA

Country: USA

Position Category: Design: Logic design, Verification: Formal verification

Desired Job Role: Senior CPU Microarchitecture & Logic Design Engineer

University: North Carolina State University, SASTRA Deemed University

Degree: Master of Science in Computer Engineering, Bachelor of Technology in Electronics and Communication Engineering

Graduation Year: 2024

Years Experience: 7

Notable Companies: Analog Devices Inc., Syrma SGS Technology Ltd.

Top Skills: Computer Architecture, Design and Verification, Programming languages: C, C++, Python, Verilog, SystemVerilog, Simulation/ Emulation Platforms, Tools and Technologies

Job Intention: full-time

Visa Status: --

Able: no

Subject: Seeking Opportunities in Computer Architecture, Design and Verification

Date Sent: 03-12-2025

Resume: Yes