Summary:

Mitchell Sharum is a senior at Harvard University pursuing a B.S. in electrical engineering with a secondary in computer science. He has a strong interest in RTL design and verification and is looking for full-time work after graduation in May. He has experience with SystemVerilog, C++, and Python among other skills. He has worked at Harvard University, Correct Designs Incorporated, and Corvalent Corporation.

Name: Mitchell Sharum

Email: msharum@college.harvard.edu

Phone: 432-770-2342

Location: Cambridge, Massachusetts 02138

Country: USA

Position Category: Design: Logic design, Verification: Design verification

Desired Job Role: FPGA and ASIC Design and Verification Engineer

University: Harvard University, Trinity School of Midland

Degree: B.S. in electrical engineering with a secondary in computer science

Graduation Year: 2025

Years Experience: 1

Notable Companies: Harvard University, Correct Designs Incorporated, Corvalent Corporation

Top Skills: SystemVerilog, C++, Python, Perl, FPGA, MATLAB, LTspice, Xilinx Vivado, Tcl, Git, Xsim

Job Intention: full-time

Visa Status: --

Able: no

Subject: Working at AheadComputing

Date Sent: 03-27-2025

Resume: Yes