Summary:

Steen Larsen is an innovative CPU Architect with extensive experience in defining and optimizing advanced server, platform, and IP architectures for Intel Datacenter. His expertise spans micro-architecture design, SoC-level features, and high-impact problem-solving. He has a proven track record in developing architecture specifications, leading cross-functional collaboration, and resolving critical issues for platforms.

Name: Steen Larsen

Email: steen.knud.larsen@gmail.com

Phone: 503-825-8729

Location: Portland OR

Country: USA

Position Category: Architecture

Desired Job Role: CPU Architect

University: Oregon State University, OHSU/OGI

Degree: PhD, Electrical and Computer Engineering, MST, Management in Science and Technology, MS, Electrical and Computer Engineering, BS, Computer Engineering

Graduation Year: --

Years Experience: 9

Notable Companies: Intel Corporation

Top Skills: CPU Architecture, SoC Design, Micro-Architecture, Performance Optimization, Memory Coherency, SystemVerilog RTL, Agile Methodology, Presilicon Postsilicon Debugging and Testing, Platform Architecture, Latency and Power analysis, Kernel Cloud Dev, AI workloads, Stakeholder Collaboration

Job Intention: full-time

Visa Status: --

Able: yes

Subject: Steen Larsen for PDX uarch/RTL role

Date Sent: 02-20-2025

Resume: Yes