

Summary:

Andr Alberto Contreras Rivera is a CPU Design Verification Engineer with experience at Intel Corporation. He has a Bachelor's degree in Electric Technologies Engineering and a Diploma in ASIC Design and Verification. He has skills in various programming languages and EDA tools.

Name: Andr Alberto Contreras Rivera

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Phone: +52 477 235 1408

Location: Zapopan, Mexico

Country: Mexico

Position Category: Verification: Design verification

Desired Job Role: CPU Design Verification Engineer

University: Instituto Tecnolgico y de Estudios Superiores de Monterrey, Centro de Investigacin y de Estudios Avanzados del IPN

Degree: Bachelor of Science Degree in Electric Technologies Engineering, Diploma of Education in ASIC Design and Verification

Graduation Year: 2023

Years Experience: 2

Notable Companies: Intel Corporation

Top Skills: Python, C/C++, Java, Bash, TCL/Pearl, SystemVerilog, Universal Verification Methodology (UVM), DVT Eclipse IDE, Cadence Xcelium, Cadence VManager, Synopsys VCS/Verdi

Job Intention: full-time

Visa Status: --

Able: yes

Subject: Working at AheadComputing

Date Sent: 02-18-2025

Resume: Yes