

Summary:

Experienced in digital design, verification, and processor architecture with a strong background in VLSI design and computer architecture. Currently pursuing a Master's in Electrical and Computer Engineering at the University of Michigan, Ann Arbor.

Name: Rohit Joshi

Email: rsjoshi@umich.edu

Phone: +1(734)358-1612

Location: Ann Arbor, USA

Country: USA

Position Category: Design: Logic design

Desired Job Role: RTL Design Engineer

University: University of Michigan, Ann Arbor, University of Mumbai

Degree: Masters of Science in Electrical and Computer Engineering, Bachelor of Technology in Electronics Engineering

Graduation Year: 2025

Years Experience: 3

Notable Companies:

Top Skills: SystemVerilog, Verilog, VLSI Design, Computer Architecture, Digital Design

Job Intention: full-time

Visa Status: --

Able: no

Subject: Application for CPU RTL Design Role

Date Sent: 03-04-2025

Resume: Yes