

Summary:

Zia Khan is a results-driven engineering leader with over 15 years of experience in design enablement, CAD tool development, and advanced CPU and GPU design methodologies. He has a proven track record of optimizing design flows, automating processes, and driving efficiency improvements. He has worked at Intel Corp and Synopsys Inc in various roles and has a PhD in Electrical Engineering.

Name: Zia Khan

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Phone: 916-765-5420

Location: Folsom, CA 95630

Country: USA

Position Category: Design: Logic design, Design: Physical Design, Verification: Formal verification, Verification: Design verification

Desired Job Role: Senior Design Enablement Engineer

University: Virginia Polytechnic Institute & State University, University of Kentucky

Degree: PhD, Electrical Engineering, MS, Electrical Engineering

Graduation Year: --

Years Experience: 15+

Notable Companies: Intel Corp, Synopsys Inc

Top Skills: CAD Tool Development & Automation, Scripting, Technology & Roadmaps, Leadership & Mentoring, EDA Vendor Collaboration, Problem Solving, CPU & ASIC Design Methodologies, Verilog, RTL Design, Physical Synthesis, Place and Route, Low Power, Timing Convergence, Verification, Formal Verification, Equivalence Checking, Power Optimization, Standard Cells, ASIC, SoC, Design Technology Co-optimization (DTCO), Power-Performance-Area (PPA) Improvement, Machine Learning (ML) Enablement

Job Intention: full-time

Visa Status: --

Able: no

Subject: Application for Senior Design Enablement Engineer Position

Date Sent: 02-20-2025

Resume: Yes