Summary:

Swapnil Bhole is a Masters student at the University of Michigan Ann Arbor with a focus on Electrical and Computer Engineering. He has over 5 years of experience in wireless system design, with strong expertise in RTL and SOC design, verification, and ASIC/FPGA development. He has a deep understanding of processor microarchitectures and high-performance computing. Swapnil is passionate about optimizing hardware efficiency, developing scalable architectures, accelerating computational workloads, and driving innovation.

Name: Swapnil Bhole

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Phone: +1 7347471756

Location: Ann Arbor, Michigan

Country: USA

Position Category: Internship

Desired Job Role: RISC-V CPU Core IP Design Intern

University: University of Michigan Ann Arbor, NIT Nagpur

Degree: Masters, Electrical and Computer Engineering, Bachelors, Electronics and Communication Engineering

Graduation Year: 2026, 2018

Years Experience: 5

Notable Companies: Centre for Development of Telematics (CDOT) Bangalore

Top Skills: C, SystemVerilog, Python, MATLAB, Embedded C, Synopsys VCS, Verdi, strong debugging skills, PCB design, Antenna design, Vivado, Vitis, Petalinux, HFSS, Eagle PCB, Keil C, Arduino, AVR Studio, Wireshark, Spectrum Analyzer, VSA, signal generator, Polaris EPC Simulator, Amarisoft eNodeB, TM500

Job Intention: Internship

Visa Status: --

Able: No

Subject: Regarding Summer 2025 internships

Date Sent: 04-02-2025

Resume: Yes