Summary:

Shravan Ramamoorthy is a recent Masters graduate in Electrical & Computer Engineering from UIUC, with a strong focus on digital design and verification. He has built and verified complex systems like cache controllers, pipelined RISC-V CPUs, and branch predictors using SystemVerilog and UVM. He also has two years of professional software development experience.

Name: Shravan Ramamoorthy

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Phone: +1 (217) 904-8896

Location: Champaign, Illinois

Country: USA

Position Category: Verification: Design verification

Desired Job Role: Design Verification Engineer

University: University of Illinois at Urbana-Champaign, SRM Institute of Science and Technology

Degree: Masters, Electrical & Computer Engineering, Bachelors, Electronics and Communication Engineering

Graduation Year: 2025

Years Experience: 2

Notable Companies: Nunify

Top Skills: SystemVerilog, Python, C/C++, UVM, Constrained-Random, Assertions-Based and Metric-Driven Verification, Object Oriented Programming, Synopsys VCS, Verdi, gem5 Simulator

Job Intention: full-time

Visa Status: --

Able: no

Subject: Seeking Design Verification Opportunities

Date Sent: 04-06-2025

Resume: Yes