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Dear Hiring Manager,

I am excited to apply for the Senior CPU Microarchitecture & Logic Design Engineer position at AheadComputing. With eight years of experience in digital design and microarchitecture, I have developed expertise in out-of-order execution, instruction scheduling, and CPU subsystem design.

In my previous roles, I have:

- Defined and refined microarchitecture specifications for complex CPU subsystems.
- Designed and implemented RTL to achieve optimal power, performance, and area (PPA) while ensuring timing closure.
- Developed and optimized out-of-order execution pipelines, including allocation, renaming, scheduling (I can design it using only gates and multiplexers, and I can calculate the critical path for the logic levels of the RS), and retirement.
- Collaborated with verification teams to develop validation strategies and assist with debugging on simulation.
- Worked cross-functionally with physical design teams to meet timing, reliability, and power requirements.

I am particularly passionate about CPU microarchitecture and performance optimization, and I am excited by the opportunity to work in a fast-paced, innovative environment. I believe my background in ALU, floating-point, and vector operations aligns well with the preferred qualifications for this role.

I have attached my resume for your review. I would welcome the opportunity to discuss how my experience can contribute to your team's success.

Thank you for your time and consideration. I look forward to your response.

Best regards,

Alin