

Summary:

Ryan Buchner is a results-driven engineer with a strong background in Risc-V, design verification, hardware design, and operating systems. He has experience in automation and verification tools that help maximize designer efficiency. He has worked as a Design Verification Engineer, Logic Design Engineer, and Kernel Software Engineer at Aril Inc, and as a Machine Learning Engineer at Quon Lab.

Name: Ryan Buchner

Email: buchner.ryan@gmail.com

Phone: 650-730-0076

Location: Los Gatos, CA

Country: USA

Position Category: Verification: Design verification

Desired Job Role: RISC-V Design Verification Engineer

University: University of California, Davis

Degree: Master of Science - Computer Science, B.S. - Statistical Data Science, B.S. - Genetics and Genomics, Minor - Computer Science

Graduation Year: 2023

Years Experience: 2

Notable Companies: Aril Inc, Quon Lab, Berkeley RISC-V Spike

Top Skills: SystemVerilog, C++, Python, RiscV Assembly, Verilog, Bash, C, UVM, Git, LLVM, Slurm

Job Intention: full-time

Visa Status: --

Able: no

Subject: Ahead Computing Verification Opportunities

Date Sent: 03-03-2025

Resume: Yes