

## Summary:

Ashish Ajayakumar is a Computer Engineering graduate with a specialization in ASIC Design Verification and Computer Architecture. He has experience in writing automation test scripts, developing test plans, Verilog design, computer architecture principles, and creating cache simulators. He has worked with notable companies such as NC State University, Maven Silicon, Advanced Micro Devices (AMD), and Infineon Technologies.

Name: Ashish Ajayakumar

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Phone: +1 9197915842

Location: Raleigh, NC

Country: USA

Position Category: Verification: Design verification, Design: Logic design, Architecture

Desired Job Role: Senior CPU Microarchitecture & Logic Design Engineer, Execution Unit, and Design Verification Engineer

University: North Carolina State University, Model Engineering College

Degree: Master of Science | Computer Engineering, Bachelor of Technology | Electronics and Communication Engineering

Graduation Year: 2024, 2018

Years Experience: 7

Notable Companies: NC State University, Maven Silicon, Advanced Micro Devices (AMD), Infineon Technologies

Top Skills: System Verilog, Verilog, Python, Tcl, C, C++, Perl, Verdi, Modelsim, QuestaSim, Synopsys VCS, Xilinx Vivado, Linux/Unix tools, Git

Job Intention: full-time

Visa Status: --

Able: yes

Subject: Inquiry regarding Senior CPU Microarchitecture & Logic Design Engineer, Execution Unit  
and Design Verification Engineer opportunities at Ahead Computing

Date Sent: 03-19-2025

Resume: Yes