

Summary:

Sharafat Hussain is a Design Verification Engineer with over 2+ years of experience, specializing in the verification of data-centric, out-of-order RISC-V cores. He has worked for 10xEngineers and has a Bachelor of Engineering degree in Electrical and Computer Engineering from the University of Engineering and Technology, Lahore.

Name: Sharafat Hussain

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Phone: +923082467156

Location: Lahore

Country: Pakistan

Position Category: Verification: Design verification

Desired Job Role: CPU Verification Engineer

University: University of Engineering and Technology, Lahore

Degree: Bachelor of Engineering in Electrical and Computer Engineering

Graduation Year: 2020

Years Experience: 2

Notable Companies: 10xEngineers

Top Skills: SystemVerilog, Universal Verification Methodology (UVM), Verilog, Questa Sim Tool, Visualizer, C, RISC-V Assembly, SystemVerilog Assertions(SVA), Git, Python

Job Intention: full-time

Visa Status: --

Able: no

Subject: Working at AheadComputing Application for CPU Verification Engineer

Date Sent: 02-20-2025

Resume: Yes