

Dear Hiring Team,

I am writing to express my interest in potential entry-level positions or internships at AheadComputing. With a Master's degree in Electrical and Computer Engineering from the Illinois Institute of Technology, specializing in VLSI Design and Microelectronics, I have developed a strong foundation in RTL design, functional verification, and FPGA hardware acceleration.

During my tenure as an RTL Design Intern at the National Institute of Electronics & Information Technology (NIELIT) in India, I led functional verification workflows and conducted static timing analysis, resulting in a 10% reduction in design iterations and a 20% acceleration in project timelines. Additionally, as an FPGA Hardware Acceleration Intern at Maven Silicon, I spearheaded the design and implementation of FPGA-based accelerators, enhancing processing performance by 25% across multiple applications.

I am particularly drawn to AheadComputing's innovative work with 64-bit RISC-V application processors and its mission to push IPC performance to new heights. I am eager to contribute my skills and collaborate with your team to drive technological advancements in this field.

I have attached my resume for your review. I would welcome the opportunity to discuss how my background and enthusiasm align with AheadComputing's goals.

Thank you for considering my application.

Best regards,

Keshava Reddygattu

(312) 684-1096

kreddygattu@gmail.com

[LinkedIn Profile](#)

[GitHub Profile](#)