

Summary:

Alin Petru Parcalab is a Senior CPU Microarchitecture & Logic Design Engineer with eight years of experience in digital design and microarchitecture. He has developed expertise in out-of-order execution, instruction scheduling, and CPU subsystem design. He has worked with companies like ALLENGRA GmbH and Capgemini Engineering.

Name: Alin Petru Parcalab

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Phone: 00-40-770/372448

Location: 410291 Oradea, Romania

Country: Romania

Position Category: Design: Logic design

Desired Job Role: Senior CPU Microarchitecture & Logic Design Engineer

University: University of Oradea, Romania

Degree: Bsc in Electronics and Computer Science

Graduation Year: 2023

Years Experience: 8

Notable Companies: ALLENGRA GmbH, Capgemini Engineering

Top Skills: RTL Design, Schematics Analog & Digital, Static Timing Analysis (STA), Clock Domain Crossing (CDC), Logic Equivalence Checking, Synthesis (DC-Compiler), Low Power Design, Asynchronous Design, Micro Architecture

Job Intention: full-time

Visa Status: --

Able: no

Subject: Working at AheadComputing

Date Sent: 03-14-2025

Resume: Yes