

## Summary:

Mallikarjun Kasha is a graduate in Electrical and Computer Engineering with a keen interest in VLSI, ASIC/RTL Design, Verification, and Validation. He has experience in RTL Design in System Verilog, Verilog, and UVM. He has worked with Maven Silicon and Tata Consultancy Services, gaining valuable professional experience. He is looking for a full-time opportunity to contribute his skills and knowledge.

Name: Mallikarjun Kasha

Email: mkasha@pdx.edu

Phone: (971)-429-9963

Location: Portland, OR, 97201

Country: USA

Position Category: Design: Logic design, Verification: Design verification

Desired Job Role: Senior CPU Microarchitecture & Logic Design Engineer

University: Portland State University, VNR Vignana Jyothi Institute of Engineering and Technology

Degree: Master of Science - Electrical and Computer Engineering, Bachelor of Technology  
Electronics and Communication Engineering

Graduation Year: 2025, 2019

Years Experience: 6

Notable Companies: Maven Silicon, Tata Consultancy Services

Top Skills: Verilog, System Verilog, Linux, C, C++, SQL, Python, ASIC Design, Pre-Silicon Validation, FPGA Design, Computer architecture, VLSI, Digital Logic Design, RTL Design, CPU verification, Assertions, Code/Functional Coverages

Job Intention: Full Time

Visa Status: --

Able: Yes

Subject: Senior CPU Microarchitecture & Logic Design Engineer, Out-of-Order Execution role

Date Sent: 03-13-2025

Resume: Yes