

Summary:

Experienced Design Verification Engineer with a strong background in semiconductor design and verification. Skilled in RTL design and verification, with expertise in UVM and SystemVerilog.

Name: Furkan Yigit Akyildiz

Email: fyabu@bu.edu

Phone: 617-971-7975

Location: Providence, RI

Country: USA

Position Category: Design: Physical Design

Desired Job Role: Design Verification Engineer

University: Boston University, Baskent University

Degree: Master of Science in Project Management

Graduation Year: 2025

Years Experience: 4

Notable Companies: Purdue System-on-Chip Extension Technologies, STG Engineering

Top Skills: SystemVerilog, UVM, SVA, TCL, Python

Job Intention: full-time

Visa Status: Not required

Able: no

Subject: RTL Design Verification Role

Date Sent: 04-10-2025

Resume: Yes