

Summary:

Venkata Ramana Molabanti is a graduate in Electrical and Computer Engineering from Portland State University, specializing in Design Verification and Emulation. He has hands-on experience with verification techniques and has completed an internship at AMD. He is seeking full-time opportunities in Digital Design Verification/Emulation starting February 2025.

Name: Venkata Ramana Molabanti

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Phone: 5032587893

Location: Austin 78735

Country: USA

Position Category: Verification: Design verification

Desired Job Role: Digital Design Verification/Emulation Engineer

University: Portland State University, Vignans University

Degree: M.S., Electrical and Computer Engineering, B.Tech., Electronics and Communication Engineering

Graduation Year: 2024

Years Experience: 1

Notable Companies: Advanced Micro Devices (AMD, USA, Austin)

Top Skills: System Verilog, Verilog, Python, Universal Verification Methodology (UVM), Assertion Based Verification (ABV), I2C

Job Intention: full-time

Visa Status: --

Able: no

Subject: Looking for unpaid Internships

Date Sent: 04-02-2025

Resume: Yes