

Summary:

Martin Roberto Linares Altamirano is a Sr. Verification/SoC Engineer at Synopsys and a Software Developer at Intel Guadalajara Design Center. He has a Masters Degree in Electronic Design and a Bachelors Degree in Electronics Engineering from ITESO. He also has an Associates Degree in Automation and Instrumentation Technologist from CETI. His skills include FPGA Design, testbench development with UVM, RTL design, and various programming languages.

Name: Martin Roberto Linares Altamirano

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Location: Guadalajara

Country: Mexico

Position Category: Design: Logic design

Desired Job Role: CPU Logic & Design Engineer

University: ITESO Instituto Tecnológico de Estudios Superiores de Occidente, CETI Centro de Enseñanza Técnica Industrial

Degree: Masters Degree: Electronic Design, Bachelors Degree: Electronics Engineering, Associates Degree: Automation and Instrumentation Technologist

Graduation Year: 2020

Years Experience: 4

Notable Companies: Synopsys, Intel Guadalajara Design Center

Top Skills: FPGA Design, testbench development with UVM, RTL design, C/C++, SystemC, Python, Java, Embedded Software, Linux

Job Intention: full-time

Visa Status: --

Able: yes

Subject: Intersted of joining AheadComputing!

Date Sent: 02-27-2025

Resume: Yes