

## Summary:

Shylaja Balagopal is a highly motivated ASIC Verification Engineer with over 28 years of experience in ASIC/SoC verification. She has a record of achievement in verifying design from concept to production on accelerated schedules. She has worked with notable companies like NGC, Microsoft, Intel, Synaptics, Xilinx, Shyla LLC, Cisco, and McData Corp. She is skilled in various areas including ASIC Design, Verification Flows, RTL Design, Simulation, Testbench Development, HVL, HDL, SystemVerilog, Verilog, HVL Methodology (OVM/VMM), Problem Solving, Constrained Random Testing, Debugging, Scripts, Coverage Models, Silicon validation, and Gate level simulation.

Name: Shylaja Balagopal

Email: --

Phone: 408-685-5453

Location: --

Country: --

Position Category: Verification: Design verification

Desired Job Role: Senior ASIC Verification Engineer

University: The University of Texas, University of Madras

Degree: Master of Science in Computer Engineering, Bachelor of Science in Electrical and Electronics

Graduation Year: --

Years Experience: 28

Notable Companies: NGC, Microsoft, Intel, Synaptics, Xilinx, Shyla LLC, Cisco, McData Corp

Top Skills: ASIC Design, Verification Flows, RTL Design, Simulation, Testbench Development, HVL, HDL, SystemVerilog, Verilog, HVL Methodology (OVM/VMM), Problem Solving, Constrained Random Testing, Debugging, Scripts, Coverage Models, Silicon validation, Gate level simulation

Job Intention: full-time

Visa Status: --

Able: no

Subject: Verification position - resume attached

Date Sent: 04-17-2025

Resume: Yes