

Summary:

Varshini Narayana is a Masters graduate in Computer Engineering from Syracuse University with experience in physical design, RTL development, and verification. She has hands-on experience in EDA tool flows, TCL, Python scripting, and low-power SoC designs. She has worked with Syracuse University and Arcom Digital.

Name: Varshini Narayana

Email: narayanavarshini.work@gmail.com

Phone: +18043962569

Location: Syracuse, NY

Country: USA

Position Category: Design: Physical Design

Desired Job Role: Physical Design Engineer

University: Syracuse University

Degree: Masters In Computer Engineering

Graduation Year: 2024

Years Experience: 2

Notable Companies: Syracuse University, Arcom Digital

Top Skills: Python, C, C++, Verilog, System Verilog, VHDL, SQL, MATLAB, SPI, I2C, UART, RTL Design, Layout Design, Logic Synthesis, PCB Design & Debugging, High-Speed Digital Design, JTAG Debugging, UVM, ASIC Development, Functional Verification, Design for Testability, Static Timing Analysis, Cadence Virtuoso, Synopsys Design Compiler, Synopsys PrimeTime, Xilinx Vivado, KiCad, Altium Designer, Linux, git

Job Intention: full-time

Visa Status: --

Able: no

Subject: Interest in Career Opportunities at Ahead Computing

Date Sent: 04-19-2025

Resume: Yes