Summary:

Joselito Isaac P. Morallo is an IC Design Engineer based in the Philippines. He is part of a team of 10 IC design engineers offering remote work. They have a range of skills including analog and digital design, simulation and validation of circuits, SoC design, physical layout design, and more. They also offer services in PCB design, test development, prototype of embedded circuits, FPGA, system development with software integration, and ARM and RISCV core integration.

Name: Joselito Isaac P. Morallo

Email: --

Phone: --

Location: Philippines

Country: Philippines

Position Category: Design: Logic design, Design: Physical Design

Desired Job Role: Remote IC Design Engineer

University: --

Degree: --

Graduation Year: --

Years Experience: --

Notable Companies: SoC advocates

Top Skills: Analog Design, Digital design, Simulation and Validation of circuits based on ASIC flow, SoC design of Blocks for large companies, Physical Layout Design, PCB design, TEST Development (DUT) after chip is fabricated, Prototype of Embedded circuts plus firmware (RTOS, Linux Kernel, etc), FPGA (digital IP design), System development with software integration on Web, App and App development, ARM and RISCV core integration to one chip harness

Job Intention: remote work

Visa Status: --

Able: no

Subject: An inquiry about careers at AC

Date Sent: 02-28-2025

Resume: No