Summary:

Mayank Rudakiya is a semiconductor professional with 3 years of RTL Design experience in India. He is skilled in Verilog, System Verilog, and various protocols. Seeking a full-time position as a CPU Design Engineer.

Name: Mayank Rudakiya

Email: rudakiyamayank355@gmail.com

Phone: +919824817093

Location: Bangalore

Country: India

Position Category: Design: Logic design

Desired Job Role: CPU Design Engineer

University: The Maharaja Sayajirao University of Baroda

Degree: Bachelor of Engineering in Electronics

Graduation Year: 2021

Years Experience: 3

Notable Companies: Cisma Consultants, Cadence, SiFive

Top Skills: Verilog, System Verilog, RTL Design, Protocol Knowledge, Python, C++

Job Intention: full-time

Visa Status: --

Able: no

Subject: Application for CPU Design Engineer

Date Sent: 03-05-2025

Resume: Yes