

## Summary:

Karthikeya Sharma is a graduate student at Georgia Institute of Technology, pursuing a Master of Science in Electrical and Computer Engineering. He has experience in RTL design, verification, hardware acceleration, performance modeling, and related areas in Computer Architecture. He has worked at Synergy Lab, SHARC Lab, and iSenSys Lab at Georgia Tech.

Name: Karthikeya Sharma

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Phone: 404-203-4923

Location: Atlanta, GA, 30309

Country: USA

Position Category: Design: Logic design, Verification: Design verification, Architecture

Desired Job Role: RTL Design Engineer

University: Georgia Institute of Technology, SRM Institute of Science and Technology

Degree: Master of Science in Electrical and Computer Engineering, Bachelor of Technology in Electronics and Communication Engineering

Graduation Year: 2025

Years Experience: 2

Notable Companies: Synergy Lab @Georgia Tech, SHARC Lab @Georgia Tech, iSenSys Lab@Georgia Tech

Top Skills: C/C++, CUDA C, Scripting Language (Python, Bash, TCL), RTL (Verilog, SystemVerilog), High-Level Synthesis, OpenGL, OpenMP, OpenMPI, SFML, PyTorch, Keras, CuPy, Linux (Ubuntu, Redhat), Windows, ScaleSim, TACOS, Chakra, ASTRA-Sim, ScaleHLS, Xilinx Vivado, Vitis HLS, Modelsim, Quartus Prime, Icarus Verilog, Libero 12.5, TAPA, ALLO, Google XLS, Softconsole, Code Composer Studio, I2C, SPI, CAN, UART, AXI3, APB, CHI (basics), Ethernet, PCIe (basics)

Job Intention: full-time

Visa Status: --

Able: no

Subject: [Enquiry]: Full time roles at Ahead Computing

Date Sent: 03-18-2025

Resume: Yes