

## Summary:

Binsy Rajendran is a Masters student in Electrical and Computer Engineering at Portland State University, specializing in Digital IC Design, Physical Design, and SoC development. She has hands-on experience with various design and simulation tools and has previously interned at the Indian Space Research Organisation. She is seeking an internship or entry-level position in RTL/IP/SoC Design or verification Engineering.

Name: Binsy Rajendran

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Phone: +1 (971)-407-7088

Location: Hillsboro, Oregon

Country: USA

Position Category: Design: Logic design, Design: Physical Design

Desired Job Role: RTL/IP/SoC Design or Verification Engineer

University: Portland State University, Kannur University

Degree: Masters in Electrical and Computer Engineering, Bachelors of Technology in Electronics and Communication Engineering

Graduation Year: 2025

Years Experience: 3

Notable Companies: Indian Space Research Organisation, Universal Group of Institutions

Top Skills: Python, C, SQL, TCL, SystemVerilog, MATLAB, RISC V, Git, Cadence Virtuoso, LTSpice, Synopsys Design Compiler, ICC2, PrimeTime, Cadence Genus, Innovus, Conformal, Xilinx Vivado, Catapult Studio, Questa Sim

Job Intention: Internship or PT/FT/Contract

Visa Status: --

Able: yes

Subject: Application for Internship / Entry-Level Role RISC-V Design Enthusiast

Date Sent: 04-20-2025

Resume: Yes