

Summary:

Devesh is a Master's student at NC State University specializing in Computer Architecture and VLSI. He has experience in FPGA and RTL Design from his time at the Indian Space Research Organization and has also interned at Tangible Synthesis Pvt Ltd. His top skills include programming in several languages and using a variety of tools and software. He is seeking an intern role for Summer 2025 or Fall 2025 Co op.

Name: Devesh Hiteshbhai Jani

Email: dhjani2@ncsu.edu

Phone: +1 (919)-520-5927

Location: Raleigh, NC

Country: USA

Position Category: Internship

Desired Job Role: ASIC Design and Design Verification Engineer

University: North Carolina State University, Charotar University of Science and Technology

Degree: M.S. in Computer Engineering, B.Tech in Electronics & Communication Engineering

Graduation Year: 2026

Years Experience: 1

Notable Companies: Indian Space Research Organization, Tangible Synthesis Pvt Ltd

Top Skills: Programming, System Verilog, Verilog, C++, C, Python, TCL, Tools and Software, CUDA, OpenCL, Vivado, Libero SoC/IDE, LabView, MATLAB, Synopsys VCS, Synopsys Verdi, LTSpice

Job Intention: Intern role for Summer 2025 or Fall 2025 Co op

Visa Status: --

Able: no

Subject: Regarding Summer 2025 Internship/ Fall Co-op at AheadComputing

Date Sent: 04-12-2025

Resume: Yes