Summary:

Vanya is an experienced electronic engineer with 5 years of hands-on experience in hardware and

software testing at Intel. She is proficient in System Verilog, Perl, Assembler, and Linux and has a

strong background in debugging, troubleshooting, and validating complex systems such as CPUs

and memory components.

Name: Vanya Michelle Medina Garcia

Email: vanyamichelle@live.com

Phone: +52 3313058479

Location: Mexico City

Country: Mexico

Position Category: Verification: Design verification

Desired Job Role: Senior CPU Verification Engineer

University: Instituto Tecnolgico de Estudios Superiores de Monterrey, Guadalajara Jalisco

Degree: B.S. Engineers Degree in Electronic engineering

Graduation Year: --

Years Experience: 5

Notable Companies: Intel

Top Skills: System Verilog, Perl, Assembler, Python, UVM (methodology), Verdi(waveforms) and log

analysis

Job Intention: full-time

Visa Status: --

Able: yes

Subject: Working at AheadComputing

Date Sent: 02-25-2025

Resume: Yes