

VANYA MICHELLE MEDINA GARCIA

Electronic Engineer

P +52 3313058479

E vanyamichelle@live.com

A Mexico City

PROFILE

Curious, enthusiastic, experienced electronic engineer with 5 years of hands-on experience in hardware and software testing at Intel. Adept at debugging, troubleshooting, and validating complex systems such as CPUs and memory components. Proficient in System Verilog, Perl, Assembler, and Linux. Passionate about delivering excellent customer experiences through effective, problem-solving technical expertise, optimizing systems and collaborating with international teams.

EXPERIENCE

Mar 2022 – Now

INTEL – CPU Design Verification Engineer [Full-time]

- Developed and implemented test content using **x86 assembly** and **System Verilog**, ensuring proper system functionality.
- Employed debugging techniques to verify the correct behavior of tests, **analyzing logs** and **waveforms** to identify and address issues to **ensure system reliability**.
- **Co-owner** of a specific **area** within the project(CPU interrupt system), ensuring **quality assurance** were met, adherence to **documentation standards**, and collaborated across teams to improve verification processes and test execution.
- Contributed to the development of **written checkers** to ensure compliance with specified scenarios.
- Engaged in **optimizing system performance** through rigorous testing, **analysis** to ensure **peak functionality** and **coverage**.
- Provided documentation (**Test plan**) and technical feedback to internal teams, enabling clear communication of system status and improvement.

Feb 2020 – Mar 2022

INTEL – Intern Full Chip IP pre-silicon validator [Part-time]

- Developed and executed tests using **System Verilog** and employ **Universal Verification Methodology (UVM)** to ensure effective verification of hardware components.
- Collaborated with internal teams to identify and resolve system errors, **tracking errors** through **logs** and **waveform analysis**.
- Gained practical experience in **problem-solving** and **troubleshooting**, ensuring hardware components functioned correctly before silicon implementation.

SKILLS & EXPERTISE

Technical Expertise:

- In-depth understanding of **hardware validation**, testing, and **debugging techniques**.
- Proficient in **Linux**-based operating systems, debugging.
- Familiarity with system optimization, including **stress testing** and **performance analysis**.
- Knowledge of **networking concepts** and currently expanding skill in this area

Programming & Tools:

- **System Verilog**, **Perl**, **Assembler**, **Python**
- **UVM (methodology)**
- **Verdi(waveforms)** and **log analysis**

Soft Skills:

- Strong problem-solving abilities.
- Effective communication skills, able to **articulate technical** concepts clearly to **both technical** and **non-technical stakeholders**.
- Passionate about continuous learning and staying up to date with latest **technological advances/trends**.
- **Team-oriented**, with experience collaborating across diverse, **international teams** to achieve common goals.

EDUCATION

Instituto Tecnológico de Estudios Superiores de Monterrey, Guadalajara Jalisco

B.S. Engineer's Degree in Electronic engineering

Formed part of the representative dance team

ADDITIONAL INFORMATION

- Available for flexible hours and on-call support as required.
- References upon request