Summary:

Phani Teja is a graduate student at the University of Southern California, specializing in Computer Architecture. He has worked on projects related to Tomasulo Out-of-Order execution and has professional work experience on power optimization. He has also served as a Teaching Assistant for the Computer Organization course.

Name: Phani Teja

Email: pvempati@usc.edu

Phone: +1 (213) 301-7078

Location: California

Country: USA

Position Category: Architecture

Desired Job Role: Computer Architecture Engineer

University: University of Southern California - Viterbi School of Engineering, VIT University

Degree: Masters of Science in Computer Engineering, Bachelor of Technology - Electronics and Communication Engineering

Graduation Year: 2025

Years Experience: 7

Notable Companies: Intel, Nvidia

Top Skills: CDC, Low Power Design, STA, DFT, DFX RTL, GPGPU, Cache Coherency, UART, AXI, PCIe, I2C, Virtual Memory, SRAM, Logic Design, CAD, Physical Design, PnR, RF Conformance testing, Verilog, VHDL, Python, C, Matlab, Git, Linux, Unix, Perforce

Job Intention: full-time

Visa Status: --

Able: no

Subject: Seeking opportunities at aheadcomputing

Date Sent: 04-03-2025

Resume: Yes