Summary:

Pre-Si Verification Engineer with 7+ years of experience on ASIC verification and integration. Has worked with notable companies like Intel Corporation and Synopsys. Skilled in various tools and languages such as SystemVerilog, UVM/OVM, Verilog, VCS, Verdi, XCELIUM, Git, Quartus II, AMBA (APB, AXI4), PCIE 2.0, Python, Perl, C/C++, Shell scripting. Experienced in methodologies like Coverage-Driven Verification, Agile/Scrum, Test Plan Development.

Name: Jesus Alfonso Lopez Chin

Email: alfonsochin1@gmail.com

Phone: 3314167588

Location: Guadalajara, Mexico

Country: Mexico

Position Category: Verification: Design verification

Desired Job Role: Design Verification Engineer

University: Instituto Tecnolgico de Estudios Superiores de Monterrey

Degree: Bachelor of Electronic Engineering

Graduation Year: --

Years Experience: 7

Notable Companies: Intel Corporation, Synopsys

Top Skills: SystemVerilog, UVM/OVM, Verilog, VCS, Verdi, XCELIUM, Git, Quartus II, AMBA (APB, AXI4), PCIE 2.0, Python, Perl, C/C++, Shell scripting, Coverage-Driven Verification, Agile/Scrum, Test Plan Development

Job Intention: full-time

Visa Status: --

Able: yes

Subject: working at AheadComputing

Date Sent: 04-20-2025

Resume: Yes