

## Summary:

Jessica Vaz is a Master's student at the University of Illinois, Urbana-Champaign with a background in ASIC design engineering at SiFive. She has experience in SoC design, hardware-based trusted execution environments, and FPGA tools. She is currently seeking an internship in computer architecture and RTL design.

Name: Jessica Danica Vaz

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Location: Champaign, IL, USA

Country: USA

Position Category: Design: Logic design

Desired Job Role: ASIC Design Engineer

University: University of Illinois Urbana-Champaign, Nitte Meenakshi Institute of Technology (NMIT)

Degree: Master of Science in Electrical and Computer Engineering, Bachelor of Engineering in Electronics and Communication Engineering

Graduation Year: 2026, 2022

Years Experience: 2

Notable Companies: SiFive India Pvt. Ltd.

Top Skills: C/C++, Python, x86, ARM and RISC-V Assembly, MATLAB, Verilog, System Verilog, Chisel, Xilinx Vivado, Synopsys CoreTools, VCS, Verdi, Arteris Magillem, Siemens ModelSim, Ngspice, ARM Keil uVision, COMSOL Multiphysics, Bash, Git

Job Intention: Internship

Visa Status: --

Able: No

Subject: Seeking Summer Internship opportunities @ AheadComputing

Date Sent: 03-10-2025

Resume: Yes