

## Summary:

Chandan Gireesha is a highly skilled engineer with a strong background in physical design, EDA tools, and scripting. He has experience working with EDA tools such as Synopsys, Cadence, and Mentor Graphics, and has developed expertise in scripting languages like TCL and Python. He has hands-on experience in the areas of static timing analysis, placement, routing, and PPA optimization.

Name: Chandan Gireesha

Email: chandangireesha@gmail.com

Phone: 4157401897

Location: San Francisco, California, 94014

Country: USA

Position Category: Design: Physical Design

Desired Job Role: Physical Design Engineer

University: San Francisco State University, Visvesvaraya Technological University

Degree: Master of Science, Electrical & Computer Engineering, Bachelor of Engineering, Electronics & Communication Engineering

Graduation Year: 2025

Years Experience: 2

Notable Companies: OpenQQuantify, RV Skills Centre for Emerging Technologies, San Francisco State University

Top Skills: EDA Tools, Scripting, Physical Design, Static Timing Analysis, Placement & Routing, Power Optimization

Job Intention: full-time

Visa Status: --

Able: no

Subject: Application for Physical Design Engineer/Silicon CAD Engineer Position at Ahead Computing

Date Sent: 04-19-2025

Resume: Yes