

Summary:

Umer Cheema is a highly experienced Deep Learning Hardware Engineer with expertise in Accelerator Design, Micro-architecture, and FPGA Design. He has over a decade of experience in hardware engineering, particularly in Custom Accelerator Design and NPU Architecture.

Name: Umer Cheema

Email: cheema.umer@gmail.com

Phone: 408-858-5362

Location: Portland, Oregon, USA, 97229

Country: USA

Position Category: Architecture

Desired Job Role: Senior CPU Microarchitecture & Logic Design - Execution Unit

University: University of Illinois at Chicago, University of Engineering & Technology

Degree: Ph.D. Electrical & Computer Engineering

Graduation Year: 2016

Years Experience: 13

Notable Companies: Intel Corporation, Whizz Systems Inc

Top Skills: Accelerator Design, Micro-architecture, Digital Logic Design, NPU Architecture, FPGA Design

Job Intention: full-time

Visa Status: --

Able: yes

Subject: CPU Microarchitecture & Logic Design Engineer, Execution Unit

Date Sent: 02-25-2025

Resume: Yes