Summary:

Pooja C R is currently pursuing her Master of Technology in VLSI Design and Embedded Systems and is keen on opportunities to grow professionally and academically. She has demonstrated skills in ASIC Design Flow, Logic Synthesis, CMOS Analog Design, and more. She has completed several projects, including the design of a two-stage comparator, a telescopic amplifier, and an array multiplier.

Name: Pooja C R

Email: poojacr2001@gmail.com

Phone: 6366320474

Location: Chunganahalli, Chikkanayakanahalli, Tumakuru

Country: India

Position Category: Design: Logic design

Desired Job Role: VLSI Design Engineer

University: Siddaganga Institute Of Technology, Tumakuru, PES University, Bangalore

Degree: Master of Technology (M.Tech) in VLSI Design and Embedded Systems, Bachelor of Technology (B. Tech) in Electronics and Communication Engineering

Graduation Year: 2023

Years Experience: 0

Notable Companies:

Top Skills: ASIC Design Flow, Logic Synthesis, CMOS Analog Design, Functional Verification, Static Time Analysis, RTL Coding, Digital Design, RISC V Processor Design, Phase Locked Loops (PLL) Design

Job Intention: full-time

Visa Status: --

Able: no

Subject: Opportunities In your company

Date Sent: 04-08-2025

Resume: Yes