

Summary:

Sayantana Ghosh is an experienced Physical Design Engineer with 7 years of experience in Physical Design and 3 years in Layout Design. He has a proven track record in successful Tape-Outs of multi-million gate blocks across diverse process technologies, methodologies, and collaborative teams. He is currently pursuing his Master of Science in Electrical and Computer Engineering at the University of Minnesota, Twin Cities.

Name: Sayantan Ghosh

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Phone: +1 515-817-4343

Location: Minnesota

Country: USA

Position Category: Design: Physical Design

Desired Job Role: Senior Physical Design Engineer

University: University of Minnesota, Twin Cities, West Bengal University of Technology (WBUT)

Degree: Master of Science in Electrical and Computer Engineering, Bachelor of Technology in Electronics and Communication Engineering

Graduation Year: 2025, 2012

Years Experience: 7

Notable Companies: Synapse Design Automation, Inc., Pozibility Technologies, Tata Elxsi, Synopsys, Inc

Top Skills: Physical Design, Layout Design, EDA Tools, Scripting Languages, Technology Nodes/PDKs

Job Intention: full-time

Visa Status: --

Able: no

Subject: Application for the Staff Physical Design Engineer CPU PPA Optimization

Date Sent: 03-16-2025

Resume: Yes