

Dear AheadComputing:

Attached is my resume for consideration for the position of Senior Staff CPU Microarchitecture & Logic Design Engineer, Out-of-Order Execution (OoOE). Aside from having actual experience taking a pipelined ARM CPU architecture to OoOE CPU architecture, while working at Qualcomm on the Snapdragon processor, I have personally studied and created an OpCode map for the RISC-V base ISA (see the attached LibreOffice spreadsheet).

Aside from having the technical experiences, I also bring the start-up experience and culture to the team with a strong "team player" mindset, such that I really enjoy mentoring and guiding the next generation of engineers while also being a strong technical contributor myself!

I look forward to joining your team and helping them succeed as quickly as possible!

Sincerely,

Manuel "Manny" Muro, Jr.

Portland, OR USA