

Hi Team,

I hope you are doing well.

I pursued my masters degree in electrical and computer engineering at Portland State University, specializing in Design Verification and Emulation. Throughout my program, Ive gained hands-on experience with verification techniques, including SystemVerilog, Pre-Silicon Validation Fundamentals, Assertion-based Verification, and Emulation.

Additionally, I completed my internship at AMD, where I contributed to Pre-Silicon Verification using emulation for AI acceleration hardware on Cadence Palladium and Siemens Veloce.

I am writing to express my interest in full-time opportunities starting in March 2025 and would like to know if you have any suitable openings that align with my background and aspirations.

Please find my resume attached for your reference.

Thank you for your time, and I look forward to hearing from you.

Best regards,

Venkata Ramana Molabanti