Summary:

Pre-Si verification engineer with 7+ years of experience on diverse ASIC verification areas.

Name: Carlos Rodrigo Fernandez Garcia

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Phone: +52 3313376016

Location: Guadalajara, Mexico

Country: Mexico

Position Category: Verification: Design verification

Desired Job Role: Senior CPU Verification Engineer

University: Instituto Tecnolgico y de Estudios Superiores de Occidente (ITESO)

Degree: Masters Degree in Electronic Design, Bachelors Degree in Electronic Engineering

Graduation Year: 2020

Years Experience: 7

Notable Companies: NXP Semiconductors, Intel Corporation

Top Skills: ASIC verification, Testbench development, Feature validation, Power management

Job Intention: full-time

Visa Status: --

Able: yes

Subject: Working at AheadComputing

Date Sent: 02-19-2025

Resume: Yes