Hello,

My name is Supreet Gulavani, and I am an MS ECE (Computer Architecture - June 2023) graduate from Portland State University. I am interested in joining AheadComputing as aCPU Performance Architect.

During my Master's, I completed courses like Microprocessor System Design, Computer Architecture I & II, Fundamentals of Pre-Silicon Validation, SoC Design using FPGAs, etc. I also did academic projects like DDR4 Memory Controller Simulator (C++), RISCV ISA Simulator (C++), an implementation of the method proposed in the paper "MLP Aware Cache Replacement Algorithm", and other projects that required a strong understanding of the microarchitectures, RTL, and verification. I have also completed courses like Embedded Systems Design using FPGAs.

I was recently a Firmware Engineer at HP, where I designed a PCIe Gen3x4-based debug tool that would performDMA operations with on-die junction temperature monitoring. I have also been a Firmware Developer Intern at Intel, working on their open-source bootloader 'Slimbootloader' and a DFT Intern at NXP Semiconductors.

I believe I have the necessary skill setfor this opportunity and a hunger to learn more. I would be relocating to Portland, OR, in May, and believe this is the perfect opportunity for me to take my career to the next level, as being an Architect is my career goal.

I would love for you to take a look at my attached resume and discuss how I can contribute.

Looking forward to hearing from you soon.

Thank you so much!

Sincerely,

Supreet Gulavani

Ph No. (503) 961-0798

https://www.linkedin.com/in/supreetgulavani/

https://github.com/supreetgulavani

Hello,

I hope you are doing well.

I was wondering if you had a chance to take a look at my application. I am really interested in this opportunity and would love to speak with you.

Thank you so much!

Sincerely,

Supreet Gulavani