

Hello,

I am a digital design engineer with about 2.5 years of experience in front-end and back-end design. I came across the job roles for CPU logic design roles at Ahead Computing, and I feel that I can be a great asset to your team.

A little about me-

I have about 2.5+ years of experience in digital design including internship. My experience spans from front-end design using SystemVerilog and static checks like lint, DFT, CDC/RDC to back-end design including synthesis, place & route, timing analysis. Additionally, I have completed coursework for Computer Architecture at school during my Master's and would love to work on those concepts. I believe my well-rounded experience will be a great fit for this position.

I'll also attach my resume for your reference. Looking forward to hearing from you.

Thanks,

Vidya

Hello,

I would like to add that I am interested in RTL design for Instruction Fetch, out-of-order execution and memory blocks.

Thanks,

Vidya