

## Summary:

Cesar is an electrical engineer with strong expertise in software development, data analysis, digital electronics design and validation, and automatic control. He has worked with notable companies like Intel and Dadial Medica. He is proficient in a variety of programming languages and tools, and has a strong academic background with degrees from multiple universities.

Name: Cesar Toms Gmez Cruz

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Location: Guadalajara, Jalisco, Mexico

Country: Mexico

Position Category: Verification: Design verification

Desired Job Role: Senior CPU Design and Validation Engineer

University: UNAM, CIMAT, ITESO, CINVESTAV

Degree: Computer Engineering, Specialization on Statistical Methods Applied to Data Science, Specialization on Embedded Systems, MSc Electrical Engineering focused on Automatic Control, Master in Electronics Design, PhD. in Sciences in Engineering with focus on IA

Graduation Year: 2028

Years Experience: 20

Notable Companies: Intel, Dadial Medica

Top Skills: Verilog / SystemVerilog / VHDL, VCS / DVE / Verdi / Questa / ModelSim, UVM / SystemVerilog Assertions, C / ARM Assembly / PIC Assembly / TMS320xx Assembly, C#/C++/C / Java / Python / Perl / Kotlin / Swift / HTML+CSS, SQL Studio / SQL Lite / MySQL / Postgress, Python / R Language / C#/C++ / Matlab / SQL, Tensorflow / PyTorch / Keras, SPSS / JMP / Orange / Splunk

Job Intention: full-time

Visa Status: --

Able: no

Subject: Working at AheadComputing

Date Sent: 02-18-2025

Resume: Yes