

Summary:

Kumar Subramanian is an accomplished Principal Engineer with 30 years of expertise in CPU design methodology, and backend implementation. He has a proven track record of leading the development of high-performance microprocessor components, including Intel Atom/E-core design, spanning multiple generations and process nodes. He has extensive experience in full-stack RTL-to-GDS implementation, PPA optimization, DTCO and collaboration across cross-functional teams.

Name: Kumar Subramanian

Email: suttamally@gmail.com

Phone: 971-645-1185

Location: 10000 Dianella Lane, Austin Tx, 78759

Country: USA

Position Category: Design: Physical Design

Desired Job Role: Principal Physical Design Engineer

University: University of Windsor, Mangalore University

Degree: Master of Applied Science (Electrical Engineering), Bachelor of Engineering

Graduation Year: 1994, 1990

Years Experience: 30

Notable Companies: Intel Corporation, TriTech Microelectronics, TriTech Inc

Top Skills: CPU design methodology, backend implementation, RTL-to-GDS implementation, PPA optimization, DTCO, collaboration across cross-functional teams

Job Intention: full-time

Visa Status: --

Able: no

Subject: Physical Design Engineer opening at Ahead Computing

Date Sent: 03-17-2025

Resume: Yes