

Summary:

Vidura Manu Wijayasekara is a highly experienced Formal Verification Engineer with a Ph.D. in Electrical and Computer Engineering from North Dakota State University. He has 9 years of industry experience, including leading Formal Verification projects for complex coherency agents in server SoCs at Intel Corp. and working as a Digital Design Engineer at a startup, Eta Compute Inc.

Name: Vidura Manu Wijayasekara

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Phone: (701) 566-4089

Location: 4050 Zircon Ln. N., Plymouth, MN 55446

Country: USA

Position Category: Verification: Formal verification

Desired Job Role: Senior Formal Verification Engineer

University: North Dakota State University

Degree: Ph.D. in Electrical and Computer Engineering, BS in Electrical Engineering

Graduation Year: 2016

Years Experience: 9

Notable Companies: Intel Corp., Eta Compute Inc.

Top Skills: Formal Verification, Digital Design, Firmware Development

Job Intention: full-time

Visa Status: --

Able: no

Subject: Application for Senior Formal Verification Engineer Position

Date Sent: 04-21-2025

Resume: Yes