

Summary:

Jujhar Singh is a highly experienced Digital Design Engineer with over 15 years of experience in full chip design flow from RTL, Synthesis, Physical Design, STA and Silicon Debug. He has a Master's degree in Electrical Engineering (VLSI) from the University of Southern California and has worked with notable companies like Microchip Technology and Quantum Dimension.

Name: Jujhar Singh

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Phone: 480-310-8730

Location: Phoenix, AZ 85018

Country: USA

Position Category: Design: Logic design

Desired Job Role: CPU RTL Design Engineer

University: University of Southern California, Punjab University, Semiconductor Laboratory, Dept. Of Space, Chd, India

Degree: MS-Electrical Engineering(VLSI), Advance Diploma in VLSI Design, BS- Electronics & Communication Engineering

Graduation Year: 2009

Years Experience: 15

Notable Companies: Microchip Technology, Quantum Dimension, Motion Technologies, Topanga Technologies, Reliance Infocomm, Semiconductor Laboratory, India

Top Skills: Processor: ARM Cortex M0+, PIC16/18, AVR & 8085, Programming Languages: Verilog, C, C++, Perl, TCL, Assembly Language, Tools: Xilinx ISE, Chip Scope, Questasim, ePD, HSPICE, Virtuoso, Primetime, Design Compiler, ICC, Redhawk, CCK, GIT, Bus Protocols: APB, SPI, UART, I2C

Job Intention: full-time

Visa Status: --

Able: no

Subject: CPU RTL Design

Date Sent: 04-18-2025

Resume: Yes