

Summary:

Highly motivated and experienced ASIC hardware logic design professional seeking a challenging role in CPU/GPU micro-architecture, multi-core processor, router, and fabric architecture research and development. Passionate about exploring innovative architectural and micro-architectural solutions, with a focus on power-optimized performance enhancement techniques.

Name: Johnsy Kanjirapallil John

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Phone: 201-532-0032

Location: San Jose, CA

Country: USA

Position Category: Design: Logic design

Desired Job Role: ASIC Hardware Logic Design Engineer

University: New Jersey Institute of Technology, Rajiv Gandhi Institute of Technology, Mahatma Gandhi University

Degree: PhD Candidate/Course Work Completed in Computer Engineering, Computer Engineering, Masters, Electronics and Communication Engineering, Bachelors

Graduation Year: 2006, 2003, 1999

Years Experience: 16

Notable Companies: Microsoft Corporation, Intel Corporation, Northrop Grumman, Intel Federal LLC, AMD Boston Design Center, Qualcomm CDMA Technologies, Agere Systems

Top Skills: ASIC hardware logic design, CPU/GPU micro-architecture, multi-core processor, router, fabric architecture research and development

Job Intention: full-time

Visa Status: US Citizen

Able: no

Subject: Working at AheadComputing

Date Sent: 02-28-2025

Resume: Yes