Summary:

Varun Subramaniam is a graduate student in Computer Engineering at NC State with a strong interest in the CPU Performance Architect position at Ahead Computing. He has a background in computer architecture and performance modeling, with hands-on experience building cycle-accurate C++ simulators for RISC-V pipelines and analyzing IPC scaling across superscalar configurations. He has worked at Cognizant and SASTRA University's Intrusion Detection Lab.

Name: Varun Subramaniam

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Phone: +1-9196370210

Location: Raleigh, North Carolina

Country: USA

Position Category: Architecture

Desired Job Role: CPU Performance Architect

University: North Carolina State University, SASTRA University

Degree: Master of Science, Computer Engineering, Bachelor of Technology, Electronics and Communication Engineering

Graduation Year: 2025

Years Experience: 2

Notable Companies: Cognizant, Intrusion Detection Lab, SASTRA University

Top Skills: C/C++, Python, Verilog, SystemVerilog, Tcl, UVM (Universal Verification Methodology), Assertion-based Verification (SVA), Constraint-Driven Verification, Functional & Code Coverage, FSMs, Datapath/Control Path Logic, Static Timing Analysis, Power-Aware RTL, Multi-Clock Domain, I2C, AMBA (AXI, AHB, APB), PCIe, Synopsys VCS, Intel Quartus Prime, QuestaSim, Git, MATLAB

Job Intention: full-time

Visa Status: --

Able: no

Subject: Application for CPU Performance Architect Role Varun Subramaniam

Date Sent: 04-14-2025

Resume: Yes