Summary:

Mark Zakharov is a highly skilled individual with a strong background in RISC-V ASIC development and research, specializing in CPU microarchitecture and logic design. He has experience working at Esperanto Technologies and holds a Master's degree in Computer Science & Engineering.

Name: Mark Zakharov

Email: markzakharov1102@gmail.com

Phone: (925)-818-3586

Location: Santa Cruz, CA

Country: USA

Position Category: Architecture

Desired Job Role: Senior CPU Microarchitecture & Logic Design Engineer

University: University of California, Santa Cruz

Degree: Master of Science, Computer Science & Engineering

Graduation Year: 2024

Years Experience: 4

Notable Companies: Esperanto Technologies

Top Skills: Verilog, Python, Bash, Chisel, C, C++, RISC-V ASM, DSLX

Job Intention: full-time

Visa Status: --

Able: --

Subject: Interest in Logic Design - Execution Unit Position

Date Sent: 02-26-2025

Resume: Yes