Summary:

Nilesh Suryavanshi is a Master's student in Computer Engineering at The University of Texas at Dallas, seeking an Internship/Full-Time opportunity in silicon verification, validation, and Design for Testability(DFT). He has experience in Formal Verification, Computer Architecture, and Design Verification. He has worked at notable companies like Bosch and Tata Consultancy Services Limited.

Name: Nilesh Suryavanshi

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Phone: +1 9455274828

Location: Austin, Texas, 78736

Country: USA

Position Category: Verification: Formal verification

Desired Job Role: Formal Verification Engineer

University: The University of Texas at Dallas, University Of Mumbai

Degree: Master of Science, Computer Engineering, Bachelor of Engineering, Instrumentation

Graduation Year: 2025, 2017

Years Experience: 4

Notable Companies: Bosch, Tata Consultancy Services Limited

Top Skills: HDL: Verilog, SystemVerilog, Programming/Framework: Python, C, C++, Git, Jira, Linux, Tools: Synopsys, Cadence, QuestaSim, Xilinx, Intel, MATLAB

Job Intention: Internship/Full-Time

Visa Status: --

Able: no

Subject: Working at AheadComputing

Date Sent: 02-25-2025

Resume: Yes