

Summary:

Experienced ASIC hardware logic design professional with a focus on CPU/GPU micro-architecture and RTL development. Holds an active US federal security clearance at the secret level.

Name: Johnsy K. John

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Phone: (201) 532 0032

Location: San Jose, CA

Country: USA

Position Category: Design: Logic design

Desired Job Role: CPU/GPU Micro-architecture Design Engineer

University: New Jersey Institute of Technology, Rajiv Gandhi Institute of Technology

Degree: PhD Candidate/Course Work Completed in Computer Engineering, Masters in Computer Engineering, Bachelors in Electronics and Communication Engineering

Graduation Year: Estimate

Years Experience: 16

Notable Companies: Microsoft Corporation, Intel Corporation, Northrop Grumman, Intel Federal LLC, AMD Boston Design Center, Qualcomm CDMA Technologies

Top Skills: ASIC, Processor/CPU/GPU/Router/Fabric Micro-architecture, RTL Development, Verilog, System Verilog, UVM, C/C++, Python, CPU/GPU Microarchitecture, Instruction Fetch, Branch Prediction, SIMD, Cache Design

Job Intention: Full-time

Visa Status: US Citizen

Able: No

Subject: Working at AheadComputing

Date Sent: 02-28-2025

Resume: Yes