Hello Team at AheadComputing,

Im Shravan Ramamoorthy, a recent Masters graduate in Electrical & Computer Engineering from UIUC with a strong focus on digital design and verification.

During mymaster's, I have built and verified complex systems like cache controllers, pipelined RISC-V CPUs, and branch predictors using SystemVerilog and UVM. My background also includes two years of professional software development experience, which taught me how to build robust, testable systems end-to-end.

Ive attached my resume for your consideration and would love to connect to explore any new grad / entry-level design verification roles where my profile could be a fit.

Thanks for your time,

Shravan Ramamoorthy

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