

## Summary:

Bhargava Saketh Nagapattinam is a highly skilled Computer Engineer with a Master's degree from The University of Texas at Dallas and a Bachelor's degree from Osmania University. He has experience working with notable companies like Defense Research and Development Laboratory and Silicon Engineers. His top skills include Verilog, SystemVerilog, C, C++, Python Scripting, and more. He is seeking a full-time position as a CPU Performance Architect.

Name: Bhargava Saketh Nagapattinam

Email: bhargava.saketh@gmail.com

Phone: 945-257-1050

Location: Novi, MI 48374

Country: USA

Position Category: Architecture

Desired Job Role: CPU Performance Architect

University: The University of Texas at Dallas, Osmania University

Degree: Master of Science in Computer Engineering, Bachelor of Engineering in Electronics and Communication & Engineering

Graduation Year: 2024, 2022

Years Experience: 2

Notable Companies: Defense Research and Development Laboratory, Silicon Engineers, Bright Mind Enrichment

Top Skills: Verilog, SystemVerilog, C, C++, Python Scripting, TCL, Perl, MySQL, UVM, Object Oriented Programming (OOPs), SPI, I2C, UART, AMBA (AXI, AHB, APB), USB, PCIe

Job Intention: full-time

Visa Status: --

Able: no

Subject: Intersted of joining AheadComputing!

Date Sent: 04-22-2025

Resume: Yes