

Summary:

Adwait Joshi is a Masters student in Computer Engineering at North Carolina State University, seeking internship opportunities for Summer 2025 in Modeling, RTL Design, and Verification for CPU, GPU, SoC, and IP domains. He has experience in various projects related to Computer Architecture and Verilog Design and has worked as an Assistant Manager at Jio Platforms Limited.

Name: Adwait Joshi

Email: asjoshi7@ncsu.edu

Phone: +1 (919) 520 8989

Location: Raleigh, NC, 27606

Country: USA

Position Category: Internship

Desired Job Role: VLSI Design and Verification Intern

University: North Carolina State University, Xavier Institute of Engineering

Degree: Master of Science in Computer Engineering, Bachelor of Engineering in Electronics and Telecommunication

Graduation Year: 2026

Years Experience: 1

Notable Companies: Jio Platforms Limited

Top Skills: C++, Verilog, SystemVerilog, Static Timing Analysis, Clock Domain Crossing, Cache, Pipeline Hazards, Out of Order Execution, Coherence vs Consistency

Job Intention: internship

Visa Status: --

Able: no

Subject: Summer and Fall 2025 Internship Opportunities at Ahead Computing

Date Sent: 03-26-2025

Resume: Yes