

Summary:

Doreen Sisanalli is a Master's student at Columbia University with a background in Electrical Engineering. She has two years of experience as a Software Engineer at BOSCH and has worked on projects involving VLSI Physical Design and FPGA implementation. She is skilled in various technical areas including C/C++, Cadence Virtuoso, FPGA, VLSI Design, and Matlab among others.

Name: Doreen Sisanalli

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Phone: +1 (646) 314-2563

Location: New York City

Country: USA

Position Category: Design: Physical Design

Desired Job Role: VLSI Physical Design Engineer

University: Columbia University, Visvesvaraya Technological University

Degree: Master of Science, Electrical Engineering, Bachelor of Engineering, Electronics and Communication Engineering

Graduation Year: 2025

Years Experience: 2

Notable Companies: BOSCH, News Corp

Top Skills: C/C++, Cadence Virtuoso, DRC/LVS, FPGA, VLSI Design, Matlab, Verilog, SystemVerilog, SoC Platforms, SystemC, RISC-V, AUTOSAR, ComStack, INCA, VectorCAN, CanDiag, Debugger (UDE), V-V model

Job Intention: full-time

Visa Status: --

Able: no

Subject: Interested in the role "Physical Design Engineer CPU PPA Optimization"

Date Sent: 04-01-2025

Resume: Yes