Summary:

Experienced Design Verification Engineer with a Master's degree in Electrical and Computer Engineering, seeking full-time opportunities in Design Verification and Validation/Application/Product Engineering roles.

Name: Hari Shashank Singh Thakur

Email: harishashanksinghthakur@gmail.com

Phone: 971-427-9428

Location: Corvallis, Oregon

Country: USA

Position Category: Design: Physical Design

Desired Job Role: Design Verification Engineer

University: Portland State University, Osmania University

Degree: Master of Science in Electrical and Computer Engineering, Bachelor of Engineering in Electronics and Communication

Graduation Year: 2024

Years Experience: 1.5

Notable Companies: Onsemi

Top Skills: System Verilog, UVM, Verilog, C

Job Intention: Full-time

Visa Status: H1B visa with 60 days remaining

Able: yes

Subject: Looking for Full Time Opportunities starting Immediately

Date Sent: 03-24-2025

Resume: Yes