

Hi Team,

I am Adwyck, MS EE student at Columbia University. I'm interested in the Formal Verification role at AheadComputing. I have taken the Formal Verification course here at Columbia University and have hands-on experience in formal verification flow and tools.

I have successfully formally verified fifo implementation with injected bugs, and completed course project where I formally verified a VGA monitor-based game controller on FPGA.

Im also working on RISC-V-based GPU vortex to develop a hardware compiler that determines hardware bottlenecks for running a particular software.

I can see the teams at AheadComputing have teams working on micro-architecture and formal verification. I would like to put up my profile for consideration for both teams. I have attached my resume and would be more than happy to take this discussion further.

Best regards,

Adwyck Gupta

Hi team,

I wanted to follow up on my last email. Please do let me know if you have any questions.

Sincerely,

Adwyck