

Summary:

Anurag Desai is a Computer Engineer with a Master's degree from Texas A&M University and a Bachelor's degree from Vellore Institute of Technology. He has experience working at Changing The Present, Oracle Corporation, and Siemens. His skills include RTL Design, Digital IC Design, CDC, Design Verification, UVM, AMBA Protocols, STA, Low Power Design, and proficiency in several programming languages.

Name: Anurag Desai

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Phone: (979)-326-9705

Location: Austin, USA

Country: USA

Position Category: Design: Logic design, Design: Physical Design, Verification: Formal verification, Verification: Design verification, Architecture

Desired Job Role: Digital VLSI and Computer Architecture Engineer

University: Texas A&M University, College Station, Vellore Institute of Technology, Vellore

Degree: Master of Science in Computer Engineering, Bachelor of Technology in Electronics and Communication Engineering

Graduation Year: 2024, 2020

Years Experience: 4

Notable Companies: Changing The Present, Oracle Corporation, Siemens

Top Skills: RTL Design, Digital IC Design, CDC, Design Verification, UVM, AMBA Protocols, STA, Low Power Design, Verilog HDL, SystemVerilog, C, C++, Python, TCL, BASH

Job Intention: full-time

Visa Status: F1 - OPT VISA

Able: no

Subject: Application for Hardware engineering roles

Date Sent: 03-22-2025

Resume: Yes