

## Summary:

Vivek Ramanathan is a former Intel formal verification engineer with a Master's degree in Computer Science from The University of Texas at Austin. He has experience in formal verification, software engineering, and research, with notable companies including Intel Corporation and Meta. His top skills include C++, SystemVerilog, ACL2 Theorem Prover, and Python.

Name: Vivek Ramanathan

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Phone: +1 (512) 954 8234

Location: --

Country: --

Position Category: Verification: Formal verification

Desired Job Role: Formal Verification Engineer

University: The University of Texas at Austin

Degree: Master of Science in Computer Science, Bachelor of Science in Computer Science, Bachelor of Science in Mathematics

Graduation Year: 2023

Years Experience: 3

Notable Companies: Intel Corporation, ForrestHunt, Inc., PRO Unlimited at Meta

Top Skills: C++/C, SystemVerilog, ACL2 Theorem Prover, SystemVerilog Assertions, SPICE Circuit Simulation, x86, Python, Git

Job Intention: full-time

Visa Status: --

Able: no

Subject: Working at AheadComputing

Date Sent: 02-26-2025

Resume: Yes