

Summary:

Sayantan is a graduate student at the University of Minnesota pursuing a Master of Science in Electrical and Computer Engineering. He has over seven years of experience in physical design, with notable roles at Synapse Design Automation, Pozibility Technologies, and Tata Elxsi. His expertise includes advanced technology nodes, power, performance, and area optimization, and he is proficient in EDA tools like Synopsys ICC2, PrimeTime, Cadence Innovus, and Genus, as well as Python and TCL scripting.

Name: Sayantan

Email: --

Phone: --

Location: Minnesota

Country: USA

Position Category: Design: Physical Design

Desired Job Role: Physical Design Engineer

University: University of Minnesota

Degree: Master of Science in Electrical and Computer Engineering

Graduation Year: --

Years Experience: 7

Notable Companies: Synapse Design Automation, Pozibility Technologies, Tata Elxsi

Top Skills: Physical Design, EDA tools like Synopsys ICC2, PrimeTime, Cadence Innovus, and Genus, Python, TCL, Low-power design methodologies, Multi-corner multi-mode (MCMM) constraints, Advanced clock distribution techniques

Job Intention: full-time

Visa Status: --

Able: no

Subject: Application for the Staff Physical Design Engineer CPU PPA Optimization

Date Sent: 03-16-2025

Resume: No