

## Summary:

Umer Cheema is a Deep Learning Hardware Engineer with over a decade of experience in hardware engineering. He has developed a strong expertise in designing and optimizing high-performance computing systems. His skills include Accelerator Design, Micro-architecture, Digital Logic Design, NPU Architecture, and many more. He has worked at notable companies like Intel Corporation and Whizz Systems Inc.

Name: Umer Cheema

Email: cheema.umer@gmail.com

Phone: 408-858-5362

Location: Portland, Oregon, 97229

Country: USA

Position Category: Design: Logic design

Desired Job Role: Senior CPU Microarchitecture & Logic Design Engineer

University: University of Illinois at Chicago, University of Engineering & Technology

Degree: Ph.D. Electrical & Computer Engineering, Master of Science (MS) in Electrical & Computer Engineering, Bachelor of Science (B.Sc.), Electrical Engineering

Graduation Year: {'Ph.D. Electrical & Computer Engineering': 2016, 'Master of Science (MS) in Electrical & Computer Engineering': 2014, 'Bachelor of Science (B.Sc.), Electrical Engineering': 2008}

Years Experience: 13

Notable Companies: Intel Corporation, Whizz Systems Inc

Top Skills: Accelerator Design, Micro-architecture, Digital Logic Design, NPU Architecture, Hardware-Software Co-design, FPGA Design, Verilog, System Verilog, Custom Silicon Architecture, Hardware Programming, Numerics, IEEE754, Power Performance Area (PPA) optimization, Embedded Systems, Hardware Prototyping, Software Development, High-Performance Computing,

Computer Architecture

Job Intention: full-time

Visa Status: U.S Citizen

Able: yes

Subject: CPU Microarchitecture & Logic Design Engineer, Execution Unit

Date Sent: 02-25-2025

Resume: Yes