Summary:

Roman Kapur is a current student at Carnegie Mellon University, pursuing a Masters of Science in Electrical & Computer Engineering. He has work experience as a Hardware Engineering Intern at Advanced Micro Devices and as a Systems Research & Design Intern at Sandia National Laboratories. He has strong skills in various programming languages and tools such as SystemVerilog, C, Python, Rust, Perl, TCL, C++, Java, Cadence Innovus, Genus Synthesis, Linux, Git, RTL, Cadence EDA, Synopsys Yield Explorer, Scripting, MATLAB, EDA, and Excel.

Name: Roman Kapur

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Phone: 630.699.1279

Location: CMU, PA

Country: USA

Position Category: Internship

Desired Job Role: Hardware Engineering Intern

University: Carnegie Mellon University

Degree: Masters of Science in Electrical & Computer Engineering, Bachelor of Science in Electrical & Computer Engineering

Graduation Year: 2025

Years Experience: 2

Notable Companies: Advanced Micro Devices, Carnegie Mellon University, Sandia National Laboratories

Top Skills: SystemVerilog, C, Python, Rust, Perl, TCL, C++, Java, Cadence Innovus, Genus Synthesis, Linux, Git, RTL, Cadence EDA, Synopsys Yield Explorer, Scripting, MATLAB, EDA, Excel

Job Intention: Internship

Visa Status: --

Able: No

Subject: Internship Inquiry

Date Sent: 02-28-2025

Resume: Yes