

Hi,

I am Suhrud Kelkar and I am currently working as a Design and Verification Engineer at Intel, Oregon.

I work on design and verification of the DDR IO IP on our testchip. This includes digital and formal verification of the complete high speed IO.

I am looking for some new exciting opportunities in the field of design verification.

I found an interesting position on your website for a Formal verification engineer.

I think I'd be a good fit for this position.

Please let me know if you are still hiring.

Please find my resume attached.

Thank you.

Suhrud Kelkar.