Dear Hiring Team at AheadComputing,

I hope this email finds you well. I am excited about the opportunity to contribute to AheadComputings mission of delivering compelling RISC-V CPU core IP. I am currently pursuing my Masters in Electrical and Computer Engineering at the University of Michigan, Ann Arbor.

Throughout my academic and project experiences, I have worked extensively on digital design, verification, and processor architecture. My recent work includes designing a reservation station, reorder buffer, and store queue for a superscalar MIPS R-10K style out-of-order processor, as well as implementing Winograd transformation and ReLU units for a CNN accelerator. Through these projects, I have developed strong proficiency in SystemVerilog and Verilog.

I have attached my resume and cover letter for your review. I would love the opportunity to discuss how my skills and experience align with AheadComputings goals. Please let me know if there is a convenient time to connect.

Thank you for your time and consideration. I look forward to hearing from you.

Best regards,

Rohit Joshi