

Summary:

Furkan Yigit Akyildiz is a Design Verification Engineer with a strong background in semiconductor design and verification in the Space & Defense industry. He has academic experience in Electrical-Electronics Engineering as a Teaching Assistant and is currently working at Purdue University.

Name: Furkan Yigit Akyildiz

Email: fyabu@bu.edu

Phone: 617-971-7975

Location: Providence, RI

Country: USA

Position Category: Verification: Design verification

Desired Job Role: Design Verification Engineer

University: Boston University, Baskent University

Degree: Master of Science in Project Management, Bachelor of Science in Electrical-Electronics Engineering

Graduation Year: --

Years Experience: 3

Notable Companies: Purdue System-on-Chip Extension Technologies, STG Engineering, Boston University Metropolitan College

Top Skills: C, VHDL, SystemVerilog, UVM, SVA, TCL, Shell, Python, Synopsys VCS, Modelsim

Job Intention: full-time

Visa Status: Does not require sponsorship

Able: no

Subject: RTL Design Verification Role

Date Sent: 04-10-2025

Resume: Yes