Summary:

M S Sai Kamesh is a Verification Engineer with 3.5 years of experience in VLSI Design Verification. He has a strong understanding of pcie protocol and amba interfaces, and is skilled in UVM, SV, Verilog, and various protocols. He has worked with notable companies such as Green Wave Radios and Excel Vlsi technology.

Name: M S Sai Kamesh

Email: sai.ss.682@gmail.com

Phone: 8790460360

Location: --

Country: --

Position Category: Verification: Design verification

Desired Job Role: PCIe Verification Engineer

University: Raghu institute of technology

Degree: B Tech in Electronics and Communication Engineering

Graduation Year: 2021

Years Experience: 3.5

Notable Companies: Green Wave Radios, Excel Vlsi technology

Top Skills: UVM, SV, Verilog, PCIe gen4, PIPE-5.1, USB3.2, AXI, APB

Job Intention: full-time

Visa Status: --

Able: no

Subject: Working at AheadComputing

Date Sent: 02-26-2025

Resume: Yes