

Summary:

Saugat Sharma is a PhD candidate in computer engineering, graduating in 2025, with extensive experience in RTL design and verification. He currently works as an FPGA design engineer and has a strong background in various programming languages and tools.

Name: Saugat Sharma

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Phone: +1 (702) 769 9169

Location: Austin, Texas

Country: USA

Position Category: Design: Logic design, Verification: Design verification

Desired Job Role: RTL Design and Verification Engineer

University: University of Nevada, Las Vegas, Tribhuvan University, Nepal

Degree: Doctorate in Electrical and Computer Engineering, Bachelors of Science in Electronics and Communication Engineering

Graduation Year: 2025

Years Experience: 6

Notable Companies: I/ONX Neuromorphic, University of Nevada, Las Vegas

Top Skills: Python, C/C++, C#, embedded C, Matlab, Git, LaTeX, Assembly, Verilog, SV, VHDL, UVM, TensorFlow, PyTorch, OpenCV, Scikit-Learn, Linux, AVR, ARM, x86, Raspberry Pi, Arduino, VCS, DC, PowerArtist, Modelsim, Quartus, Vivado, Xilinx ISE

Job Intention: full-time

Visa Status: --

Able: no

Subject: Career: Opportunity at ahead computing

Date Sent: 03-03-2025

Resume: Yes