Summary:

Eduardo Jimnez Lpez is a Digital Design Engineer with over 8 years of experience in design and verification for hardware architectures. He has proven ability for RTL design, verification, and integration of large systems. He has worked with Micron Semiconductor and Intel Corporation.

Name: Eduardo Jimnez Lpez

Email: eduardojilo@outlook.com

Phone: +523118479362

Location: Guadalajara, JAL, Mxico

Country: Mexico

Position Category: Design: Logic design

Desired Job Role: Logic Design Engineer

University: Tecnolgico de Monterrey ITESM, Universidad de Guadalajara UdeG

Degree: M.S. Computer Science, B.S Electronics and Communications Eng.

Graduation Year: 2023, 2017

Years Experience: 8

Notable Companies: Micron Semiconductor, Intel Corporation

Top Skills: Verilog/SystemVerilog, Computer architecture, Performance analysis, Object-oriented programming, RTL development, C/C++, Python, Linux

Job Intention: full-time

Visa Status: --

Able: yes

Subject: Intersted of joining AheadComputing!

Date Sent: 03-14-2025

Resume: Yes