

Summary:

Jessica Vaz is a first-year master's student at the University of Illinois Urbana-Champaign with experience in ASIC design engineering at SiFive. She is seeking a summer internship related to computer architecture and RTL design at AheadComputing.

Name: Jessica Vaz

Email: jessicavazd@gmail.com

Phone: +1 217-200-0956

Location: Champaign, IL, USA

Country: USA

Position Category: Internship

Desired Job Role: RTL Design Intern

University: University of Illinois Urbana-Champaign

Degree: Master of Science in Electrical and Computer Engineering

Graduation Year: 2026

Years Experience: --

Notable Companies: SiFive

Top Skills: RTL Design, ASIC Design, System Verilog, FPGA Design

Job Intention: Internship

Visa Status: --

Able: no

Subject: Seeking Summer Internship opportunities @ AheadComputing

Date Sent: 03-10-2025

Resume: Yes