

Summary:

Experienced ASIC hardware logic design professional with a focus on CPU/GPU micro-architecture seeking challenging roles in research and development. Passionate about innovative architectural solutions.

Name: Johnsy Kanjirapallil John

Email: johnsyjohnk@gmail.com

Phone: 201-532-0032

Location: San Jose, CA

Country: USA

Position Category: --

Desired Job Role: CPU/GPU Microarchitecture Research and Development Engineer

University: New Jersey Institute of Technology, Rajiv Gandhi Institute of Technology

Degree: PhD Candidate/Course Work Completed in Computer Engineering, Masters in Computer Engineering, Bachelors in Electronics and Communication Engineering

Graduation Year: --

Years Experience: 16

Notable Companies: Microsoft Corporation, Intel Corporation, Northrop Grumman, Intel Federal LLC, Qualcomm CDMA Technologies, AMD Boston Design Center

Top Skills: ASIC hardware logic design, CPU/GPU micro-architecture, RTL development, Verilog, System Verilog, C, C++, Python, CPU/GPU microarchitecture, Instruction fetch, prefetch, branch prediction

Job Intention: --

Visa Status: --

Able: no

Subject: Working at AheadComputing

Date Sent: 02-28-2025

Resume: Yes