

## Summary:

Sheik Dhawood Ashfaq Asick Ali is a Masters student in Computer Engineering at the University of Southern California, specializing in Hardware Design. He has experience in RTL design, verification, synthesis, and physical implementation, with a focus on low-power techniques and system-on-chip (SoC) architectures. He has worked on ASIC tape-out and ML Accelerators, and has experience in mapping ASIC RTL to FPGA platforms. He has also worked as a System Engineer at Tata Consultancy Services in India.

Name: Sheik Dhawood Ashfaq Asick Ali

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Phone: +1 (213) 675-3715

Location: Los Angeles, CA

Country: USA

Position Category: Design: Logic design

Desired Job Role: ASIC Design Engineer

University: University of Southern California, Amrita Vishwa Vidyapeetham

Degree: Masters of Science in Electrical and Computer Engineering, Bachelor of Technology in Electrical and Electronics Engineering

Graduation Year: 2025, 2021

Years Experience: 2

Notable Companies: Tata Consultancy Services

Top Skills: RTL design, ASIC, SoC, FPGA, SystemVerilog, Verilog, VHDL, C++, Python

Job Intention: full-time

Visa Status: --

Able: no

Subject: Passionate Digital design Engineer Bringing Trojan Spirit to AheadComputing

Date Sent: 02-20-2025

Resume: Yes