

Summary:

Manuel R. Muro, Jr. is an experienced engineer with a strong background in logic design, verification, and microarchitecture. He has worked at notable companies such as Qualcomm, Intel, and Honeywell. He has a passion for mentoring the next generation of engineers and brings a strong team player mindset to his work. He has significant knowledge and skills in Machine Learning and the design of Artificial Neural Network functional blocks.

Name: Manuel R. Muro, Jr.

Email: manny.muro@gmail.com

Phone: (503) 515-3818

Location: Portland, OR

Country: USA

Position Category: Design: Logic design

Desired Job Role: Senior Staff CPU Microarchitecture & Logic Design Engineer

University: North Carolina State University, Florida Institute of Technology, Kingsland University

Degree: B.S.E.E. with a Minor in Business, Full Stack-Block Chain Developer Program, C++, VLSI Processing and DSP Graduate Courses

Graduation Year: 1995

Years Experience: 27

Notable Companies: Qualcomm, Honeywell Corp, Intel Corp, Megh Computing Corp, Cirrus Logic, Hewlett-Packard, Corp., Northwest Logic Corp, Vefxi Corp, Teradyne Corp, Cypress Semiconductor, Intersil Corporation, Harris Corporation

Top Skills: Verilog, SystemVerilog, OVM/UVM, VHDL, Python, Perl, C/C++, Assembly Language, SQL, Matlab/Octave, JSON, Machine Learning, Artificial Neural Network Design

Job Intention: full-time

Visa Status: --

Able: yes

Subject: Working at AheadComputing (Sr. Staff CPU uArch & Logic DE, OoO Exec - Candidate)

Date Sent: 03-07-2025

Resume: Yes