

Dear Hiring Manager,

Im excited to apply for the Senior CPU Microarchitecture & Logic Design Engineer role at ahead computing. With an MS in Electrical Engineering and experience in SOC DFT at AMD with hands on personal projects on optimizing an ALU for timing and power, and designing a high-performance cache controller, Im eager to contribute my RTL design, microarchitecture, and verification skills to your team.

Id welcome the chance to discuss how I can support your projects. Thank you for your consideration.

Thanks, Neh