

Summary:

Balaji Velikandanathan is a seasoned VLSI professional with expertise in synthesis, P&R, signoff flows, and methodology. He has held various technical and managerial roles at Intel, Qualcomm, and Synopsys. His experience includes CAD tool development, PD implementation, flow and methodology development, DfX - DV execution, and RTL to GDS implementation. He has worked on the implementation of multiple CPU, Chipset, and Mobile SOC's across process nodes from TSMC, SEC, and INTC foundries.

Name: Balaji Velikandanathan

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Phone: +1-(425)-770-7186

Location: Austin, TX 78717

Country: USA

Position Category: Design: Physical Design

Desired Job Role: Senior Solutions Architect

University: Syracuse University, Guindy Engineering College, Anna University

Degree: M.S., in Computer Science, B.E., in Electronics and Communication Engineering

Graduation Year: --

Years Experience: 23

Notable Companies: Intel, Qualcomm, Synopsys

Top Skills: synthesis, P&R, signoff flows, methodology, CAD tool development, PD implementation, Flow and methodology development, DfX - DV execution, RTL to GDS implementation

Job Intention: full-time

Visa Status: --

Able: no

Subject: Resume

Date Sent: 03-20-2025

Resume: Yes