Summary:

Andrew P. Thomas is a 4th year student at Oregon State University, majoring in Electrical and Computer Engineering with a minor in Computer Science. He has a deep interest in CPU, GPU and AI design. He has a strong skill set including VHDL, System Verilog Design, RISC-V ISA, CPU and GPU Architectures, Cadence Virtuoso, FPGA programming, and more. He is seeking a Masters/PhD Program at a leading university.

Name: Andrew P. Thomas

Email: thomasa3@oregonstate.edu

Phone: 971-716-4882

Location: 5696 NW 128TH TER, Portland, OR 97229

Country: USA

Position Category: Architecture

Desired Job Role: Computer Architecture Engineer

University: Oregon State University

Degree: Bachelors in Electrical and Computer Engineering with Computer Science Minor

Graduation Year: 2025

Years Experience: 4

Notable Companies:

Top Skills: VHDL, System Verilog Design, RISC-V ISA, CPU and GPU Architectures, Cadence Virtuoso, FPGA programming, PCB design, Nix-based Operating Systems, C, C++, Assembly, Spice, Python, JAVA, Analog and Digital Circuit Design

Job Intention: Seeking Masters/PhD Program at a leading university

Visa Status: U.S. citizen

Able: yes

Subject: Resume - Andrew Thomas

Date Sent: 03-18-2025

Resume: Yes