

## Summary:

Gayatri Sridhar is a Master of Science Electrical Engineering student with experience in ASIC flow, Clock Tree Synthesis, and CPU, GPU & TPU architectures. She has worked as a Circuit Design Intern at AMD and as an Application Development Associate at Accenture. She is proficient in various tools and programming languages such as Cadence Virtuoso, MentorGraphics ModelSim, Verilog, Python, and C++.

Name: Gayatri Sridhar

Email: gayatris@usc.edu

Phone: +1(408)-592-1517

Location: Los Angeles, CA

Country: USA

Position Category: Design: Logic design

Desired Job Role: VLSI Design Engineer

University: University of Southern California, IP University, Delhi, India

Degree: Master of Science Electrical Engineering, Bachelor of Technology in Electronics & Communications Engineering

Graduation Year: 2025, 2021

Years Experience: 2

Notable Companies: AMD, Accenture

Top Skills: Cadence Virtuoso, MentorGraphics ModelSim, Custom Compiler, Vivado, HSPICE, Verilog, TCL, System Verilog, UVM, Python, SVA, C++, Object Oriented Programming, Pytorch, RTL Design, ASIC flow, Clock Tree Synthesis, Clock Distribution, CPU, GPU & TPU architectures

Job Intention: full-time

Visa Status: --

Able: no

Subject: Gayatri - Resume

Date Sent: 04-21-2025

Resume: Yes