

Hi @aheadcomputing,

Hope you are doing well.

I am Mallikarjun Kasha, graduated with masters in Electrical and Computer Engineering. So, I am currently looking for opportunities in the field of VLSI as a ASIC/RTL Design, Verification, Validation Engineer and I have interest in this Senior CPU Microarchitecture & Logic Design Engineer, Out-of-Order Execution job. With experience in RTL Design in System Verilog, Verilog and UVM, I am eager to contribute to your team and would love to discuss this. Please find the attached resume for your reference, I just need one opportunity to prove my skills. Looking forward to hearing from you. Thank you for your time and consideration.

Thanks & Regards

Mallikarjun Kasha