

Hello

My name is Kumar Subramanian and I am writing to express my interest for the position of Physical design Engineer at Ahead Computing.

I have close to 30 years of experience in the areas of high performance CPU designs, tool, flow, methodology development and convergence of PPA critical CPU subsystems. In my 25 years at Intel I have worked on the backend implementation of high performance Intel Architecture (IA) CPUs spanning several leading edge architecture and process (both internal and external) nodes, all of which have entered High Volume Manufacturing (HVM).

I have a proven track record of delivering scalable, modular and highly reproducible tools, flows, methodologies (TFM) and Power, Performance, Area (PPA) recipes for high performance CPU designs.

In addition, I have a history of working closely with EDA vendors to drive major enhancements such as Next Generation Latch Analysis (NGLA) in Primetime, and native support for Structural Multi-source CTS in ICC2/FusionCompiler amongst others. I have successfully led cross functional teams across diverse geographies and EDA tool vendors to drive innovation and achieve aggressive PPA targets.

Most recently, I led the development of Atom Soft IP (Atom SIP) development, which has paved the way for Intel to pursue a dual foundry vendor (i.e. Internal and External) strategy, culminating in several (flagship) products that are in HVM.

I was awarded the Intel Achievement Award (IAA), for the first Intel owned IA architecture CPU implemented on an external process node as a test chip, to prove Atom SIP readiness and in parallel, investigate Intels readiness to manufacture its flagship products on external foundries.

I am very well versed/experienced in all aspects of TFM development and back end design convergence of PPA critical blocks/subsystems. In addition I have been responsible for STA sign-off of several generations of CPUs at Intel.

I have a solid track record of mentoring junior engineers and help them grow in the (technical)

career path, and am proud of the fact that several of them are principal engineers at Intel.

I look forward for an opportunity, to discuss how I can bring my data driven decision making, extensive RTL2GDS, sign-off verification (STA/Power), TFM and convergence skills/expertise to team

Ahead Computing

I have attached my resume for your perusal

-Kumar