

IBM

Personal Computer

IBM Enhanced Graphics Adapter

IBM ENHANCED GRAPHICS ADAPTER

Contents

Description	1
Major Components	3
Modes of Operation	5
Basic Operations	8
Registers	12
Programming Considerations	62
Programming the Registers	62
RAM Loadable Character Generator	69
Creating a 512 Character Set	70
Creating an 80 by 43 Alphanumeric Mode	71
Vertical Interrupt Feature	72
Creating a Split Screen	73
Compatibility Issues	74
Interface	76
Feature Connector	76
Specifications	79
System Board Switches	79
Configuration Switches	80
Direct Drive Connector	83
Light Pen Interface	84
Jumper Descriptions	85
Logic Diagrams	87
BIOS Listing	103
Vectors with Special Meanings	103
Index	Index-1

Description

The IBM Enhanced Graphics Adapter (EGA) is a graphics controller that supports both color and monochrome direct drive displays in a variety of modes. In addition to the direct drive port, a light pen interface is provided. Advanced features on the adapter include bit-mapped graphics in four planes and a RAM (Random Access Memory) loadable character generator. Design features in the hardware substantially reduce the software overhead for many graphics functions.

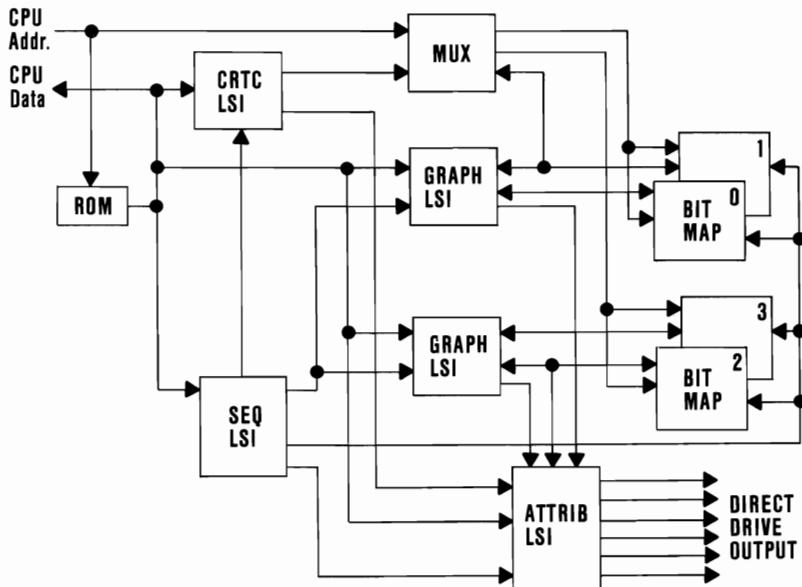
The Enhanced Graphics Adapter provides Basic Input Output System (BIOS) support for both alphanumeric (A/N) modes and all-points-addressable (APA) graphics modes, including all modes supported by the Monochrome Display Adapter and the Color/Graphics Monitor Adapter. Other modes provide APA 640x350 pel graphics support for the IBM Monochrome Display, full 16 color support in both 320x200 pel and 640x200 pel resolutions for the IBM Color Display, and both A/N and APA support with resolution of 640x350 for the IBM Enhanced Color Display. In alphanumeric modes, characters are formed from one of two ROM (Read Only Memory) character generators on the adapter. One character generator defines 7x9 characters in a 9x14 character box. For Enhanced Color Display support, the 9x14 character set is modified to provide an 8x14 character set. The second character generator defines 7x7 characters in an 8x8 character box. These generators contain dot patterns for 256 different characters. The character sets are identical to those provided by the IBM Monochrome Display Adapter and the IBM Color/Graphics Monitor Adapter.

The adapter contains 64K bytes of storage configured as four 16K byte bit planes. Memory expansion options are available to expand the adapter memory to 128K bytes or 256K bytes.

The adapter is packaged on a single 13-1/8 inch (333.50 mm) card. The direct drive port is a right-angle mounted connector at the rear of the adapter and extends through the rear panel of the system unit. Also on the card are five large scale integration (LSI) modules custom designed for this controller.

Located on the adapter is a feature connector that provides access to internal functions through a 32-pin berg connector. A separate 64-pin connector provides an interface for graphics memory expansion.

The following is a block diagram of the Enhanced Graphics Adapter:



Enhanced Graphics Adapter Block Diagram

Major Components

CRT Controller

The CRT (Cathode Ray Tube) Controller (CRTC) generates horizontal and vertical synchronous timings, addressing for the regenerative buffer, cursor and underline timings, and refresh addressing for the dynamic RAMs.

Sequencer

The Sequencer generates basic memory timings for the dynamic RAMs and the character clock for controlling regenerative memory fetches. It allows the processor to access memory during active display intervals by inserting dedicated processor memory cycles periodically between the display memory cycles. Map mask registers are available to protect entire memory maps from being changed.

Graphics Controller

The Graphics Controller directs the data from the memory to the attribute controller and the processor. In graphics modes, memory data is sent in serialized form to the attribute chip. In alpha modes the memory data is sent in parallel form, bypassing the graphics controller. The graphics controller formats the data for compatible modes and provides color comparators for use in color painting modes. Other hardware facilities allow the processor to write 32 bits in a single memory cycle, (8 bits per plane) for quick color presetting of the display areas, and additional logic allows the processor to write data to the display on non-byte boundaries.

Attribute Controller

The Attribute Controller provides a color palette of 16 colors, each of which may be specified separately. Six color outputs are

available for driving a display. Blinking and underlining are controlled by this chip. This chip takes data from the display memory and formats it for display on the CRT screen.

Display Buffer

The display buffer on the adapter consists of 64K bytes of dynamic read/write memory configured as four 16K byte video bit planes. Two options are available for expanding the graphics memory. The Graphics Memory Expansion Card plugs into the memory expansion connector on the adapter, and adds one bank of 16K to each of the four bit planes, increasing the graphics memory to 128K bytes. The expansion card also provides DIP sockets for further memory expansion. Populating the DIP sockets with the Graphics Memory Module Kit adds two additional 16K banks to each bit plane, bringing the graphics memory to its maximum of 256K bytes.

The address of the display buffer can be changed to remain compatible with other video cards and application software. Four locations are provided. The buffer can be configured at segment address hex A0000 for a length of 128K bytes, at hex A0000 for a length of 64K bytes, at hex B0000 for a length of 32K bytes, or at hex B8000 for a length of 32K bytes.

BIOS

A read-only memory (ROM) Basic Input Output System (BIOS) module on the adapter is linked to the system BIOS. This ROM BIOS contains character generators and control code and is mapped into the processor address at hex C0000 for a length of 16K bytes.

Support Logic

The logic on the card surrounding the LSI modules supports the modules and creates latch buses for the CRT controller, the

processor, and character generator. Two clock sources (14 MHz and 16 MHz) provide the dot rate. The clock is multiplexed under processor I/O control. Four I/O registers also resident on the card are not part of the LSI devices.

Modes of Operation

IBM Color Display

The following table describes the modes supported by BIOS on the IBM Color Display:

MODE #	TYPE	COLORS	ALPHA FORMAT	BUFFER START	BOX SIZE	MAX. PAGES	RESOLUTION
0	A/N	16	40x25	B8000	8x8	8	320x200
1	A/N	16	40x25	B8000	8x8	8	320x200
2	A/N	16	80x25	B8000	8x8	8	640x200
3	A/N	16	80x25	B8000	8x8	8	640x200
4	APA	4	40x25	B8000	8x8	1	320x200
5	APA	4	40x25	B8000	8x8	1	320x200
6	APA	2	80x25	B8000	8x8	1	640x200
D	APA	16	40x25	A0000	8x8	2/4/8	320x200
E	APA	16	80x25	A0000	8x8	1/2/4	640x200

Modes 0 through 6 emulate the support provided by the IBM Color/Graphics monitor Adapter.

Modes 0,2 and 5 are identical to modes 1,3 and 4 respectively at the adapter's direct drive interface.

The Maximum Pages fields for modes D and E indicate the number of pages supported when 64K, 128K or 256K bytes of graphics memory is installed, respectively.

IBM Monochrome Display

The following table describes the modes supported by BIOS on the IBM Monochrome Display.

MODE #	TYPE	COLORS	ALPHA FORMAT	BUFFER START	BOX SIZE	MAX. PAGES	RESOLUTION
7	A/N	4	80x25	B0000	9x14	8	720x350
F	APA	4	80x25	A0000	8x14	1/2	640x350

Mode 7 emulates the support provided by the IBM Monochrome Display Adapter.

IBM Enhanced Color Display

The Enhanced Graphics Adapter supports attachment of the IBM Enhanced Color Display. The IBM Enhanced Color Display is capable of running at the standard television frequency of 15.75 KHz as well as running 21.85 KHz. The table below summarizes the characteristics of the IBM Enhanced Color Display:

PARAMETER	TV FREQUENCY	HIGH RESOLUTION
Horiz Scan Rate	15.75 KHz.	21.85 KHz.
Vertical Scan Rate	60 Hz.	60 Hz.
Video Bandwidth	14.318 MHz.	16.257 MHz.
Displayable Colors	16 Maximum	16 or 64
Character Size	7 by 7 Pels	7 by 9 Pels
Character Box Size	8 by 8 Pels	8 by 14 Pels
Maximum Resolution	640x200 Pels	640 by 350 Pels
Alphanumeric Modes	0,1,2,3	0,1,2,3
Graphics Modes	4,5,6,D,E	10

In the television frequency mode, the IBM Enhanced Color Display displays information identical in color and resolution to the IBM Color Display.

In the high resolution mode, the adapter provides enhanced alphanumeric character support. This enhanced alphanumeric support consists of transforming the 8 by 8 character box into an 8 by 14 character box, and providing 16 colors out of a palette of

64 possible display colors. Display colors are changed by altering the programming of the color palette registers in the Attribute Controller. In alphanumeric modes, any 16 of 64 colors are displayable. the screen resolution is 320x350 for modes 0 and 1, and 640x350 for modes 2 and 3.

The resolution displayed on the IBM Enhanced Color Display is selected by the switch settings on the Enhanced Graphics Adapter.

The Enhanced Color Display is compatible with all modes listed for the IBM Color Display. the following table describes additional modes supported by BIOS for the IBM Enhanced Color Display:

MODE #	TYPE	COLORS	ALPHA FORMAT	BUFFER START	BOX SIZE	MAX. PAGES	RESOLUTION
0*	A/N	16/64	40x25	B8000	8x14	8	320x350
1*	A/N	16/64	40x25	B8000	8x14	8	320x350
2*	A/N	16/64	80x25	B8000	8x14	8	640x350
3*	A/N	16/64	80x25	B8000	8x14	8	640x350
10*	APA	4/16 16/64	80x25	A0000	8x14	1/2	640x350

* Note that modes 0, 1, 2, and 3, are also listed for IBM Color Display support. BIOS provides enhanced support for these modes when an Enhanced Color Display is attached.

The values in the "COLORS" field indicate 16 colors of a 64 color palette or 4 colors of a sixteen color palette.

In mode 10, The dual values for the "COLORS" field and the "MAX. PAGES" field indicate the support provided when 64K or when greater than 64K of graphics memory is installed, respectively.

Basic Operations

Alphanumeric Modes

The data format for alphanumeric modes on the Enhanced Graphics Adapter is the same as the data format on the IBM Color/Graphics Monitor Adapter and the IBM Monochrome Display Adapter. As an added function, bit three of the attribute byte may be redefined by the Character Map Select register to act as a switch between character sets. This gives the programmer access to 512 characters at one time. This function is valid only when memory has been expanded to 128K bytes or more.

When an alphanumeric mode is selected, the BIOS transfers character patterns from the ROM to bit plane 2. The processor stores the character data in bit plane 0, and the attribute data in bit plane 1. The programmer can view bit planes 0 and 1 as a single buffer in alphanumeric modes. The CRTC generates sequential addresses, and fetches one character code byte and one attribute byte at a time. The character code and row scan count address bit plane 2, which contains the character generators. The appropriate dot patterns are then sent to the palette in the attribute chip, where color is assigned according to the attribute data.

Graphics Modes

320x200 Two and Four Color Graphics (Modes 4 and 5)

Addressing, mapping and data format are the same as the 320x200 pel mode of the Color/Graphics Monitor Adapter. The display buffer is configured at hex B8000. Bit image data is stored in bit planes 0 and 1.

640x200 Two Color Graphics (Mode 6)

Addressing, mapping and data format are the same as the 640x200 pel black and white mode of the Color/Graphics

Monitor Adapter. The display buffer is configured at hex B8000. Bit image data is stored in bit plane 0.

640x350 Monochrome Graphics (Mode F)

This mode supports graphics on the IBM Monochrome Display with the following attributes: black, video, blinking video, and intensified video. Resolution of 640x350 requires 56K bytes to support four attributes. By chaining maps 0 and 1, then maps 2 and 3 together, two 32K bit planes can be formed. This chaining is done only when necessary (less than 128K of graphics memory). The first map is the video bit plane, and the second map is the intensity bit plane. Both planes reside at hex address A0000.

Two bits, one from each bit plane, define one picture element (pel) on the screen. The bit definitions for the pels are given in the following table. The video bit plane is denoted by C0 and the Intensity Bit Plane is denoted by C2.

C2	C0	Pixel Color	Valid Attributes
0	0	Black	0
0	1	Video	3
1	0	Blinking Video	C
1	1	Intensified Video	F

The byte organization in memory is sequential. The first eight pels on the screen are defined by the contents of memory in location A000:0H, the second eight pels by location A000:1H, and so on. The first pel within any one byte is defined by bit 7 in the byte. The last pel within the byte is defined by bit 0 in the byte.

Monochrome graphics works in odd/even mode, which means that even CPU addresses go into even bit planes and odd CPU addresses go into odd bit planes. Since both bit planes reside at address A0000, the user must select which plane or planes he desires to update. This is accomplished by the map mask register of the sequencer. (See the table above for valid attributes).

16/64 Color Graphics Modes (Mode 10)

These modes support graphics in 16 colors on either a medium or high resolution monitor. The memory in these modes consists of using all four bit planes. Each bit plane represents a color as shown below. The bit planes are denoted as C0,C1,C2 and C3 respectively.

C0 = Blue Pels

C1 = Green Pels

C2 = Red Pels

C3 = Intensified Pels

Four bits (one from each plane) define one pel on the screen. The color combinations are illustrated in the following table:

I	R	G	B	Color
0	0	0	0	Black
0	0	0	1	Blue
0	0	1	0	Green
0	0	1	1	Cyan
0	1	0	0	Red
0	1	0	1	Magenta
0	1	1	0	Brown
0	1	1	1	White
1	0	0	0	Dark Gray
1	0	0	1	Light Blue
1	0	1	0	Light Green
1	0	1	1	Light Cyan
1	1	0	0	Light Red
1	1	0	1	Light Magenta
1	1	1	0	Yellow
1	1	1	1	Intensified White

The display buffer resides at address A0000. The map mask register of the sequencer is used to select any or all of the bit planes to be updated when a memory write to the display buffer is executed by the CPU.

Color Mapping

The Enhanced Graphics Adapter supports 640x350 Graphics for both the IBM Monochrome and the IBM Enhanced Color

Displays. Four color capability is supported on the EGA without the Graphics Memory Expansion Card (base 64 KB), and sixteen colors are supported when the Graphics Memory Expansion Card is installed on the adapter (128 KB or above). This section describes the differences in the colors displayed depending upon the graphics memory available. Note that colors 0H, 1H, 4H, and 7H map directly regardless of the graphics memory available.

Character Attribute	Monochrome	Mode 10H 64KB	Mode 10H >64KB
00H*	Black	Black	Black
01H*	Video	Blue	Blue
02H	Black	Black	Green
03H	Video	Blue	Cyan
04H*	Blinking	Red	Red
05H	Intensified	White	Magenta
06H	Blinking	Red	Brown
07H*	Intensified	White	White
08H	Black	Black	Dark Gray
09H	Video	Blue	Light Blue
0AH	Black	Black	Light Green
0BH	Video	Blue	Light Cyan
0CH	Blinking	Red	Light Red
0DH	Intensified	White	Light Magenta
0EH	Blinking	Red	Yellow
0FH	Intensified	White	Intensified White

* Graphics character attributes which map directly regardless of the graphics memory available.

Registers

External Registers

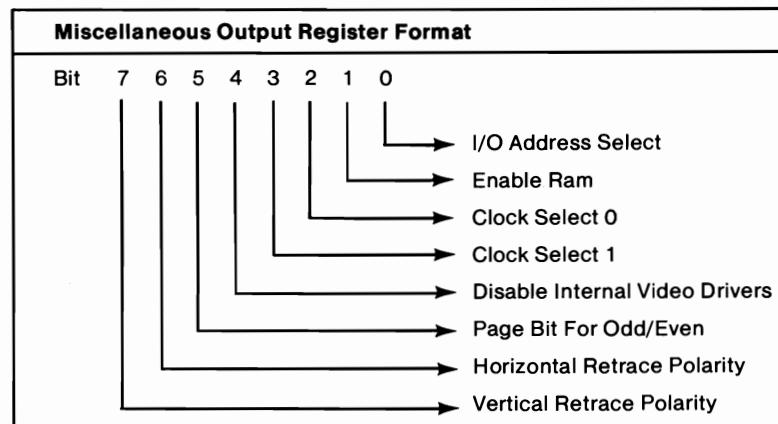
This section contains descriptions of the registers of the Enhanced Graphics Adapter that are not contained in an LSI device.

Name	Port	Index
Miscellaneous Output Register	3C2	-
Feature Control Register	3?A	-
Input Status Register 0	3C2	-
Input Status Register 1	3?2	-

? = B in Monochrome Modes ? = D in Color Modes

Miscellaneous Output Register

This is a write-only register. The processor output port address is hex 3C2. A hardware reset causes all bits to reset to zero.



Bit 0

3BX/3DX CRTC I/O Address—This bit maps the CRTC I/O addresses for IBM Monochrome or Color/Graphics Monitor Adapter emulation. A logical 0 sets CRTC addresses to 3BX and Input Status Register 1's address to 3BA for Monochrome emulation. A logical 1 sets CRTC

addresses to 3DX and Input Status Register 1's address to 3DA for Color/Graphics Monitor Adapter emulation.

Bit 1

Enable RAM—A logical 0 disables RAM from the processor; a logical 1 enables RAM to respond at addresses designated by the Control Data Select value programmed into the Graphics Controllers.

Bit 2-Bit 3

Clock Select—These two bits select the clock source according to the following table:

Bits

3 2

- | | |
|-------------|---|
| 0 0- | Selects 14 MHz clock from the processor I/O channel |
| 0 1- | Selects 16 MHz clock on-board oscillator |
| 1 0- | Selects external clock source from the feature connector. |
| 1 1- | Not used |

Bit 4

Disable Internal Video Drivers—A logical 0 activates internal video drivers; a logical 1 disables internal video drivers. When the internal video drivers are disabled, the source of the direct drive color output becomes the feature connector direct drive outputs.

Bit 5

Page Bit For Odd/Even—Selects between two 64K pages of memory when in the Odd/Even modes (0,1,2,3,7). A logical 0 selects the low page of memory; a logical 1 selects the high page of memory.

Bit 6

Horizontal Retrace Polarity—A logical 0 selects positive horizontal retrace; a logical 1 selects negative horizontal retrace.

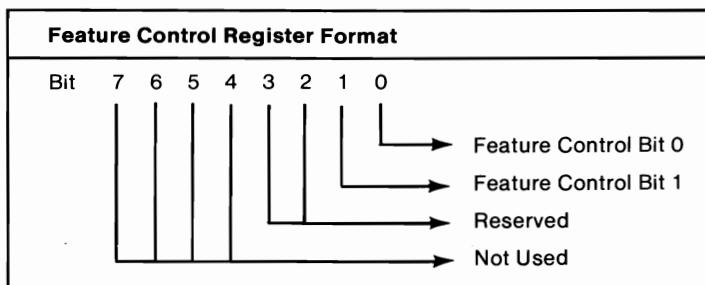
Bit 7

Vertical Retrace Polarity—A logical 0 selects positive vertical retrace; a logical 1 selects

negative vertical retrace. The IBM Monochrome display requires a negative vertical retrace polarity.

Feature Control Register

This is a write-only register. The processor output register is hex 3BA or 3DA.

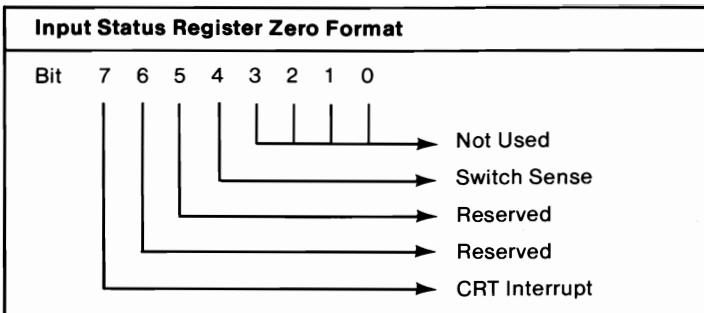


Bits 0 and 1

Feature Control Bits—These bits are used to convey information to the feature connector. The output of these bits goes to the FEAT 0 (pin 19) and FEAT 1 (pin 17) of the feature connector.

Input Status Register Zero

This is a read-only register. The processor input port address is hex 3C2.



Bit 4 **Switch Sense**—When set to 1, this bit allows the processor to read the four configuration switches on the board. The setting of the CLKSEL field determines which switch is being read. The switch configuration can be determined by reading byte 40:88H in RAM.

Bit 3: Switch 4 ; Logical 0 = switch closed

Bit 2: Switch 3 ; Logical 0 = switch closed

Bit 1: Switch 2 ; Logical 0 = switch closed
Bit 2: Switch 1 ; Logical 0 = switch closed

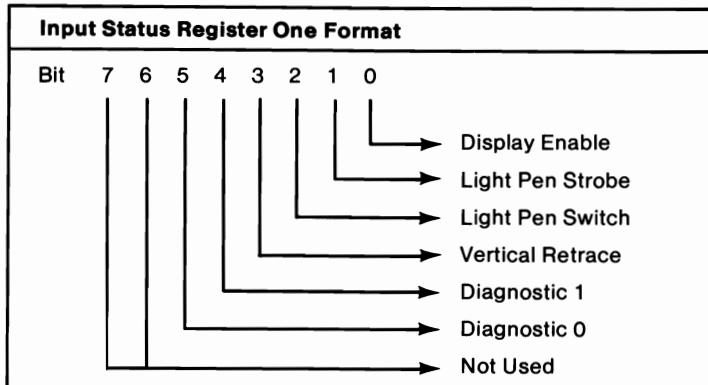
Bit 0: Switch 1 ; Logical 0 = switch closed

Bits 5 and 6 Feature Code—These bits are input from the Feat (0) and Feat (1) pins on the feature connector.

Bit 7	CRT Interrupt—A logical 1 indicates video is being displayed on the CRT screen; a logical 0 indicates that vertical retrace is occurring.
--------------	---

Input Status Register One

This is a read-only register. The processor port address is hex 3BA or hex 3DA.



- Bit 0** Display Enable—Logical 0 indicates the CRT raster is in a horizontal or vertical retrace interval. This bit is the real time status of the display enable signal. Some programs use this status bit to restrict screen updates to inactive display intervals. The Enhanced Graphics Adapter does not require the CPU to update the screen buffer during inactive display intervals to avoid glitches in the display image.
- Bit 1** Light Pen Strobe—A logical 0 indicates that the light pen trigger has not been set; a logical 1 indicates that the light pen trigger has been set.
- Bit 2** Light Pen Switch—A logical 0 indicates that the light pen switch is closed; a logical 1 indicates that the light pen switch is open.
- Bit 3** Vertical Retrace—A logical 0 indicates that video information is being displayed on the CRT screen; a logical 1 indicates the CRT is in a vertical retrace interval. This bit can be programmed to interrupt the processor on interrupt level 2 at the start of the vertical retrace. This is done through bits 4 and 5 of the Vertical Retrace End Register of the CRTC.
- Bits 4 and 5** Diagnostic Usage—These bits are selectively connected to two of the six color outputs of the

Attribute Controller. The Color Plane Enable register controls the multiplexer for the video wiring. The following table illustrates the combinations available and the color output wiring.

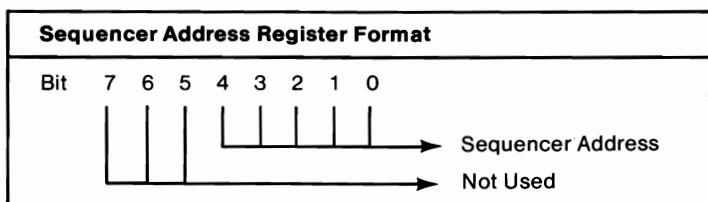
Color Plane Register		Input Status Register One	
Bit 5	Bit 4	Bit 5	Bit 4
0	0	Red	Blue
0	1	Secondary Blue	Green
1	0	Secondary Red	Secondary Green
1	1	Not Used	Not Used

Sequencer Registers

Name	Port	Index
Address	3C4	-
Reset	3C5	00
Clocking Mode	3C5	01
Map Mask	3C5	02
Character Map Select	3C5	03
Memory Mode	3C5	04

Sequencer Address Register

The Address Register is a pointer register located at address hex 3C4. This register is loaded with a binary value that points to the sequencer data register where data is to be written. This value is referred to as "Index" in the table above.

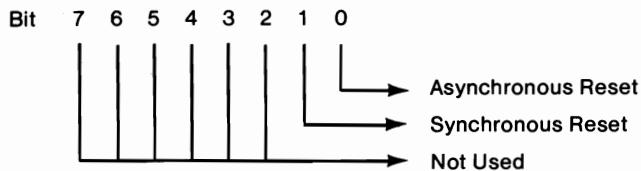


Bit 0-Bit 3

Sequencer Address Bits—A binary value pointing to the register where data is to be written.

Reset Register

This is a write-only register pointed to when the value in the address register is hex 00. The output port address for this register is hex 3C5.

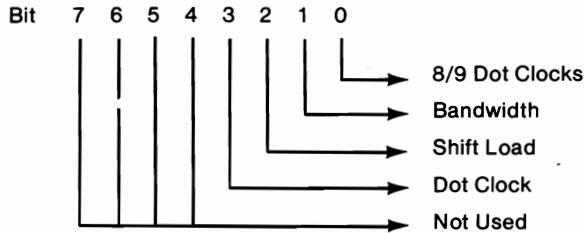
Reset Register Format

Bit 0 Asynchronous Reset—A logical 0 commands the sequencer to asynchronous clear and halt. All outputs are placed in the high impedance state when this bit is a 0. A logical 1 commands the sequencer to run unless bit 1 is set to zero. Resetting the sequencer with this bit can cause data loss in the dynamic RAMs.

Bit 1 Synchronous Reset—A logical 0 commands the sequencer to synchronous clear and halt. Bits 1 and 0 must both be ones to allow the sequencer to operate. Reset the sequencer with this bit before changing the Clocking Mode Register, if memory contents are to be preserved.

Clocking Mode Register

This is a write-only register pointed to when the value in the address register is hex 01. The output port address for this register is hex 3C5.

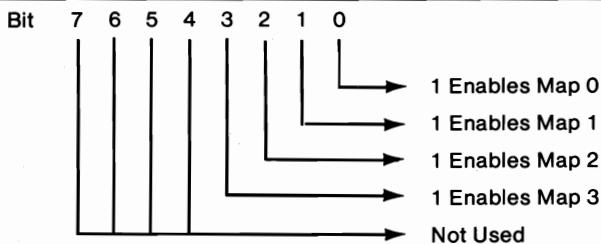
Clocking Mode Register Format

- Bit 0** 8/9 Dot Clocks—A logical 0 directs the sequencer to generate character clocks 9 dots wide; a logical 1 directs the sequencer to generate character clocks 8 dots wide. Monochrome alphanumeric mode (07H) is the only mode that uses character clocks 9 dots wide. All other modes must use 8 dots per character clock.
- Bit 1** Bandwidth—A logical 0 makes CRT memory cycles occur on 4 out of 5 available memory cycles; a logical 1 makes CRT memory cycles occur on 2 out of 5 available memory cycles. Medium resolution modes require less data to be fetched from the display buffer during the horizontal scan time. This allows the CPU greater access time to the display buffer. All high resolution modes must provide the CRTC with 4 out of 5 memory cycles in order to refresh the display image.
- Bit 2** Shift Load—When set to 0, the video serializers are reloaded every character clock; when set to 1, the video serializers are loaded every other character clock. This mode is useful when 16 bits are fetched per cycle and chained together in the shift registers.
- Bit 3** Dot Clock—A logical 0 selects normal dot clocks derived from the sequencer master clock input. When this bit is set to 1, the master clock will be divided by 2 to generate the dot clock. All the other timings will be stretched since they are derived from the dot clock. Dot clock divided by two is used for 320x200 modes (0, 1, 4, 5) to provide a pixel rate of 7 MHz, (9 MHz for mode D).

Map Mask Register

This is a write-only register pointed to when the value in the address register is hex 02. The output port address for this register is hex 3C5.

Map Mask Register Format



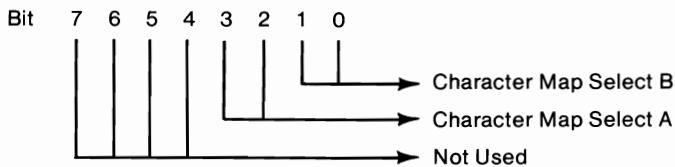
Bit 0–Bit 3

Map Mask—A logical 1 in bits 3 through 0 enables the processor to write to the corresponding maps 3 through 0. If this register is programmed with a value of 0FH, the CPU can perform a 32-bit write operation with only one memory cycle. This substantially reduces the overhead on the CPU during display update cycles in graphics modes. Data scrolling operations are also enhanced by setting this register to a value of 0FH and writing the display buffer address with the data stored in the CPU data latches. This is a read-modify-write operation. When odd/even modes are selected, maps 0 and 1 and maps 2 and 3 should have the same map mask value.

Character Map Select Register

This is a write-only register pointed to when the value in the address register is hex 03. The output port address for this register is 3C5.

Character Map Select Register Format



Bit 0-Bit 1 Character Map Select B—Selects the map used to generate alpha characters when attribute bit 3 is a 0, according to the following table:

Bits 1 0	Map Selected	Table Location
Value		
0 0	0	1st 8K of Plane 2 Bank 0
0 1	1	2nd 8K of Plane 2 Bank 1
1 0	2	3rd 8K of Plane 2 Bank 2
1 1	3	4th 8K of Plane 2 Bank 3

Bit 2-Bit 3 Character Map Select A—Selects the map used to generate alpha characters when attribute bit 3 is a 1, according to the following table:

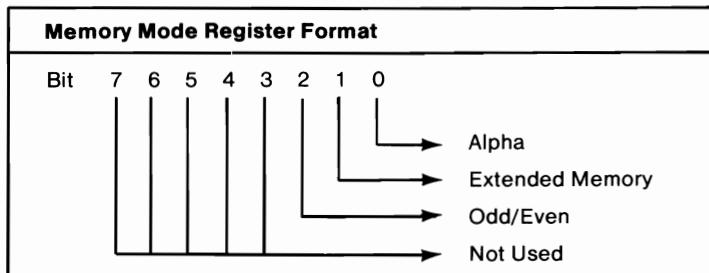
Bits 3 2	Map Selected	Table Location
Value		
0 0	0	1st 8K of Plane 2 Bank 0
0 1	1	2nd 8K of Plane 2 Bank 1
1 0	2	3rd 8K of Plane 2 Bank 2
1 1	3	4th 8K of Plane 2 Bank 3

In alphanumeric modes, bit 3 of the attribute byte normally has the function of turning the foreground intensity on or off. This bit however may be redefined as a switch between character sets. This function is enabled when there is a difference between the value in Character Map Select A and the value in Character Map Select B. Whenever these two values are the same, the character select function is disabled. The memory mode register bit 1 must be a 1 (indicates the memory extension card is installed in the unit) to enable this function; otherwise, bank 0 is always selected.

128K of graphics memory is required to support two character sets. 256K supports four character sets. Asynchronous reset clears this register to 0. This should be done only when the sequencer is reset.

Memory Mode Register

This is a write-only register pointed to when the value in the address register is hex 04. The processor output port address for this register is 3C5.



- | | |
|--------------|--|
| Bit 0 | Alpha—A logical 0 indicates that a non-alpha mode is active. A logical 1 indicates that alpha mode is active and enables the character generator map select function. |
| Bit 1 | Extended Memory—A logical 0 indicates that the memory expansion card is not installed. A logical 1 indicates that the memory expansion card is installed and enables access to the extended memory through address bits 14 and 15. |
| Bit 2 | Odd/Even—A logical 0 directs even processor addresses to access maps 0 and 2, while odd processor addresses access maps 1 and 3. A logical 1 causes processor addresses to sequentially access data within a bit map. The maps are accessed according to the value in the map mask register. |

CRT Controller Registers

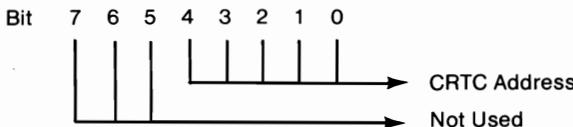
Name	Port	Index
Address Register	3?4	-
Horizontal Total	3?5	00
Horizontal Display End	3?5	01
Start Horizontal Blank	3?5	02
End Horizontal Blank	3?5	03
Start Horizontal Retrace	3?5	04
End Horizontal Retrace	3?5	05
Vertical Total	3?5	06
Overflow	3?5	07
Preset Row Scan	3?5	08
Max Scan Line	3?5	09
Cursor Start	3?5	0A
Cursor End	3?5	0B
Start Address High	3?5	0C
Start Address Low	3?5	0D
Cursor Location High	3?5	0E
Cursor Location Low	3?5	0F
Vertical Retrace Start	3?5	10
Light Pen High	3?5	10
Vertical Retrace End	3?5	11
Light Pen Low	3?5	11
Vertical Display End	3?5	12
Offset	3?5	13
Underline Location	3?5	14
Start Vertical Blank	3?5	15
End Vertical Blank	3?5	16
Mode Control	3?5	17
Line Compare	3?5	18

? = B in Monochrome Modes and D in Color Modes

CRT Controller Address Register

The Address register is a pointer register located at hex 3B4 or hex 3D4. If an IBM Monochrome Display is attached to the adapter, address 3B4 is used. If a color display is attached to the adapter, address 3D4 is used. This register is loaded with a binary value that points to the CRT Controller data register where data is to be written. This value is referred to as "Index" in the table above.

CRT Controller Address Register Format



Bit 0-Bit 4

CRT Controller Address Bits—A binary value pointing to the CRT Controller register where data is to be written.

Horizontal Total Register

This is a write-only register pointed to when the value in the CRT Controller address register is hex 00. The processor output port address for this register is hex 3B5 or hex 3D5.

Horizontal Total Register Format



This register defines the total number of characters in the horizontal scan interval including the retrace time. The value directly controls the period of the horizontal retrace output signal. An internal horizontal character counter counts character clock inputs to the CRT Controller, and all horizontal and vertical timings are based upon the horizontal register. Comparators are used to compare register values with horizontal character values to provide horizontal timings.

Bit 0-Bit 7

Horizontal Total—The total number of characters less 2.

Horizontal Display Enable End Register

This is a write-only register pointed to when the value in the CRT Controller address register is hex 01. The processor output port address for this register is hex 3B5 or hex 3D5.

Horizontal Display Enable End Register Format

Bit 7 6 5 4 3 2 1 0



Horizontal Display Enable End

This register defines the length of the horizontal display enable signal. It determines the number of displayed character positions per horizontal line.

Bit 0-Bit 7 Horizontal display enable end —A value one less than the total number of displayed characters.

Start Horizontal Blanking Register

This is a write-only register pointed to when the value in the CRT Controller address register is hex 02. The processor output port address for this register is hex 3B5 or hex 3D5.

Start Horizontal Blanking Register Format

Bit 7 6 5 4 3 2 1 0



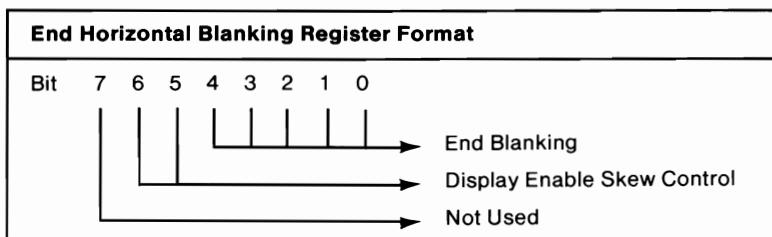
Start Vertical Blanking

This register determines when the horizontal blanking output signal becomes active. The row scan address and underline scan line decode outputs are multiplexed on the memory address outputs and cursor outputs respectively during the blanking interval. These outputs are latched external to the CRT Controller with the falling edge of the BLANK output signal. The row scan address and underline signals remain on the output signals for one character count beyond the end of the blanking signal.

Bit 0-Bit 7 Start Horizontal Blanking—The horizontal blanking signal becomes active when the horizontal character counter reaches this value.

End Horizontal Blanking Register

This is a write-only register pointed to when the value in the CRT Controller address register is hex 03. The processor output port address for this register is hex 3B5 or hex 3D5.



This register determines when the horizontal blanking output signal becomes inactive. The row scan address and underline scan line decode outputs are multiplexed on the memory address outputs and the cursor outputs respectively during the blanking interval. These outputs are latched external to the CRT Controller with the falling edge of the BLANK output signal. The row scan address and underline signals remain on the output signals for one character count beyond the end of the blanking signal.

Bit 0-Bit 4	End Horizontal Blanking—A value equal to the five least significant bits of the horizontal character counter value at which time the horizontal blanking signal becomes inactive (logical 0). To obtain a blanking signal of width W , the following algorithm is used: Value of Start Blanking Register + Width of Blanking signal in character clock units = 5-bit result to be programmed into the End Horizontal Blanking Register.
--------------------	---

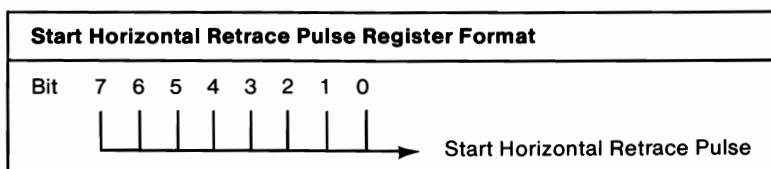
Bit 5-Bit 6

Display Enable Skew Control—These two bits determine the amount of display enable skew. Display enable skew control is required to provide sufficient time for the CRT Controller to access the display buffer to obtain a character and attribute code, access the character generator font, and then go through the Horizontal Pel Panning Register in the Attribute Controller. Each access requires the display enable signal to be skewed one character clock unit so that the video output is in synchronization with the horizontal and vertical retrace signals. The bit values and amount of skew are shown in the following table:

Bits	
6 5	
0 0	Zero character clock skew
0 1	One character clock skew
1 0	Two character clock skew
1 1	Three character clock skew

Start Horizontal Retrace Pulse Register

This is a write-only register pointed to when the value in the CRT Controller address register is hex 04. The processor output port address for this register is hex 3B5 or hex 3D5.



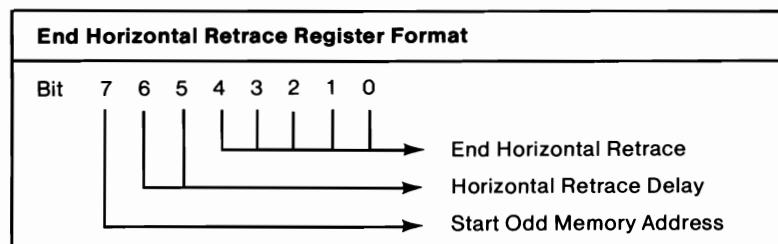
This register is used to center the screen horizontally, and to specify the character position at which the Horizontal Retrace Pulse becomes active.

Bit 0-Bit 7

Start Horizontal Retrace Pulse—The value programmed is a binary count of the character position number at which the signal becomes active.

End Horizontal Retrace Register

This is a write-only register pointed to when the value in the CRT Controller address register is hex 05. The processor output port address for this register is hex 3B5 or hex 3D5.



This register specifies the character position at which the Horizontal Retrace Pulse becomes inactive (logical 0).

Bit 0-Bit 4

End Horizontal Retrace—A value equal to the five least significant bits of the horizontal character counter value at which time the horizontal retrace signal becomes inactive (logical 0). To obtain a retrace signal of width W, the following algorithm is used: Value of Start Retrace Register + width of horizontal retrace signal in character clock units = 5-bit result to be programmed into the End Horizontal Retrace Register.

Bit 5-Bit 6

Horizontal Retrace Delay—These bits control the skew of the horizontal retrace signal. Binary 00 equals no Horizontal Retrace Delay. For some modes, it is necessary to provide a horizontal retrace signal that takes up the entire blanking interval. Some internal timings are generated by the falling edge of the horizontal retrace signal. To guarantee the signals are

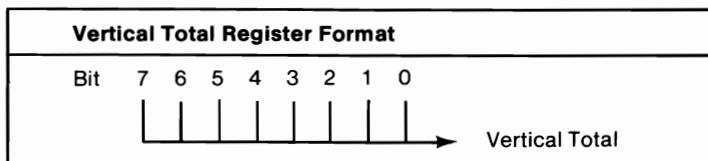
latched properly, the retrace signal is started before the end of the display enable signal, and then skewed several character clock times to provide the proper screen centering.

Bit 7

Start Odd/Even Memory Address—This bit controls whether the first CRT memory address output after a horizontal retrace begins with an even or an odd address. A logical 0 selects even addresses; a logical 1 selects odd addresses. This bit is used for horizontal pel panning applications. Generally, this bit should be set to a logical 0.

Vertical Total Register

This is a write-only register pointed to when the value in the CRT Controller address register is hex 06. The processor output port address for this register is hex 3B5 or 3D5.



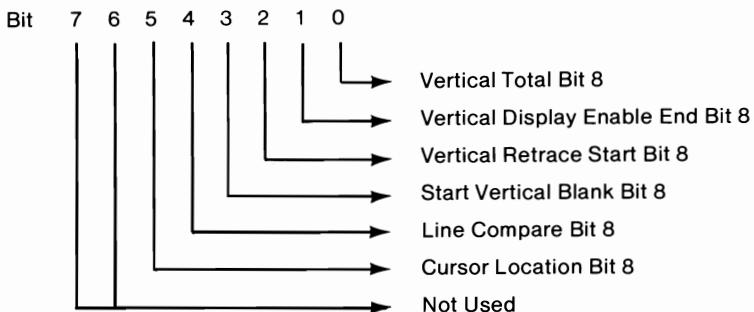
Bit 0-Bit 7

Vertical Total—This is the low-order eight bits of a nine-bit register. The binary value represents the number of horizontal raster scans on the CRT screen, including vertical retrace. The value in this register determines the period of the vertical retrace signal. Bit 8 of this register is contained in the CRT Controller Overflow Register hex 07 bit 0.

CRT Controller Overflow Register

This is a write-only register pointed to when the value in the CRT Controller Address Register is hex 07. The processor output port address for this register is hex 3B5 or hex 3D5.

CRTC Overflow Register Format



Bit 0 Vertical Total—Bit 8 of the Vertical Total register (index hex 06).

Bit 1 Vertical Display Enable End—Bit 8 of the Vertical Display Enable End register (index hex 12).

Bit 2 Vertical Retrace Start—Bit 8 of the Vertical Retrace Start register (index hex 10).

Bit 3 Start Vertical Blank—Bit 8 of the Start Vertical Blank register (index hex 15).

Bit 4 Line Compare—Bit 8 of the Line Compare register (index hex 18).

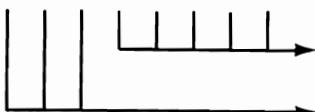
Bit 5 Cursor Location—Bit 8 of the Cursor Location register (index hex 0A).

Preset Row Scan Register

This is a write-only register pointed to when the value in the CRT Controller address register is hex 08. The processor output port address for this register is hex 3B5 or hex 3D5.

Preset Row Scan Register Format

Bit 7 6 5 4 3 2 1 0



Starting Row Scan Count after
a Vertical Retrace
Not Used

This register is used for pel scrolling.

Bit 0-Bit 4

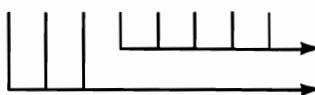
Preset Row Scan (Pel Scrolling)—This register specifies the starting row scan count after a vertical retrace. The row scan counter increments each horizontal retrace time until a maximum row scan occurs. At maximum row scan compare time the row scan is cleared (not preset).

Maximum Scan Line Register

This is a write-only register pointed to when the value in the CRT Controller address register is hex 09. The processor output port address for this register is hex 3B5 or hex 3D5.

Maximum Scan Line Register Format

Bit 7 6 5 4 3 2 1 0



Maximum Scan Line
Not Used

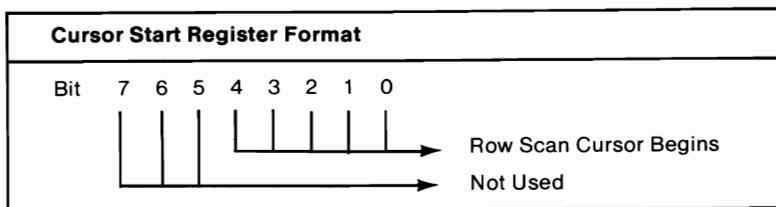
Bit 0-Bit 4

Maximum Scan Line—This register specifies the number of scan lines per character row. The number to be programmed is the maximum row scan number minus one.

Cursor Start Register

This is a write-only register pointed to when the value in the CRT Controller address register is hex 0A. The processor output port

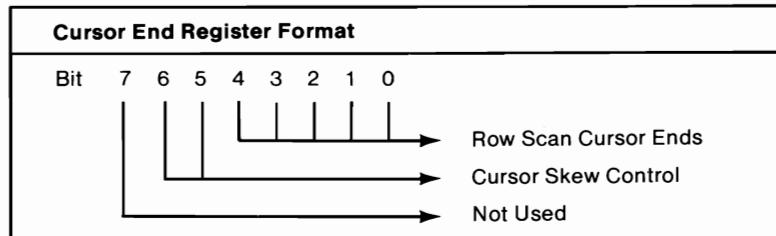
address for this register is hex 3B5 or hex 3D5.



Bit 0-Bit 4 Cursor Start—This register specifies the row scan of a character line where the cursor is to begin. The number programmed should be one less than the starting cursor row scan.

Cursor End Register

This is a write-only register pointed to when the value in the CRT Controller address register is hex 0B. The processor output port address for this register is hex 3B5 or hex 3D5.



Bit 0-Bit 4 Cursor End—These bits specify the row scan where the cursor is to end.

Bit 5-Bit 6 Cursor Skew—These bits control the skew of the cursor signal.

Bits**6 5**

-
- 0 0** Zero character clock skew
 - 0 1** One character clock skew
 - 1 0** Two character clock skew
 - 1 1** Three character clock skew

Start Address High Register

This is a read/write register pointed to when the value in the CRT Controller address register is hex 0C. The processor input/output port address for this register is hex 3B5 or hex 3D5.

Start Address High Register Format

Bit	7	6	5	4	3	2	1	0
-----	---	---	---	---	---	---	---	---



High Order Start Address

Bit 0-Bit 7

Start Address High—These are the high-order eight bits of the start address. The 16-bit value, from the high-order and low-order start address registers, is the first address after the vertical retrace on each screen refresh.

Start Address Low Register

This is a read/write register pointed to when the value in the CRT Controller address register is hex 0D. The processor input/output port address for this register is hex 3B5 or hex 3D5.

Start Address Low Register Format

Bit	7	6	5	4	3	2	1	0
-----	---	---	---	---	---	---	---	---

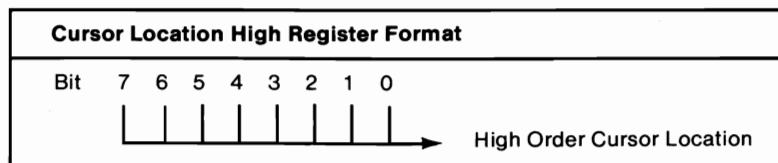


Low Order Start Address

Bit 0-Bit 7 Start Address Low—These are the low-order 8 bits of the start address.

Cursor Location High Register

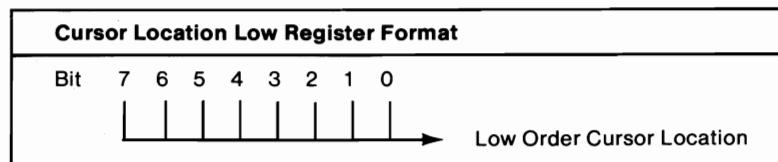
This is a read/write register pointed to when the value in the CRT Controller address register is hex 0E. The processor input/output port address for this register is hex 3B5 or hex 3D5.



Bit 0-Bit 7 Cursor Location High—These are the high-order 8 bits of the cursor location.

Cursor Location Low Register

This is a read/write register pointed to when the value in the CRT Controller address register is hex 0F. The processor input/output port address for this register is hex 3B5 or Hex 3D5.



Bit 0-Bit 7 Cursor Location Low— These are the low-order 8 bits of the cursor location.

Vertical Retrace Start Register

This is a write-only register pointed to when the value in the CRT Controller address register is hex 10. The processor output port address for this register is hex 3B5 or hex 3D5.

Vertical Retrace Start Register Format

Bit 7 6 5 4 3 2 1 0



Low Order Vertical Retrace Pulse

Bit 0-Bit 7

Vertical Retrace Start—This is the low-order 8 bits of the vertical retrace pulse start position programmed in horizontal scan lines. Bit 8 is in the overflow register location hex 07.

Light Pen High Register

This is a read-only register pointed to when the value in the CRT Controller address register is hex 10. The processor input port address for this register is hex 3B5 or hex 3D5.

Light Pen High Register Format

Bit 7 6 5 4 3 2 1 0



High Order Memory Address Counter

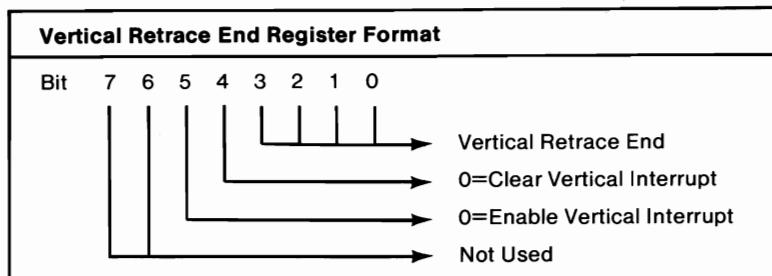
Bit 0-Bit 7

Light Pen High—This is the high order 8 bits of the memory address counter at the time the light pen was triggered.

Vertical Retrace End Register

This is a write-only register pointed to when the value in the CRT Controller address register is hex 11. The processor output port

address for this register is hex 3B5 or hex 3D5.



- Bit 0-Bit 3** Vertical Retrace End—These bits determine the horizontal scan count value when the vertical retrace output signal becomes inactive. The register is programmed in units of horizontal scan lines. To obtain a vertical retrace signal of width W, the following algorithm is used: Value of Start Vertical Retrace Register + width of vertical retrace signal in horizontal scan units = 4-bit result to be programmed into the End Horizontal Retrace Register.
- Bit 4** Clear Vertical Interrupt—A logical 0 will clear a vertical interrupt.
- Bit 5** Enable Vertical Interrupt—A logical 0 will enable vertical interrupt.

Light Pen Low Register

This is a read-only register pointed to when the value in the CRT Controller address register is hex 11. The processor input port address for this register is hex 3B5 or 3D5.

Light Pen Low Register Format

Bit 7 6 5 4 3 2 1 0



Low Order Memory Address Counter

Bit 0-Bit 7

Light Pen Low—This is the low-order 8 bits of the memory address counter at the time the light pen was triggered.

Vertical Display Enable End Register

This is a write-only register pointed to when the value in the CRT Controller address register is hex 12. The processor output port address for this register is hex 3B5 or hex 3D5.

Vertical Display Enable End Register Format

Bit 7 6 5 4 3 2 1 0



Low Order Vertical Display Enable End

Bit 0-Bit 7

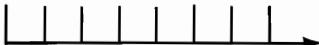
Vertical Display Enable End—These are the low-order 8 bits of the vertical display enable end position. This address specifies which scan line ends the active video area of the screen. Bit 8 is in the overflow register location hex 07.

Offset Register

This is a write-only register pointed to when the value in the CRT Controller address register is hex 13. The processor output port address for this register is hex 3B5 or hex 3D5.

Offset Register Format

Bit 7 6 5 4 3 2 1 0



Logical line width of the screen

Bit 0-Bit 7

Offset—This register specifies the logical line width of the screen. The starting memory address for the next character row is larger than the current character row by this amount. The Offset Register is programmed with a word address. Depending upon the method of clocking the CRT Controller, this word address is either a word or double word address.

Underline Location Register

This is a write-only register pointed to when the value in the CRT Controller address register is hex 14. The processor output port address for this register is hex 3B5 or hex 3D5.

Underline Location Register Format

Bit 7 6 5 4 3 2 1 0



Horizontal row scan where underline will occur

Not Used

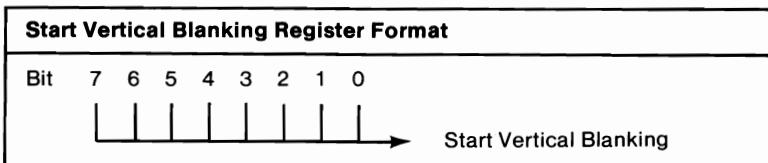
Bit 0-Bit 4

Underline Location—This register specifies the horizontal row scan on which underline will occur. The value programmed is one less than the scan line number desired.

Start Vertical Blanking Register

This is a write-only register pointed to when the value in the CRT Controller address register is hex 15. The processor output port

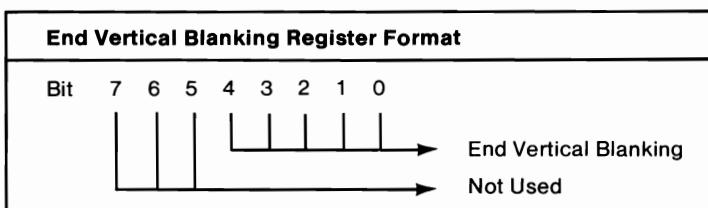
address for this register is hex 3B5 or hex 3D5.



Bit 0-Bit 7 Start Vertical Blank—These are the low 8 bits of the horizontal scan line count, at which the vertical blanking signal becomes active. Bit 8 bit is in the overflow register hex 07.

End Vertical Blanking Register

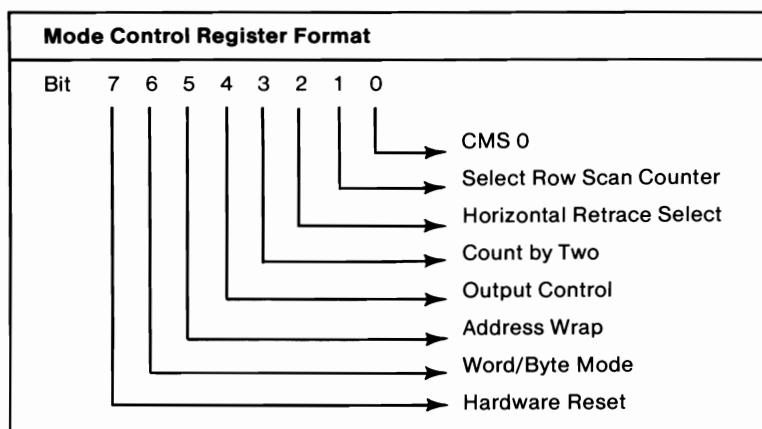
This is a write-only register pointed to when the value in the CRT Controller address register is hex 16. The processor output port address for this register is hex 3B5 or hex 3D5.



Bit 0-Bit 4 End Vertical Blank—This register specifies the horizontal scan count value when the vertical blank output signal becomes inactive. The register is programmed in units of horizontal scan lines. To obtain a vertical blank signal of width W , the following algorithm is used: Value of Start Vertical Blank Register + width of vertical blank signal in horizontal scan units = 5-bit result to be programmed into the End Vertical Blank Register.

Mode Control Register

This is a write-only register pointed to when the value in the CRT Controller address register is hex 17. The processor output port address for this register is hex 3B5 or hex 3D5.



Bit 0

Compatibility Mode Support— When this bit is a logical 0, the row scan address bit 0 is substituted for memory address bit 13 during active display time. A logical 1 enables memory address bit 13 to appear on the memory address output bit 13 signal of the CRT Controller. The CRT Controller used on the IBM Color/Graphics Monitor Adapter is the 6845. The 6845 has 128 horizontal scan line address capability. To obtain 640 by 200 graphics resolution, the CRTC was programmed for 100 horizontal scan lines with 2 row scan addresses per character row. Row scan address bit 0 became the most significant address bit to the display buffer. Successive scan lines of the display image were displaced in memory by 8K bytes. This bit allows compatibility with the 6845 and Color Graphics APA modes of operation.

- Bit 1** Select Row Scan Counter—A logical 0 selects row scan counter bit 1 on MA 14 output pin. A logical 1 selects MA 14 counter bit on MA 14 output pin.
- Bit 2** Horizontal Retrace Select—This bit selects Horizontal Retrace or Horizontal Retrace divided by 2 as the clock that controls the vertical timing counter. This bit can be used to effectively double the vertical resolution capability of the CRT Controller. The vertical counter has a maximum resolution of 512 scan lines due to the 9-bit wide Vertical Total Register. If the vertical counter is clocked with the horizontal retrace divided by 2 clock, then the vertical resolution is doubled to 1024 horizontal scan lines. A logical 0 selects HRTC and a logical 1 selects HRTC divided by 2.
- Bit 3** Count By Two— When this bit is set to 0, the memory address counter is clocked with the character clock input. A logical 1 clocks the memory address counter with the character clock input divided by 2. This bit is used to create either a byte or word refresh address for the display buffer.
- Bit 4** Output Control—A logical 0 enables the module output drivers. A logical 1 forces all outputs into high impedance state.
- Bit 5** Address Wrap—This bit selects Memory Address counter bit MA 13 or bit MA 15, and it appears on the MA 0 output pin in the word address mode. If you are not in the word address mode, MA 0 counter output appears on the MA 0 output pin. A logical 1 selects MA 15. In odd/even mode, bit MA 13 should be selected when the 64K memory is installed on the board. Bit MA 15 should be selected when greater than 64K memory is installed. This function is used to implement Color Graphics Monitor Adapter compatibility.

Bit 6

Word Mode or Byte Mode—When this bit is a logical 0, the Word Mode shifts all memory address counter bits down one bit, and the most significant bit of the counter appears on the least significant bit of the memory address outputs. See table below for address output details. A logical 1 selects the Byte Address mode.

Internal Memory Address Counter Wiring to the Output Multiplexer		
CRTC Out Pin	Byte Address Mode	Word Address Mode
MA 0/RFA 0	MA 0	MA 15 or MA 13
MA 1/RFA 1	MA 1	MA 0
MA 2/RFA 2	MA 2	MA 1
MA 3/RFA 3	MA 3	MA 2
*	*	*
*	*	*
*	*	*
MA 14/RS 3	MA 14	MA 13
MA 15/RS 4	MA 15	MA 14

Bit 7

Hardware Reset—A logical 0 forces horizontal and vertical retrace to clear. A logical 1 forces horizontal and vertical retrace to be enabled.

Line Compare Register

This is a write-only register pointed to when the value in the CRT Controller address register is hex 18. The processor output port address for this register is hex 3B5 or hex 3D5.

Line Compare Register Format								
Bit	7	6	5	4	3	2	1	0

Bit 0-Bit 7

Line Compare—This register is the low-order 8 bits of the compare target. When the vertical

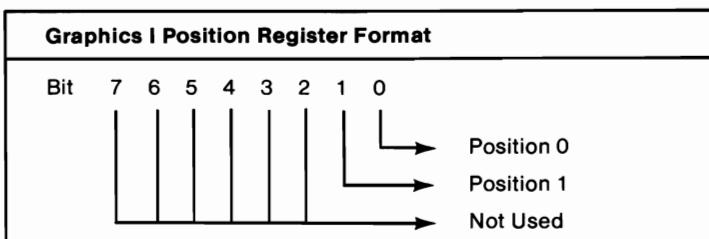
counter reaches this value, the internal start of the line counter is cleared. This allows an area of the screen to be immune to scrolling. Bit 8 of this register is in the overflow register hex 07.

Graphics Controller Registers

Name	Port	Index
Graphics 1 Position	3CC	-
Graphics 2 Position	3CA	-
Graphics 1 & 2 Address	3CE	-
Set/Reset	3CF	00
Enable Set/Reset	3CF	01
Color Compare	3CF	02
Data Rotate	3CF	03
Read Map Select	3CF	04
Mode Register	3CF	05
Miscellaneous	3CF	06
Color Don't Care	3CF	07
Bit Mask	3CF	08

Graphics 1 Position Register

This is a write-only register. The processor output port address for this register is hex 3CC.

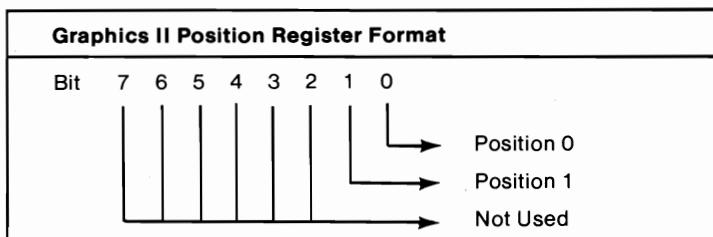


Bit 0-Bit 1

Position—These 2 bits are binary encoded hierarchy bits for the graphics chips. The position register controls which 2 bits of the processor data bus each chip responds to. Graphics 1 must be programmed with a position register value of 0 for this card.

Graphics 2 Position Register

This is a write-only register. The processor output port address for this register is hex 3CA.

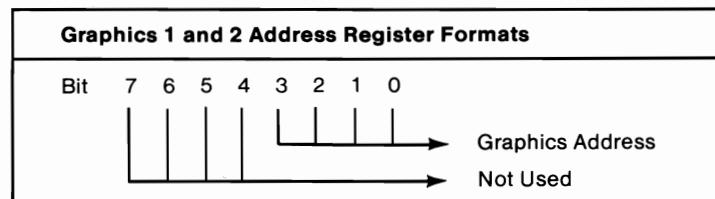


Bit 0-Bit 1

Position—These 2 bits are binary encoded hierarchy bits for the graphics chips. The position register controls which 2 bits of the processor data bus to which each chip responds. Graphics 2 must be programmed with a position register value of 1 for this card.

Graphics 1 and 2 Address Register

This is a write-only register and the processor output port address for this register is hex 3CE.

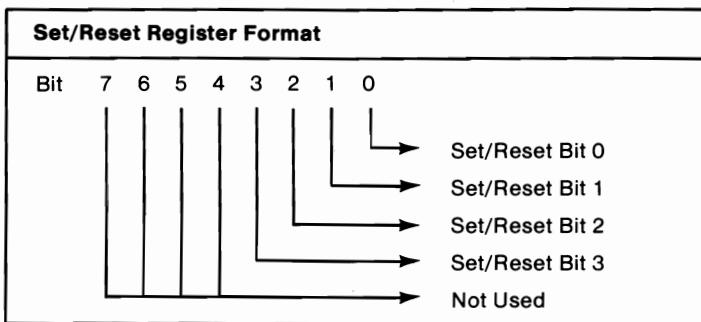


Bit 0-Bit 3

Graphics 1 and 2 Address Bits—This output loads the address register in both graphics chips simultaneously. This register points to the data register of the graphics chips.

Set/Reset Register

This is a write-only register pointed to by the value in the Graphics 1 and 2 address register. This value must be hex 00 before writing can take place. The processor output port address for this register is hex 3CF.



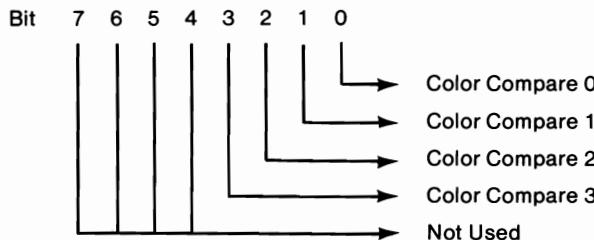
Bit 0-Bit 3

Set/Reset—These bits represent the value written to the respective memory planes when the processor does a memory write with write mode 0 selected and set/reset mode is enabled. Set/Reset can be enabled on a plane by plane basis with separate OUT commands to the Set/Reset register.

Enable Set/Reset Register

This is a write-only register and is pointed to by the value in the Graphics 1 and 2 address register. This value must be hex 01 before writing can take place. The processor output port for this register is hex 3CF.

Color Compare Register Format



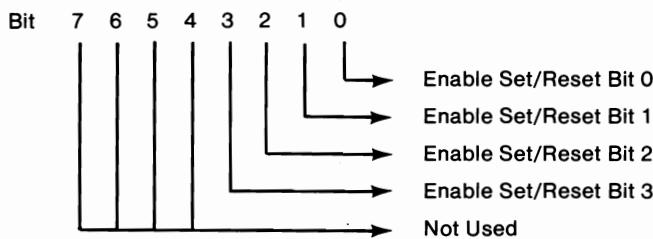
Bit 0-Bit 3

Enable Set/Reset—These bits enable the set/reset function. The respective memory plane is written with the value of the Set/Reset register provided the write mode is 0. When write mode is 0 and Set/Reset is not enabled on a plane, that plane is written with the value of the processor data.

Color Compare Register

This is a write-only register pointed to by the value in the Graphics 1 and 2 address register. This value must be hex 02 before writing can take place. The processor output port address for this register is hex 3CF.

Enable Set/Reset Register Format



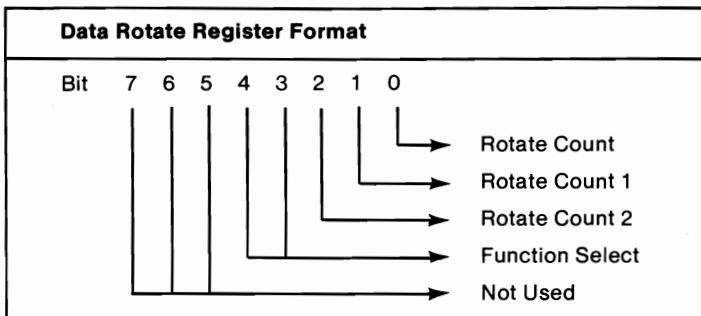
Bit 0-Bit 3

Color Compare—These bits represent a 4 bit color value to be compared. If the processor sets

read mode 1 on the graphics chips, and does a memory read, the data returned from the memory cycle will be a 1 in each bit position where the 4 bit planes equal the color compare register.

Data Rotate Register

This is a write-only register pointed to by the value in the Graphics 1 and 2 address register. This value must be hex 03 before writing can take place. The processor output port address for this register is hex 3CF.



Bit 0-Bit 2 Rotate Count—These bits represent a binary encoded value of the number of positions to rotate the processor data bus during processor memory writes. This operation is done when the write mode is 0. To write unrotated data the processor must select a count of 0.

Bit 3-Bit 4 Function Select—Data written to memory can operate logically with data already in the processor latches. The bit functions are defined in the following table.

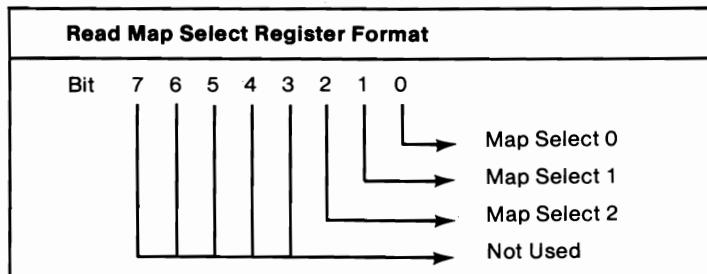
Bits
4 3

- 0 0** Data unmodified.
 - 0 1** Data AND'ed with latched data.
 - 1 0** Data OR'ed with latched data.
 - 1 1** Data XOR'ed with latched data.

Data may be any of the choices selected by the Write Mode Register except processor latches. If rotated data is selected, the rotate applies before the logical function.

Read Map Select Register

This is a write-only register pointed to by the value in the Graphics 1 and 2 address register. This value must be hex 04 before writing can take place. The processor output port address for this register is hex 3CF.

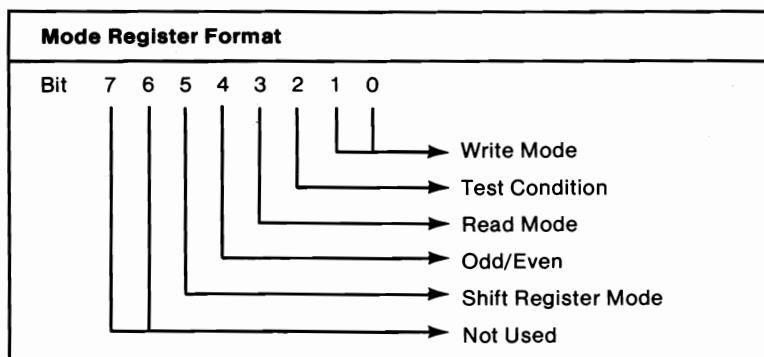


Bit 0-Bit 2 Map Select—These bits represent a binary encoded value of the memory plane number from which the processor reads data. This register has no effect on the color compare read mode described elsewhere in this section.

Mode Register

This is a write-only register pointed to by the value in the Graphics 1 and 2 address register. This value must be hex 05.

before writing can take place. The processor output port address for this register is 3CF.



Bit 0-Bit 1 Write Mode

Bits
1 0

- | | |
|------------|--|
| 0 0 | Each memory plane is written with the processor data rotated by the number of counts in the rotate register, unless Set/Reset is enabled for the plane. Planes for which Set/Reset is enabled are written with 8 bits of the value contained in the Set/Reset register for that plane. |
| 0 1 | Each memory plane is written with the contents of the processor latches. These latches are loaded by a processor read operation. |
| 1 0 | Memory plane n (0 through 3) is filled with 8 bits of the value of data bit n . |
| 1 1 | Not Valid |

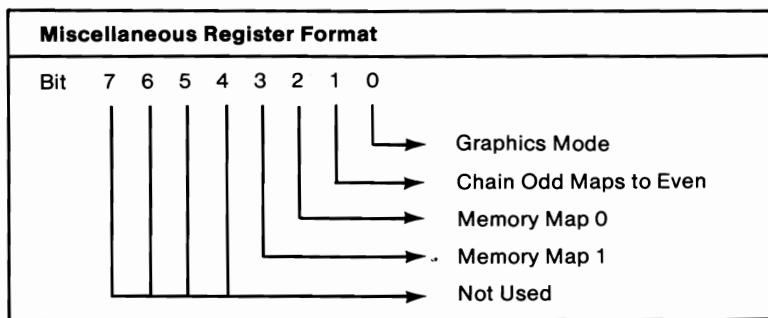
The logic function specified by the function select register also applies.

Bit 2 Test Condition—A logical 1 directs graphics controller outputs to be placed in high impedance state for testing.

- | | |
|--------------|---|
| Bit 3 | Read Mode—When this bit is a logical 0, the processor reads data from the memory plane selected by the read map select register. When this bit is a logical 1, the processor reads the results of the comparison of the 4 memory planes and the color compare register. |
| Bit 4 | Odd/Even—A logical 1 selects the odd/even addressing mode, which is useful for emulation of the Color Graphics Monitor Adapter compatible modes. Normally the value here follows the value of the Memory Mode Register bit 3 of the Sequencer. |
| Bit 5 | Shift Register—A logical 1 directs the shift registers on each graphics chip to format the serial data stream with even numbered bits on the even numbered maps and odd numbered bits on the odd maps. |

Miscellaneous Register

This is a write-only register pointed to by the value in the Graphics 1 and 2 address register. This value must be hex 06 before writing can take place. The processor output port for this register is hex 3CF.



Bit 0 Graphics Mode—This bit controls alpha-mode addressing. A logical 1 selects graphics mode. When set to graphics mode, the character generator address latches are disabled.

Bit 1 Chain Odd Maps To Even Maps—When set to 1, this bit directs the processor address bit 0 to be replaced by a higher order bit and odd/even maps to be selected with odd/even values of the processor A0 bit, respectively.

Bit 2-Bit 3 Memory Map—These bits control the mapping of the regenerative buffer into the processor address space.

Bits

3 2

0 0 Hex A000 for 128K bytes.

0 1 Hex A000 for 64K bytes.

1 0 Hex B000 for 32K bytes

1 1 Hex B800 for 32K bytes.

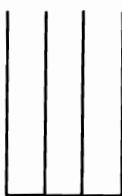
If the display adapter is mapped at address hex A000 for 128K bytes, no other adapter can be installed in the system.

Color Don't Care Register

This is a write-only register and is pointed to by the value in the Graphics 1 and 2 address register. This value must be hex 07 before writing can take place. The processor output port for this register is hex 3CF.

Color Don't Care Register Format

Bit 7 6 5 4 3 2 1 0



Color Plane 0=Don't Care
Color Plane 1=Don't Care
Color Plane 2=Don't Care
Color Plane 3=Don't Care
Not Used

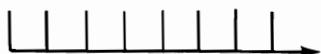
- Bit 0** Color Don't Care—Color plane 0=don't care when reading color compare when this bit is set to 1.
- Bit 1** Color Don't Care—Color plane 1=don't care when reading color compare when this bit is set to 1.
- Bit 2** Color Don't Care—Color plane 2=don't care when reading color compare when this bit is set to 1.
- Bit 3** Color Don't Care—Color plane 3=don't care when reading color compare when this bit is set to 1.

Bit Mask Register

This is a write-only register and is pointed to by the value in the Graphics 1 and 2 address register. This value must be hex 08 before writing can take place. The processor output port for this register is hex 3CF.

Bit Mask Register Format

Bit 7 6 5 4 3 2 1 0



0-Immune to change
1-Unimpeded Writes

Bit 0-Bit 7

Bit Mask—Any bit programmed to n causes the corresponding bit n in each bit plane to be immune to change provided that the location being written was the last location read by the processor. Bits programmed to a 1 allow unimpeded writes to the corresponding bits in the bit planes.

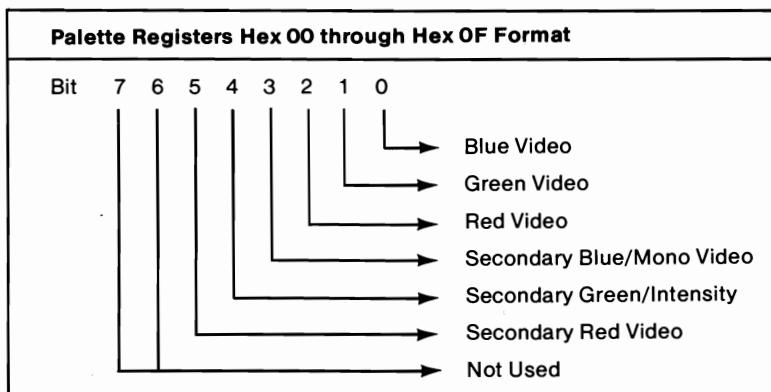
The bit mask applies to any data written by the processor (rotate, AND'ed, OR'ed, XOR'ed, DX, and S/R). To preserve bits using the bit mask, data must be latched internally by reading the location. When data is written to preserve the bits, the most current data in latches is written in those positions. The bit mask applies to all bit planes simultaneously.

Attribute Controller Registers

Name	Port	Index
Address Register	3C0	-
Palette Registers	3C0	00-0F
Mode Control Register	3C0	10
Overscan Color Register	3C0	11
Color Plane Enable Register	3C0	12
Horizontal Pel Panning Register	3C0	13

Attribute Address Register

This is a write-only register. The processor output port is hex 3C0.



Bit 0-Bit 4

Attribute Address Bits—The Address Register is a pointer register located at hex 3C0. This register is loaded with a binary value that points to the attribute data register where data is to be written. The Attribute Controller does not have an address bit input to control selection of the address and data registers. An internal address flip-flop controls selection of either the address or data registers. To initialize the flip-flop, an IOR instruction is issued to the Attribute Controller at address 3BA or 3DA. This clears the flip-flop, and selects the Address Register. After the Address Register has been loaded, the

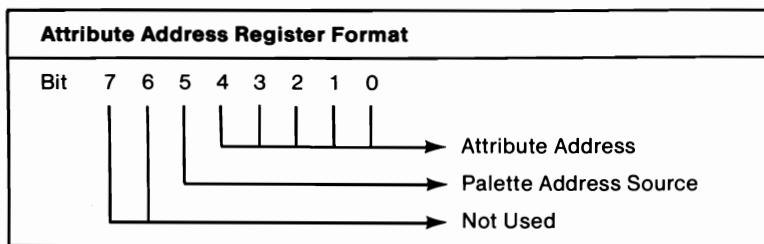
next OUT instruction loads the data register. The flip-flop toggles each time an OUT is issued to the Attribute Controller.

Bit 5

Palette Address Source—When loading the color palette registers, bit 5 must be cleared to 0. To enable the memory data to access the color palette, bit 5 must be set to 1.

Palette Register Hex 00 through Hex 0F

This is a write-only register. The processor output port is hex 3C0.

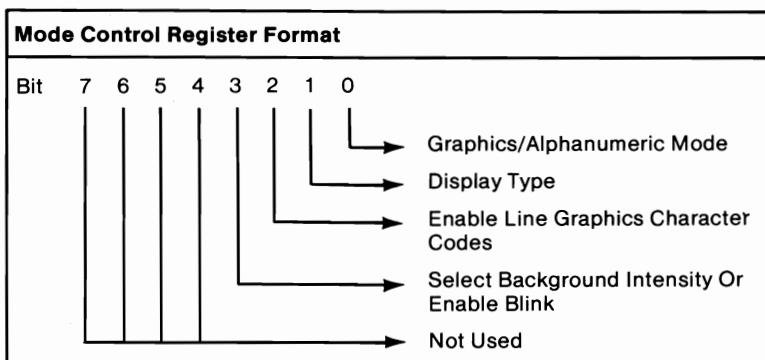


Bit 0-Bit 5

Palette—These 6-bit registers allow a dynamic mapping between the text attribute or graphic color input value and the display color in the CRT screen. A logical 1 selects the appropriate color. A logical 0 de-selects. The color palette register should be modified only during the vertical retrace interval to avoid glitches in the displayed image. Note that some color monitors do not have an intensity input and only a maximum of eight colors are available. Monitors with four color inputs display sixteen colors, and monitors with six color inputs display 64 colors.

Mode Control Register

This is a write-only register pointed to by the value in the Attribute address register. This value must be hex 10 before writing can take place. The processor output port address for this register is hex 3C0.



Bit 0 Graphics/Alphanumeric Mode—A logical 0 selects alphanumeric mode. A logical 1 selects graphics mode.

Bit 1 Monochrome Display/Color Display—A logical 0 selects color display attributes. A logical 1 selects IBM Monochrome Display attributes.

Bit 2 Enable Line Graphics Character Codes—When this bit is set to 0, the ninth dot will be the same as the background. A logical 1 enables the special line graphics character codes for the IBM Monochrome Display adapter. This bit when enabled forces the ninth dot of a line graphic character to be identical to the eighth dot of the character. The line graphics character codes for the Monochrome Display Adapter are Hex C0 through Hex DF.

For character fonts that do not utilize the line graphics character codes in the range of Hex C0

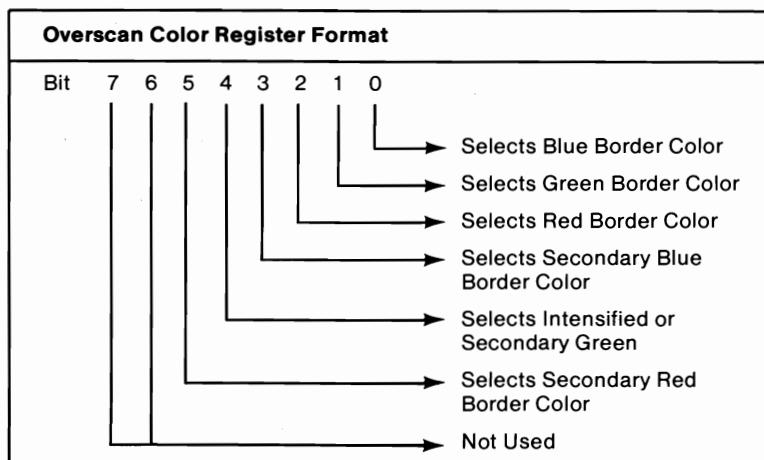
through Hex DF, bit 2 of this register should be a logical 0. Otherwise unwanted video information will be displayed on the CRT screen.

Bit 3

Enable Blink/Select Background Intensity—A logical 0 selects the background intensity of the attribute input. This mode was available on the Monochrome and Color Graphics adapters. A logical 1 enables the blink attribute in alphanumeric modes. This bit must also be set to 1 for blinking graphics modes.

Overscan Color Register

This is a write-only register pointed to by the value in the Attribute address register. This value must be hex 11 before writing can take place. The processor output port address for this register is hex 3C0.

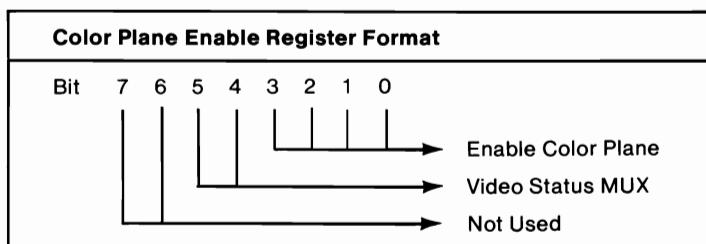


Bit 0-Bit 5

Overscan Color—This 6-bit register determines the overscan (border) color displayed on the CRT screen. For monochrome display this register should be set to a value of 0. A logical 1 selects the appropriate color.

Color Plane Enable Register

This is a write-only register pointed to by the value in the Attribute address register. This value must be hex 12 before writing can take place. The processor output port address for this register is 3C0.



Bit 0-Bit 3	Enable Color Plane—Writing a logical 1 in any of bits 0 through 3 enables the respective display memory color plane.
--------------------	--

Bit 4-Bit 5 Video Status MUX—Selects two of the six color outputs to be available on the status port. The following table illustrates the combinations available and the color output wiring.

COLOR PLANE ENABLE REGISTER		INPUT STATUS REGISTER ONE	
Bit 5	Bit 4	Bit 5	Bit 4
0	0	Red	Blue
0	1	Secondary Blue	Green
1	0	Secondary Red	Secondary Green
1	1	Not Used	Not Used

Horizontal Pel Panning Register

This is a write-only register pointed to by the value in the Attribute address register. This value must be hex 12 before writing can take place. The processor output port address for this register is hex 3C0.

Horizontal Pel Panning Register Format

Bit 7 6 5 4 3 2 1 0



Bit 0-Bit 3

Horizontal Pel Panning—This 4 bit register selects the number of picture elements (pels) to shift the video data horizontally to the left. Pel panning is available in both A/N and APA modes. In Monochrome A/N mode, the image can be shifted a maximum of 9 pels. In all other A/N and APA modes, the image can be shifted a maximum of 8 pels. The sequence for shifting the image is given below:

9 pels/character : 8, 0, 1, 2, 3, 4, 5, 6, 7
(Monochrome A/N mode only)

8 pels/character : 0, 1, 2, 3, 4, 5, 6, 7 (All other Modes)

Programming Considerations

Programming the Registers

Each of the LSI devices has an address register and a number of data registers. The address register serves as a pointer to the other registers on the LSI device. It is a write-only register that is loaded by the processor by executing an 'OUT' instruction to its I/O address with the index of the selected data register.

The data registers on each LSI device are accessed through a common I/O address. They are distinguished by the pointer (index) in the address register. To write to a data register, the address register is loaded with the index of the appropriate data register, then the selected data register is loaded by executing an 'OUT' instruction to the common I/O address.

The external registers that are not part of an LSI device and the Graphics I and II registers are not accessed through an address register; they are written to directly.

The following tables define the values that are loaded into the registers by BIOS to support the different modes of operation supported by this adapter.

Register			Mode of Operation																	
Name	Port	Index	0	1	2	3	4	5	6	7	D	E	F	10	F*	10*	0*	1*	2*	3*
Miscellaneous	3C2	-	23	23	23	23	23	23	23	23	A6	23	23	A2	A7	A2	A7	A7	A7	A7
Feature Cntrl	3?A	-	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
Input Stat 0	3C2	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-
Input Stat 1	3?2	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-

? = B in monochrome modes ? = D in color modes

*Values for these modes when the IBM Enhanced Color Display is attached

#Values for these modes when greater than 64K Graphics Memory is installed

External Registers

Register			Mode of Operation																	
Name	Port	Index	0	1	2	3	4	5	6	7	D	E	F	10	F*	10*	0*	1*	2*	3*
Seq Address	3C4	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-
Reset	3C5	00	03	03	03	03	03	03	03	03	03	03	03	03	03	03	03	03	03	03
Clock Mode	3C5	01	0B	0B	01	01	0B	0B	01	00	0B	01	05	05	01	01	01	0B	0B	01
Map Mask	3C5	02	03	03	03	03	03	03	01	03	0F	0F	0F	0F	0F	0F	03	03	03	03
Char Gen Sel	3C5	03	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
Memory Mode	3C5	04	03	03	03	03	02	02	06	03	06	06	00	00	06	06	03	03	03	03

*Values for these modes when the IBM Enhanced Color Display is attached

#Values for these modes when greater than 64K Graphics Memory is installed

Sequencer Registers

Register			Mode of Operation																	
Name	Port	Index	0	1	2	3	4	5	6	7	D	E	F	10	F*	10*	0*	1*	2*	3*
Address Reg	3?4	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-
Horiz Total	3?5	00	37	37	70	70	37	37	70	60	37	70	60	5B	60	5B	2D	2D	5B	5B
Hz Disp End	3?5	01	27	27	4F	4F	27	27	4F	4F	27	4F	4F	4F	4F	4F	27	27	4F	4F
Strt Hz Blk	3?5	02	2D	2D	5C	5C	2D	2D	59	56	2D	56	56	53	56	53	2B	2B	53	53
End Hz Blk	3?5	03	37	37	2F	2F	37	37	2D	3A	37	2D	1A	17	3A	37	2D	2D	37	37
Strt Hz Retr	3?5	04	31	31	5F	5F	30	30	5E	51	30	5E	50	50	50	52	2B	2B	51	51
End Hz Retr	3?5	05	15	15	07	07	14	14	06	60	14	06	E0	BA	60	00	6D	6D	5B	5B
Vert Total	3?5	06	04	04	04	04	04	04	04	70	04	04	70	6C	70	6C	6C	6C	6C	6C
Overflow	3?5	07	11	11	11	11	11	11	11	1F	11	11	1F	1F	1F	1F	1F	1F	1F	1F
Preset Row SC	3?5	08	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
Max Scan Line	3?5	09	07	07	07	07	01	01	01	0D	00	00	00	00	00	00	0D	0D	0D	0D
Cursor Start	3?5	0A	06	06	06	06	00	00	00	0B	00	00	00	00	00	00	0B	0B	0B	0B
Cursor End	3?5	0B	07	07	07	07	00	00	00	0C	00	00	00	00	00	00	0C	0C	0C	0C
Strt Addr Hi	3?5	0C	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-
Strt Addr Lo	3?5	0D	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-

? = B in monochrome modes ? = D in color modes

*Values for these modes when the IBM Enhanced Color Display is attached

:Values for these modes when greater than 64K Graphics Memory is installed

CRT Controller Registers (1 of 2)

Register			Mode of Operation																	
Name	Port	Index	0	1	2	3	4	5	6	7	D	E	F	10	F*	10*	0*	1*	2*	3*
Cursor LC Hi	3?5	0E	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-
Cursor LC Low	3?5	0F	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-
Vrt Retr Strt	3?5	10	E1	E1	E1	E1	E1	E1	E0	5E	E1	E0	5E	5E	5E	5E	5E	5E	5E	5E
Light Pen Hi	3?5	10	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-
Vert Retr End	3?5	11	24	24	24	24	24	24	23	2E	24	23	2E	2B	2E	2B	2B	2B	2B	2B
Light Pen Low	3?5	11	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-
Vrt Disp End	3?5	12	C7	C7	C7	C7	C7	C7	C7	5D	C7	C7	5D	5D	5D	5D	5D	5D	5D	5D
Offset	3?5	13	14	14	28	28	14	14	28	28	14	28	14	14	28	28	14	14	28	28
Underline Loc	3?5	14	08	08	08	08	00	00	00	0D	00	00	0D	0F	0D	0F	0F	0F	0F	0F
Strt Vert Blk	3?5	15	E0	E0	E0	E0	E0	E0	DF	5E	E0	DF	5E	5F	5E	5F	5E	5E	5E	5E
End Vert Blk	3?5	16	F0	F0	F0	F0	F0	F0	EF	6E	F0	EF	6E	0A	6E	0A	0A	0A	0A	0A
Mode Control	3?5	17	A3	A3	A3	A3	A2	A2	C2	A3	E3	E3	8B	8B	E3	E3	A3	A3	A3	A3
Line Compare	3?5	18	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF
? = B in monochrome modes ? = D in color modes																				
*Values for these modes when the IBM Enhanced Color Display is attached																				
:Values for these modes when greater than 64K Graphics Memory is installed																				

CRT Controller Registers (2 of 2)

Register			Mode of Operation																	
Name	Port	Index	0	1	2	3	4	5	6	7	D	E	F	10	F8	10%	0*	1*	2*	3*
Grphx I Pos	3CC	-	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	
Grphx II Pos	3CA	-	01	01	01	01	01	01	01	01	01	01	01	01	01	01	01	01	01	
Grphx I II AD	3CE	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	
Set Reset	3CF	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	
Enable S/R	3CF	01	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	
Color Compare	3CF	02	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	
Data Rotate	3CF	03	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	
Read Map Sel	3CF	04	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	
Mode Register	3CF	05	10	10	10	10	30	30	00	10	00	00	10	10	00	00	10	10	10	
Miscellaneous	3CF	06	0E	0E	0E	0E	0F	0F	0D	0A	05	05	07	07	05	05	0E	0E	0E	
Color No Care	3CF	07	00	00	00	00	00	00	00	00	0F	0F	0F	0F	0F	0F	00	00	00	
Bit Mask	3CF	08	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	
*Values for these modes when the IBM Enhanced Color Display is attached																				
:Values for these modes when greater than 64K Graphics Memory is installed																				

Graphics SI Registers

Register			Mode of Operation																		
Name	Port	Index	0	1	2	3	4	5	6	7	D	E	F	10	F#	10#	0*	1*	2*	3*	
Address	3?A	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	
Palette	3C0	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	
Palette	3C0	01	01	01	01	01	13	13	17	08	01	01	08	01	08	01	01	01	01	01	
Palette	3C0	02	02	02	02	02	15	15	17	08	02	02	00	00	00	02	02	02	02	02	
Palette	3C0	03	03	03	03	03	17	17	17	08	03	03	00	00	00	03	03	03	03	03	
Palette	3C0	04	04	04	04	04	02	02	17	08	04	04	18	04	18	04	04	04	04	04	
Palette	3C0	05	05	05	05	05	05	04	04	17	08	05	05	18	07	18	05	05	05	05	
Palette	3C0	06	06	06	06	06	06	06	06	17	08	06	06	00	00	00	06	14	14	14	
Palette	3C0	07	07	07	07	07	07	07	07	17	08	07	07	00	00	00	07	07	07	07	
Palette	3C0	08	10	10	10	10	10	10	17	10	10	10	00	00	00	38	38	38	38	38	
Palette	3C0	09	11	11	11	11	11	11	11	17	18	11	11	08	01	08	39	39	39	39	
Palette	3C0	0A	12	12	12	12	12	12	12	17	18	12	12	00	00	00	3A	3A	3A	3A	
Palette	3C0	0B	13	13	13	13	13	13	13	17	18	13	13	00	00	00	3B	3B	3B	3B	

? = B in monochrome modes ? = D in color modes

*Values for these modes when the IBM Enhanced Color Display is attached

:Values for these modes when greater than 64K Graphics Memory is installed

Attribute Registers (1 of 2)

Register			Mode of Operation																	
Name	Port	Index	0	1	2	3	4	5	6	7	D	E	F	10	F*	10*	0*	1*	2*	3*
Palette	3C0	0C	14	14	14	14	14	14	17	18	14	14	00	04	00	3C	3C	3C	3C	3C
Palette	3C0	0D	15	15	15	15	15	15	17	18	15	15	18	07	18	3D	3D	3D	3D	3D
Palette	3C0	0E	16	16	16	16	16	16	17	18	16	16	00	00	00	3E	3E	3E	3E	3E
Palette	3C0	0F	17	17	17	17	17	17	18	17	17	00	00	00	3F	3F	3F	3F	3F	
Mode Control	3C0	10	08	08	08	08	01	01	01	0E	01	01	0B	0B	0B	01	08	08	08	08
Overscan	3C0	11	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
Color Plane	3C0	12	0F	0F	0F	0F	03	03	01	0F	0F	0F	05	05	05	0F	0F	0F	0F	0F
Hz Panning	3C0	13	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00

*Values for these modes when the IBM Enhanced Color Display is attached

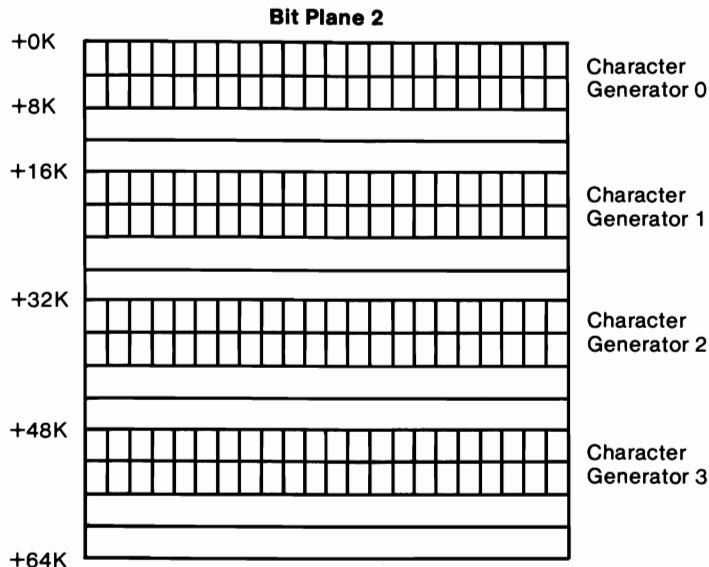
:Values for these modes when greater than 64K Graphics Memory is installed

Attribute Registers (2 of 2)

RAM Loadable Character Generator

The character generator on the adapter is RAM loadable and can support characters up to 32 scan lines high. Two character generators are stored within the BIOS and one is automatically loaded into the RAM by the BIOS when an alphanumeric mode is selected. The Character Map Select Register can be programmed to define the function of bit 3 of the attribute byte to be a character generator switch. This allows the user to select between any two character sets residing in bit plane 2. This effectively gives the user access to 512 characters instead of 256. character tables may be loaded off line. The adapter must have 128K bytes of storage to support this function. Up to four tables can be loaded with 256K of graphics memory installed.

The structure of the character tables is described in the following figure. The character generator is in bit plane 2 and must be protected using the map mask function.



The following figure illustrates the structure of each character pattern. If the CRT controller is programmed to generate n row

scans, then n bytes must be filled in for each character in the character generator. The example assumes eight row scans per character.

Address	Byte Image	Data
CC * 32 + 0		18H
1		3EH
2		66H
3		66H
4		7EH
5		66H
6		66H
7		66H

CC = Value of the character code. For example, 41H in the case of an ASCII "A".

Creating a 512 Character Set

This section describes how to create a 512 character set on the IBM Color Display. Note that only 256 characters can be printed on the printer. This is a special application which the Enhanced Graphics Adapter will support. The 9 by 14 characters will be displayed when attribute bit 3 is a logical 0, and the IBM Color/Graphics Monitor Adapter 8 by 8 characters will be displayed when the attribute bit 3 is a logical 1. This example is for demonstrative purposes only. The assembly language routine for creating 512 characters is given below. Debug 2.0 was used for this example. The starting assembly address is 100 and the character string is stored in location 200. This function requires 128K or more of graphics memory.

```

a100
mov ax,1102          ;load 8x8 character font in character
mov bl,02              ;generator number 2
int 10

mov ax,1103          ;select 512 character operation
mov bl,08              ;if attribute bit 3=1 use 8x8 font
int 10                ;if attribute bit 3=0 use 9x14 font

mov ax,1000          ;set color plane enable to 7H to disable
mov bx,0712          ;attribute bit 3 in the color palette
int 10                ;lookup table

mov ax,1301          ;write char. string with attribute bit 3=1
mov bx,000F          ;cx = character string length
mov cx,003A          ;write character on line 22 of display
mov dx,1600          ;pointer to character string location
mov bp,0200
push cs
pop es
int 10

mov ax ,1301          ;write char. string with attribute bit 3=0
mov bx,0007          ;cx = character string length
mov cx,003A          ;write character on line 23 of display
mov dx,1700          ;pointer to character string location
mov bp,0200
push cs
pop es
int 10
int 3

a200 db              ;"This character string is used to show 512
                      ;characters"

```

Creating an 80 by 43 Alphanumeric Mode

The following examples show how to create 80 column by 43 row, both alphanumeric and graphics, images on the IBM Monochrome Display. The BIOS Interface supports an 80 column by n row display by using the character generator load routine call. The print screen routine must be revectored to

handle the additional character rows on the screen. The assembly language required for both an alphanumeric and a graphics screen is shown below.

```
mov al,7          ;Monochrome alphanumeric mode
int 10           ;video interrupt call
mov ax,1112       ;character generator BIOS routine
mov bl,0          ;load 8 by 8 double dot character font
int 10           ;video interrupt call
mov ax,1200       ;alternate screen routine
move bl,20        ;select alternate print screen routine
int 10           ;video interrupt call
int 3

mov ax,f          ;Monochrome graphic mode
int 10           ;video interrupt call
mov ax,1123       ;character generator BIOS routine
mov bl,0          ;load 8 by 8 double dot character font
mov dl,2B         ;43 character rows
int 10           ;video interrupt call
mov ax,1200       ;alternate screen routine
mov bl,20         ;alternate print screen routine
int 10           ;video interrupt call
int 3
```

Vertical Interrupt Feature

The Enhanced Graphics Adapter can be programmed to create an interrupt each time the vertical display refresh time has ended.

An interrupt handler routine must be written by the application to take advantage of this feature. The CRT Vertical interrupt is on IRQ2. The CPU can poll the Enhanced Graphics Adapter Input Status Register 0 (bit 7) to determine whether the CRTC caused the interrupt to occur.

The Vertical Retrace End Register (11H) in the CRT controller contains two bits which are used to control the interrupt circuitry. The remaining bits must be output as per the value in the mode table.

Bit 5 Enable Vertical Interrupt—A logical 0 will enable vertical interrupt.

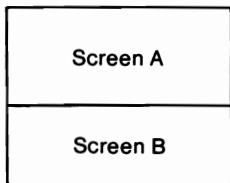
Bit 4 Clear Vertical Interrupt—A logical 0 will clear a vertical interrupt.

The sequence of events which occur in an interrupt handler are outlined below.

1. Clear IRQ latch and enable driver
2. Enable IRQ latch
3. Wait for vertical interrupt
4. Poll Interrupt Status Register 0 to determine if CRTC has caused the interrupt
5. If CRTC interrupt, then clear IRQ latch; if not, then branch to next interrupt handler.
6. Enable IRQ latch
7. Update Enhanced Graphics Adapter during vertical blanking interval
8. Wait for next vertical interrupt

Creating a Split Screen

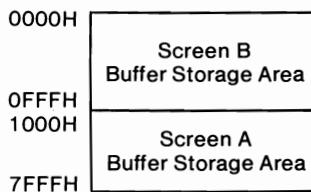
The Enhanced Graphics Adapter hardware supports an alphanumeric mode dual screen display. The top portion of the screen is designated as screen A, and the bottom portion of the screen is designated as screen B as per the following figure.



Dual Screen Definition

The following figure shows the screen mapping for a system containing a 32K byte alphanumeric storage buffer. Note that the Enhanced Graphics Adapter has a 32K byte storage buffer in alphanumeric mode. Information displayed on screen A is

defined by the start address high and low registers (0CH and 0DH) of the CRTC. Information displayed on screen B always begins at address 0000H.



Screen Mapping Within the Display Buffer Address Space

The Line Compare Register (18H) of the CRT Controller is utilized to perform the split screen function. The CRTC has an internal horizontal scan counter, and logic which compares the horizontal scan counter value to the Line Compare Register value and clears the memory address generator when a compare occurs. The linear address generator then sequentially addresses the display buffer starting at location zero, and each subsequent row address is determined by the 16 bit addition of the start of line latch and the offset register.

Screen B can be smoothly scrolled onto the CRT screen by updating the Line compare in synchronization with the vertical retrace signal. The information on screen B is immune from scrolling operations which utilize the Start Address High and Low registers to scroll through the Screen A address map.

Compatibility Issues

The CRT Controller on the IBM Enhanced Graphics Adapter is a custom design, and is different than the 6845 controller used on the IBM Monochrome Monitor Adapter and the IBM Color/Graphics Monitor Adapter. It should be noted that several CRTC register addresses differ between the adapters. The following figure illustrates the registers which do not map directly across the two controllers.

Register	6485 Function	EGA CRTC Function
02H	Start Horiz. Retrace	Start Horiz. Blanking
03H	End Horiz. Retrace	End Horiz. Blanking
04H	Vertical Total	Start Horiz. Retrace
05H	Vertical Total Adjust	End Horiz. Retrace
06H	Vertical Displayed	Vertical Total
07H	Vertical Sync Position	Overflow
08H	Interlace Mode and Skew	Preset Row Scan

Existing applications which utilize the BIOS interface will generally be compatible with the Enhanced Graphics Adapter.

Horizontal screen centering was required on the IBM Color/Graphics Monitor Adapter in order to center the screen when generating composite video. This was done through the Horizontal Sync Position Register. Since the Enhanced Graphics Adapter does not support a composite video monitor, programs which do screen centering may cause loss of the screen image if centering is attempted.

The Enhanced Graphics Adapter offers a wider variety of displayable monochrome character attributes than the IBM Monochrome Display Adapter. Some attribute values may display differently between the two Adapters. The values listed in the table below, in any combinations with the blink and intensity attributes, will display identically.

Background R G B	Foreground R G B	Function
0 0 0	0 0 0	Non-Display
0 0 0	0 0 1	Underline
0 0 0	1 1 1	White Character/Black Background
1 1 1	0 0 0	Reverse Video

Software which explicitly addresses 3D8 (Mode Select Register) or 3D9 (Color Select Register) on the Color Graphics Monitor Adapter may produce different results on the Enhanced Graphics Adapter. For example, blinking which is disabled by writing to 3D8 on the Color Graphics Adapter will not be disabled on the Enhanced Graphics Adapter.

Interface

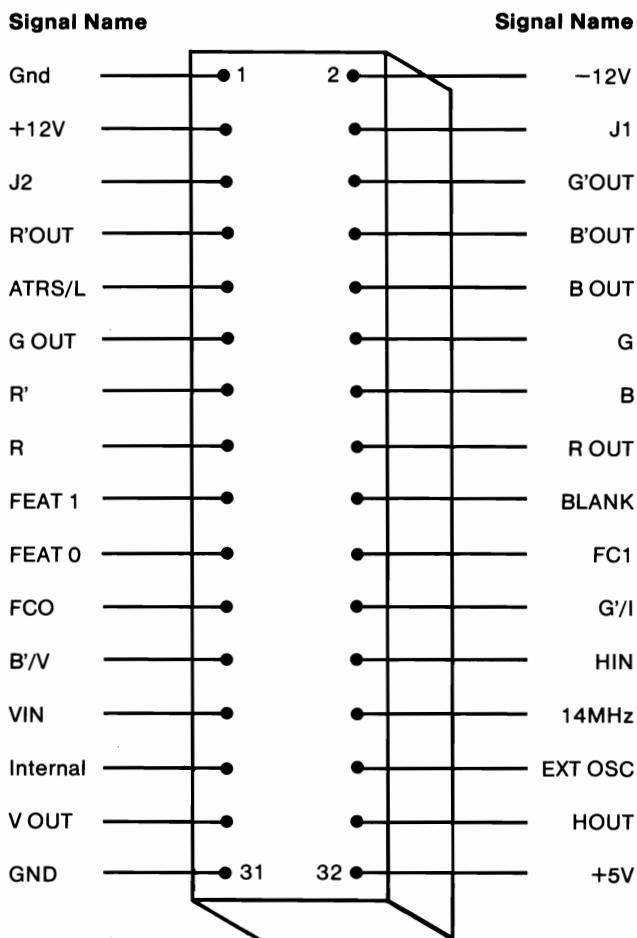
Feature Connector

The following is a description of the Enhanced Graphics Adapter feature connector. Note that signals coming from the Enhanced Graphics Adapter are labeled "inputs" and the signals coming to the Enhanced Graphics Adapter through the feature connector are labeled "outputs".

Signal	Description
J2	This pin is connected to auxiliary jack 2 on the rear panel of the adapter.
R'OUT	Secondary red output
ATRS/L	Attribute shift load. This signal controls the serialization of the video information. The shift register parallel loads at the dot clock leading edge when this signal is low.
G OUT	Primary green output
R'	Secondary red input
R	Primary red input
FC1	This signal is input from bit 1 (Feature Control Bit 1) of the Feature Control Register.
FC0	This signal is input from bit 0 (Feature Control Bit 0) of the Feature control Register.
FEAT 0	This signal is output to bit 5 (Feature Code 0) of Input Status Register 0.
B'/V	Secondary blue input/Monochrome video
VIN	Vertical retrace input

Internal	This signal is output to bit 4 (Disable Internal Video Drivers) of the Miscellaneous Output Register.
V OUT	Vertical retrace output
J1	This pin is connected to auxiliary jack 1 on the rear panel of the adapter.
G'OUT	Secondary green output
B'OUT	Secondary blue output
B OUT	Blue output
G	Green input
B	Blue input
R OUT	Red output
BLANK	This is a composite horizontal and vertical blanking signal from the CRTC.
FEAT 1	This signal is output to bit 6 (Feature Code 1) of Input Status Register 0.
G'/I	Secondary green/Intensity input
HIN	Horizontal retrace input from the CRTC
14MHZ	14 MHz signal from the system board
EXT OSC	External dot clock output
HOUT	Horizontal retrace output

The following figure shows the layout and pin numbering of the feature connector.

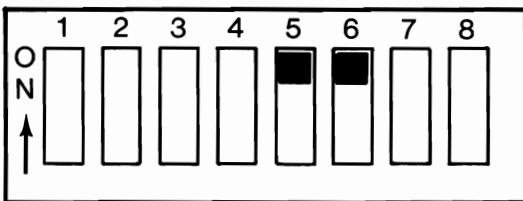


Feature Connector Diagram

Specifications

System Board Switches

The following figure shows the proper system board DIP switch settings for the IBM Enhanced Graphics Adapter when used with the Personal Computer and the Personal Computer XT. The switch block locations are illustrated in the Technical Reference Manual "System Board Component Diagram". The Personal Computer has two DIP switch blocks; the switch settings shown pertain to DIP Switch Block 1. The Personal Computer XT has one DIP switch block.

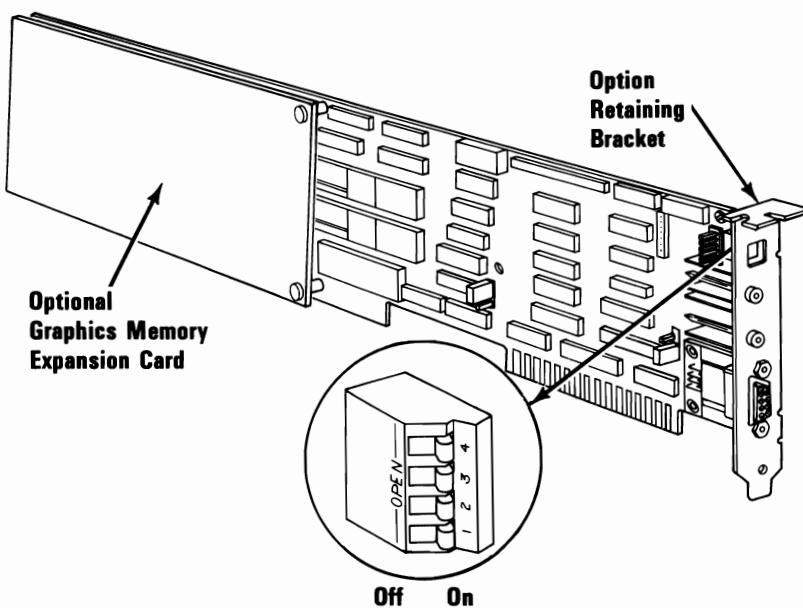


Switch Block (1)

Note: The DIP switches must be set as shown whenever the IBM Enhanced Graphics Adapter is installed, regardless of display type. This is true even when a second display adapter is installed in the system.

Configuration Switches

The following diagram shows the location and orientation of the configuration switches on the Enhanced Graphics Adapter.



Configuration Switch Settings

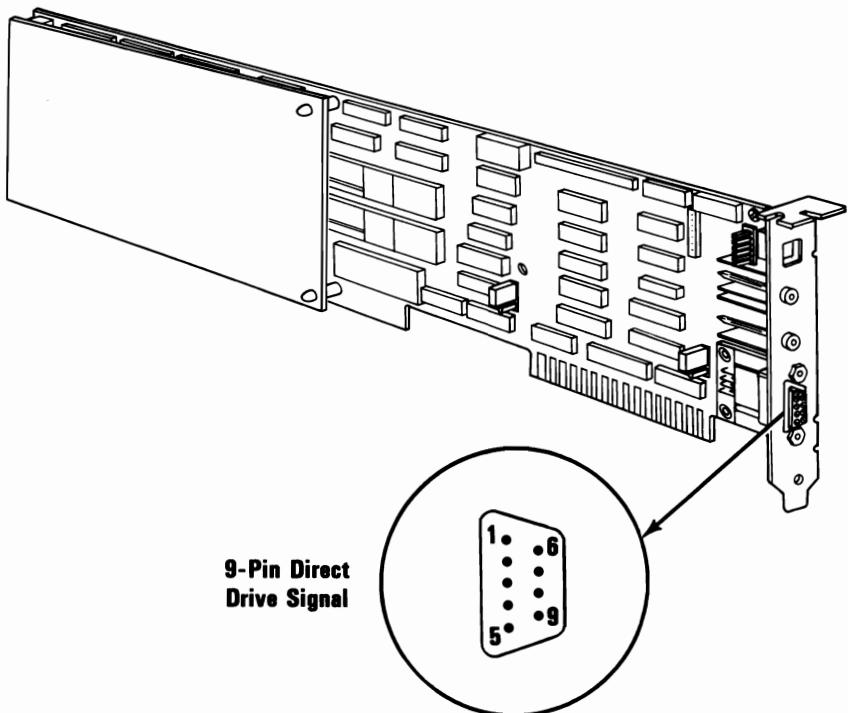
The configuration switches on the Enhanced Graphics Adapter determine the type of display support the adapter provides, as follows:

Switch Settings for Enhanced Graphics Adapter as Primary Display Adapter					Configuration		
SW1	SW2	SW3	SW4		Enhanced Adapter	Monochrome Adapter	Color/Graphics Adapter
On	Off	Off	On	Color Display 40x25	Secondary	—	
Off	Off	Off	On	Color Display 80x25	Secondary	—	
On	On	On	Off	Enhanced Display Emulation Mode	Secondary	—	
Off	On	On	Off	Enhanced Display Hi Res Mode	Secondary	—	
On	Off	On	Off	Monochrome	—	Secondary 40x25	
Off	Off	On	Off	Monochrome	—	Secondary 80x25	

**Switch Settings for Enhanced Graphics Adapter
as Secondary Display Adapter**

SW1	SW2	SW3	SW4	Configuration		
				Enhanced Adapter	Monochrome Adapter	Color/Graphics Adapter
On	On	On	On	Color Display 40x25	Primary	—
Off	On	On	On	Color Display 80x25	Primary	—
On	Off	On	On	Enhanced Display Emulation Mode	Primary	—
Off	Off	On	On	Enhanced Display Hi Res Mode	Primary	—
On	On	Off	On	Monochrome	—	Primary 40x25
Off	On	Off	On	Monochrome	—	Primary 80x25

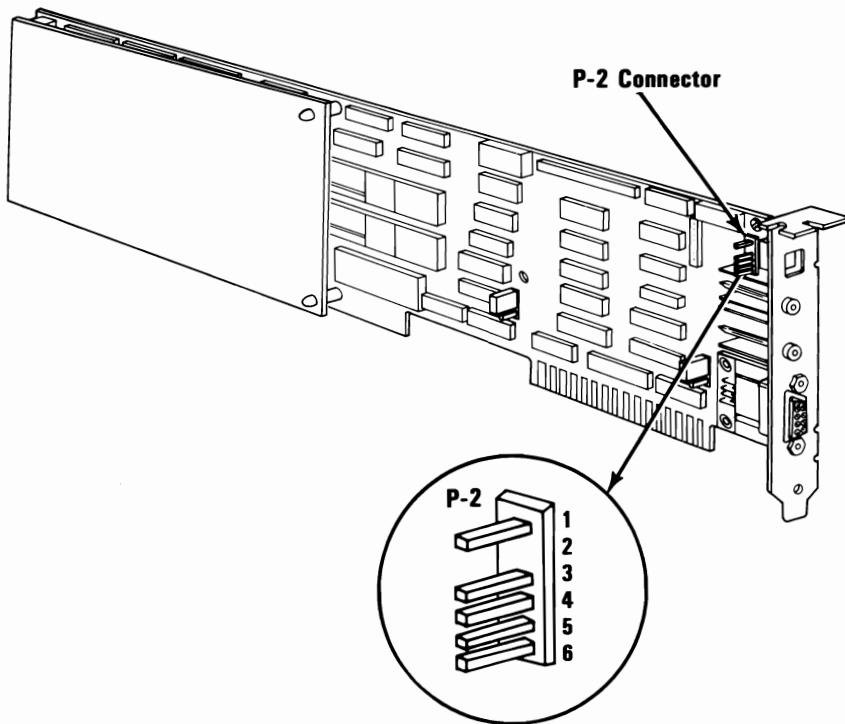
Direct Drive Connector



9-Pin Direct Drive Signal

	Signal Name - Description	Pin	
Direct Drive Display	Ground	1	Enhanced Graphics Adapter
	Secondary Red	2	
	Primary Red	3	
	Primary Green	4	
	Primary Blue	5	
	Secondary Green/Intensity	6	
	Secondary Blue/Mono Video	7	
	Horizontal Retrace	8	
	Vertical Retrace	9	

Light Pen Interface



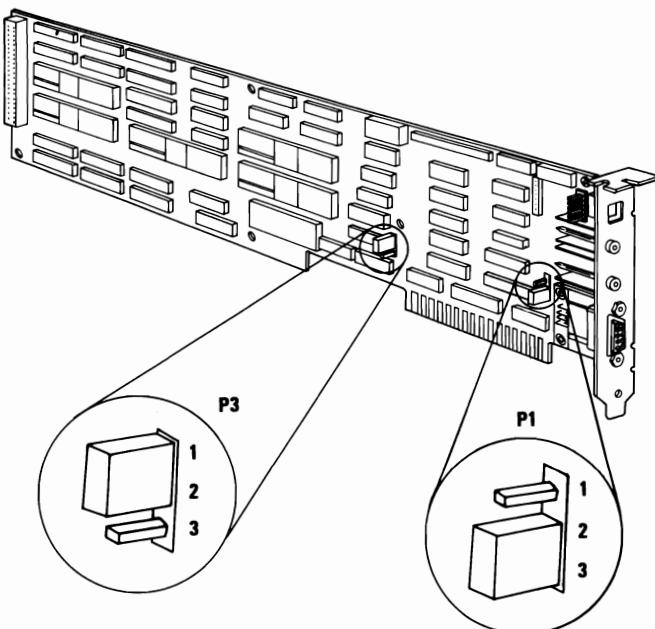
	P-2 Connector	Pin	
Light Pen Attachment	+Light Pen Input	1	Enhanced Graphics Adapter
	Not used	2	
	+Light Pen Switch	3	
	Ground	4	
	+5 Volts	5	
	12 Volts	6	

Jumper Descriptions

Located on the adapter are two jumpers designated P1 and P3. Jumper P1 changes the function of pin 2 on the direct drive interface. When placed on pins 2 and 3, jumper P1 selects ground as the function of direct drive interface, pin 2. This selection is for displays that support five color outputs, such as the IBM Color Display. When P1 is placed on pins 1 and 2, red prime output is placed on pin 2 of the direct drive interface connector. This supports the IBM Enhanced Color Display, which utilizes six color outputs on the direct drive interface.

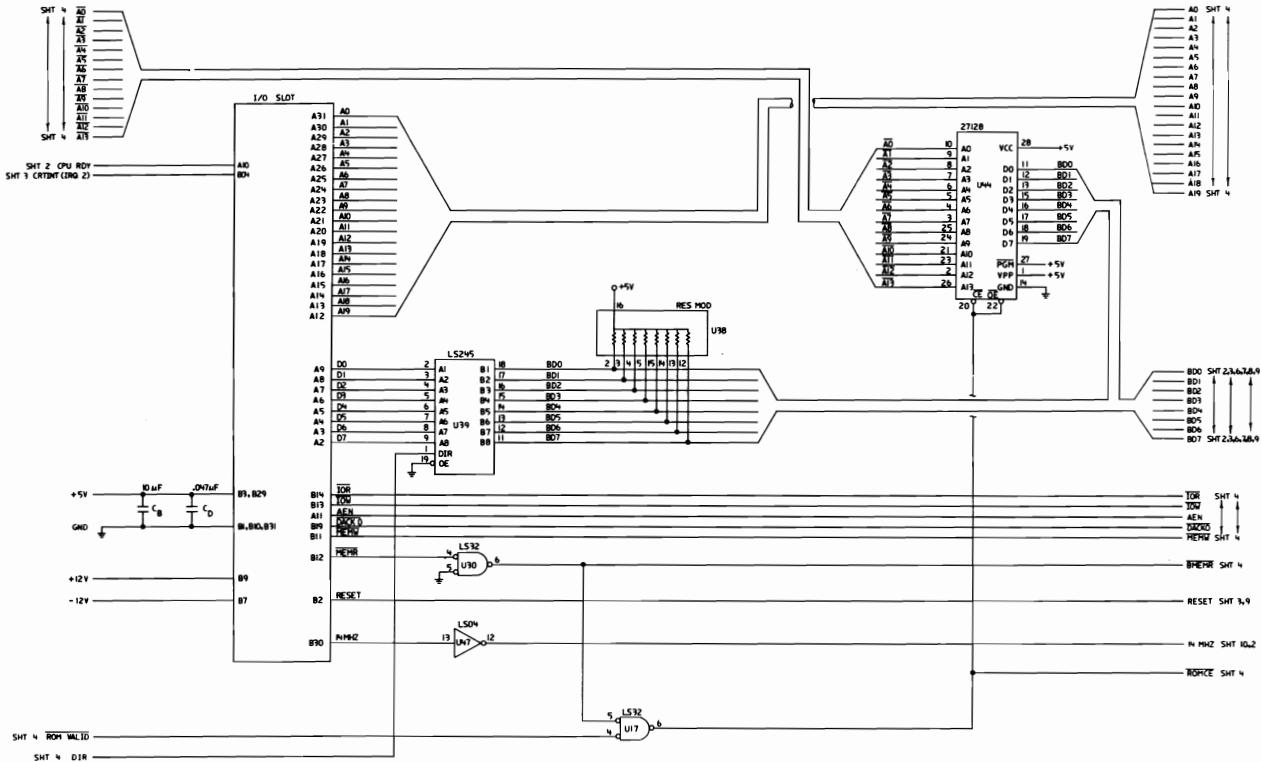
Jumper P3 changes the I/O address port of the Enhanced Graphics Adapter within the system. In its normal position, (pins 1 and 2), all Enhanced Graphics Adapter addresses are in the range 3XX. Moving jumper P3 to pins 2 and 3 changes the addresses to 2XX. Operation of the adapter in the 2XX mode is not supported in BIOS.

The following figure shows the location of the jumpers and numbering of the connectors.

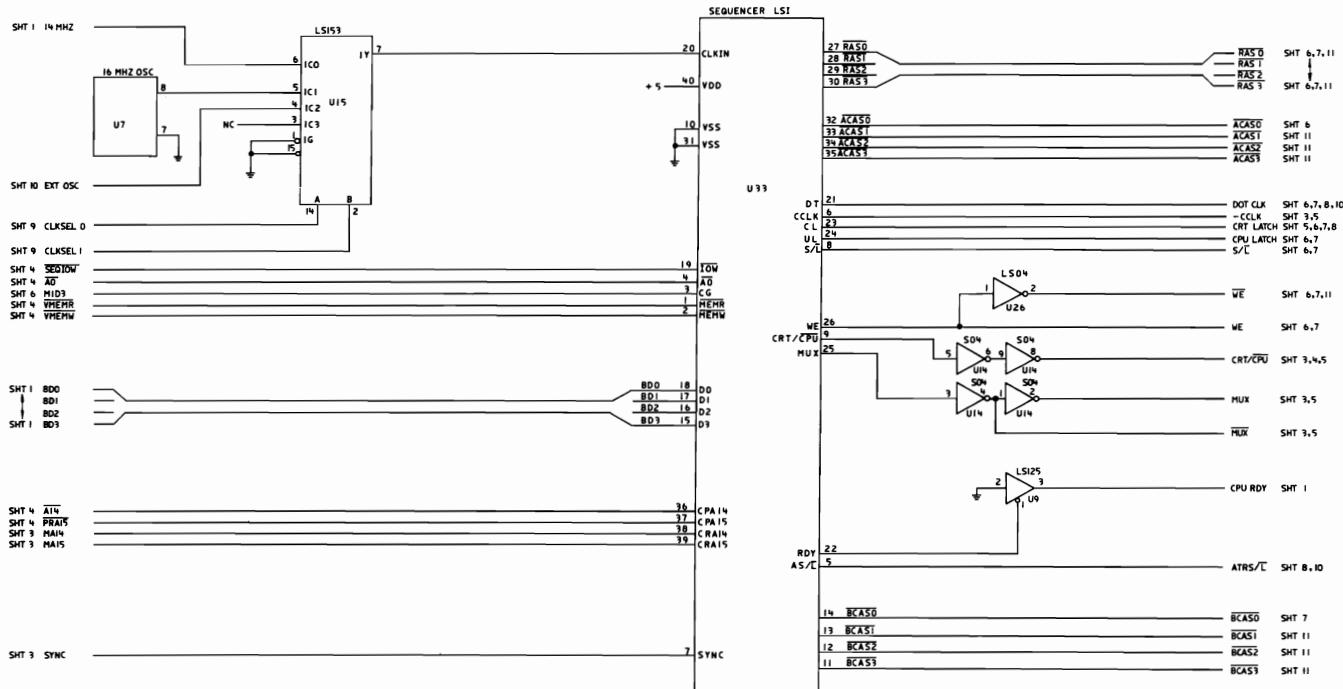


Logic Diagrams

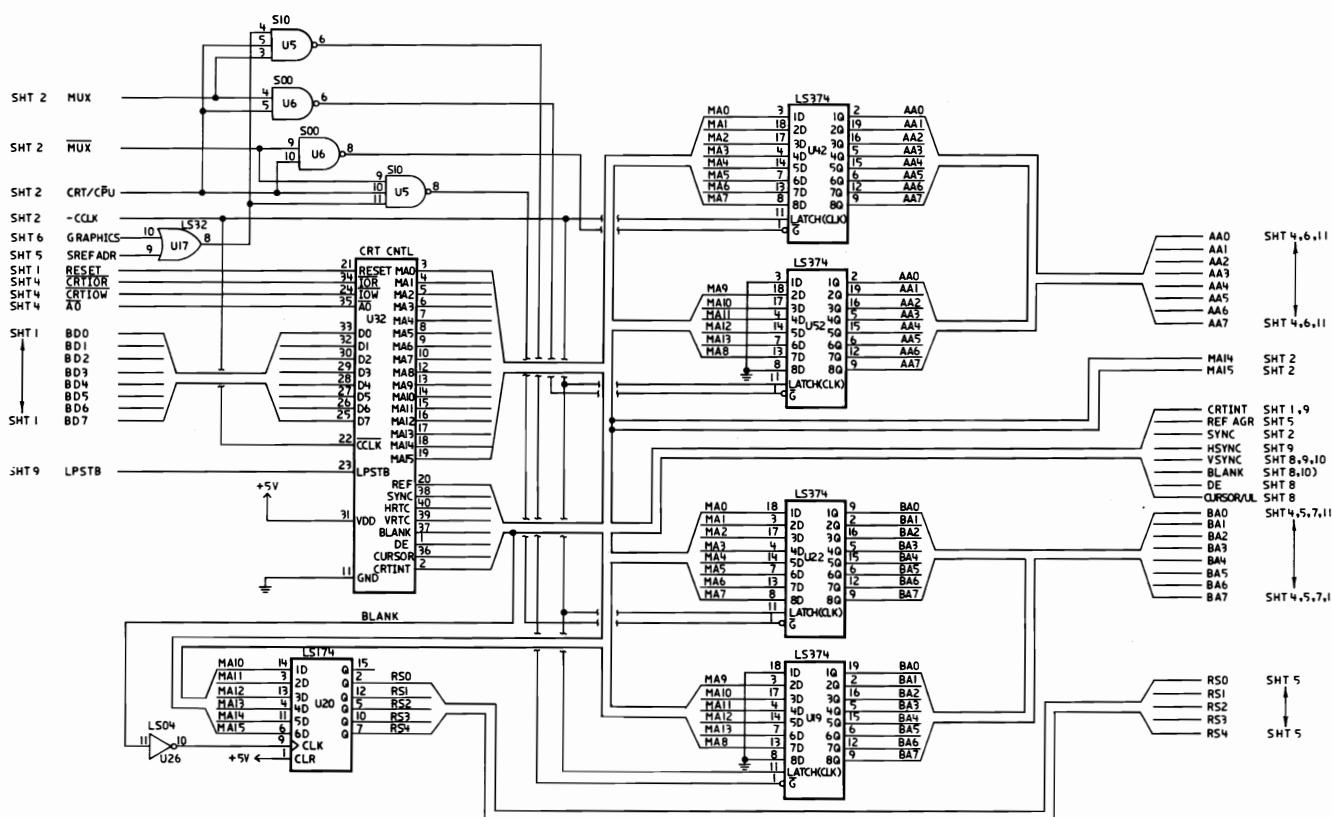
ENHANCED GRAPHICS ADAPTER



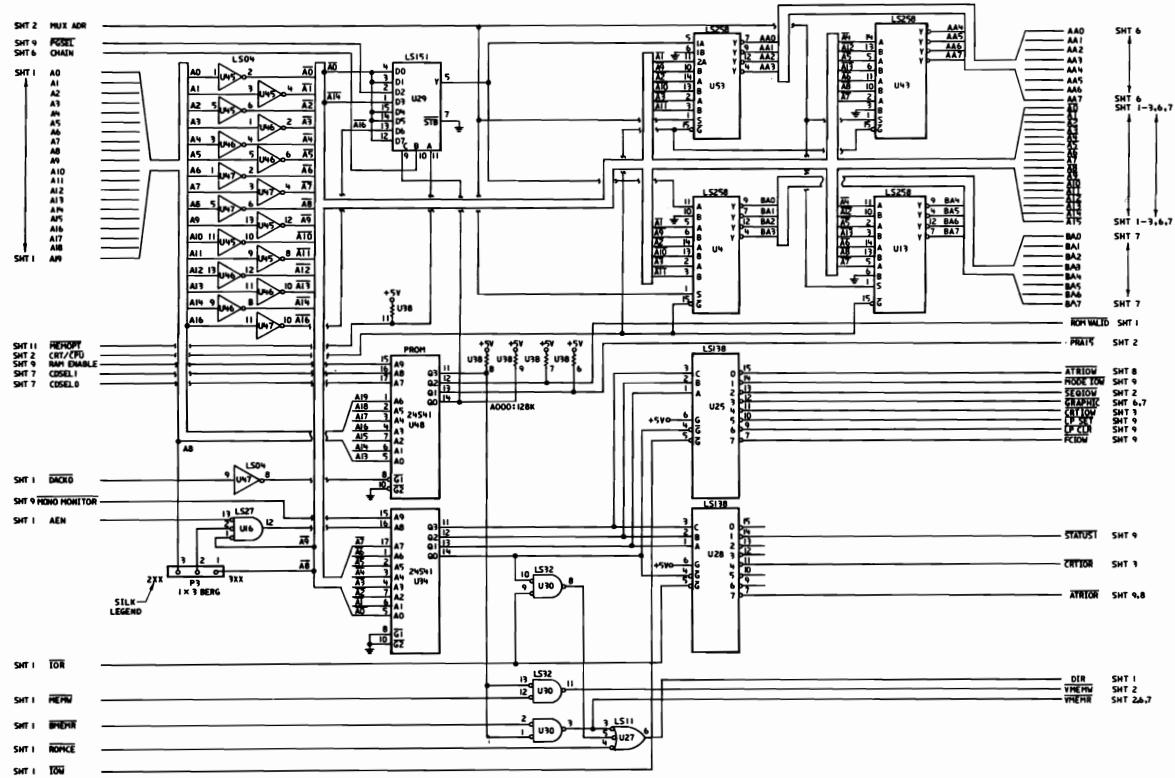
Enhanced Graphics Adapter Sheet 1 of 11



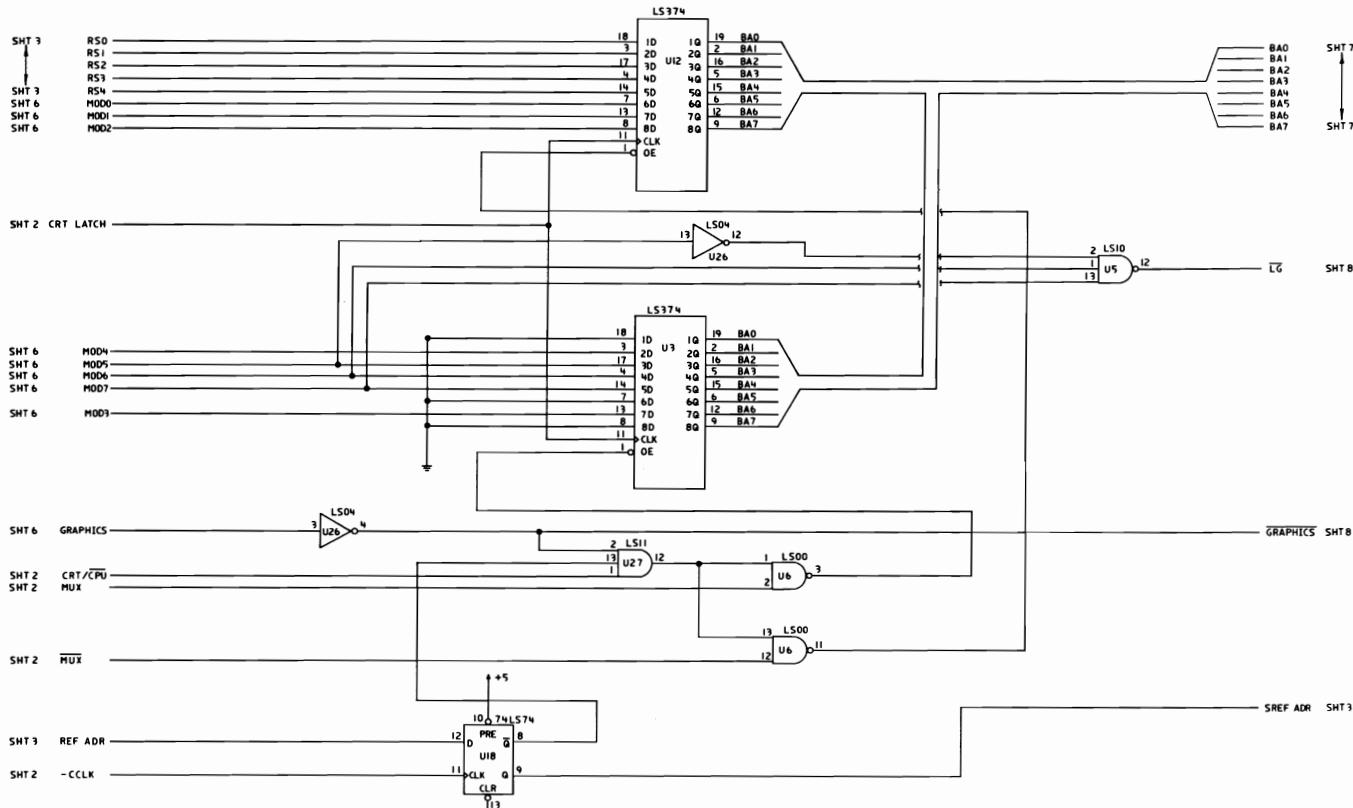
)



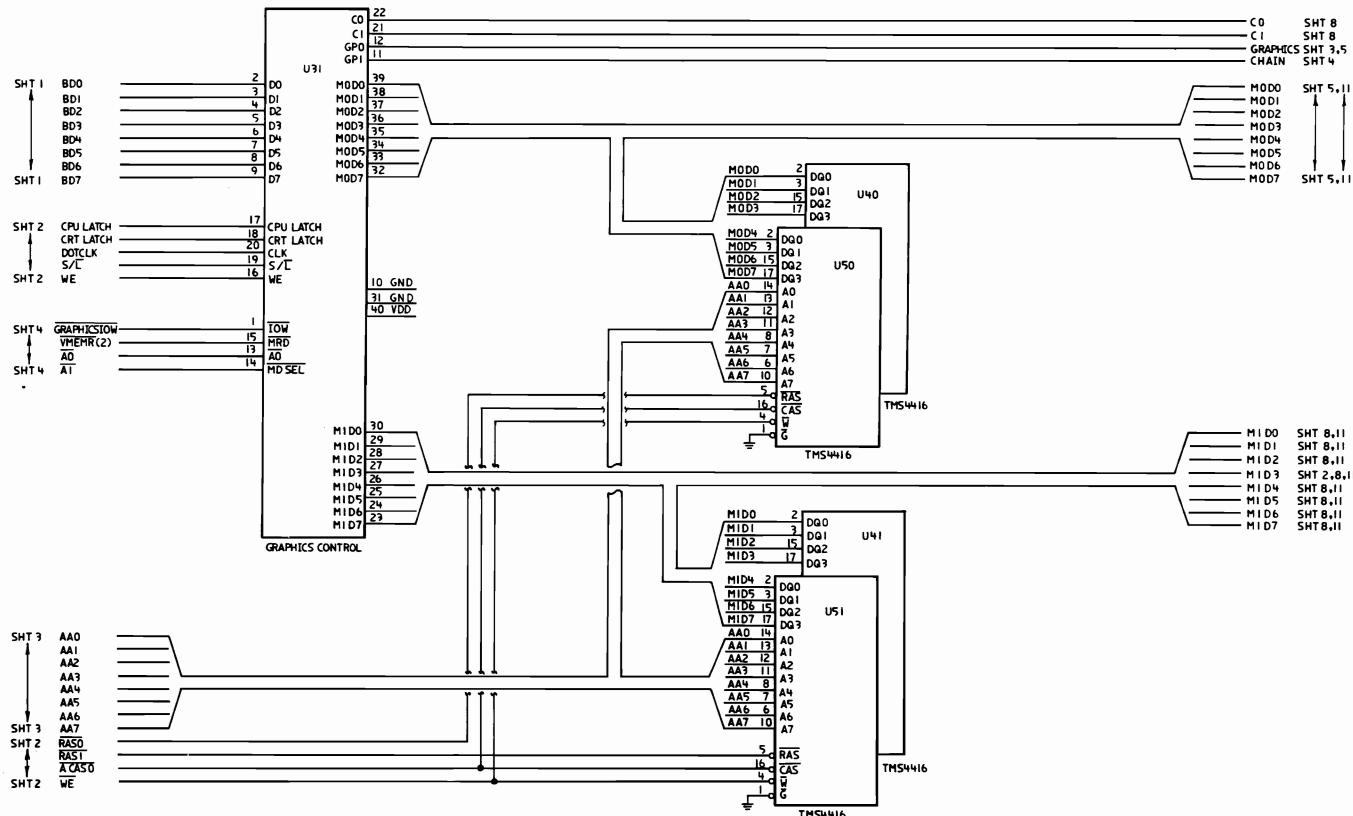
Enhanced Graphics Adapter Sheet 3 of 11

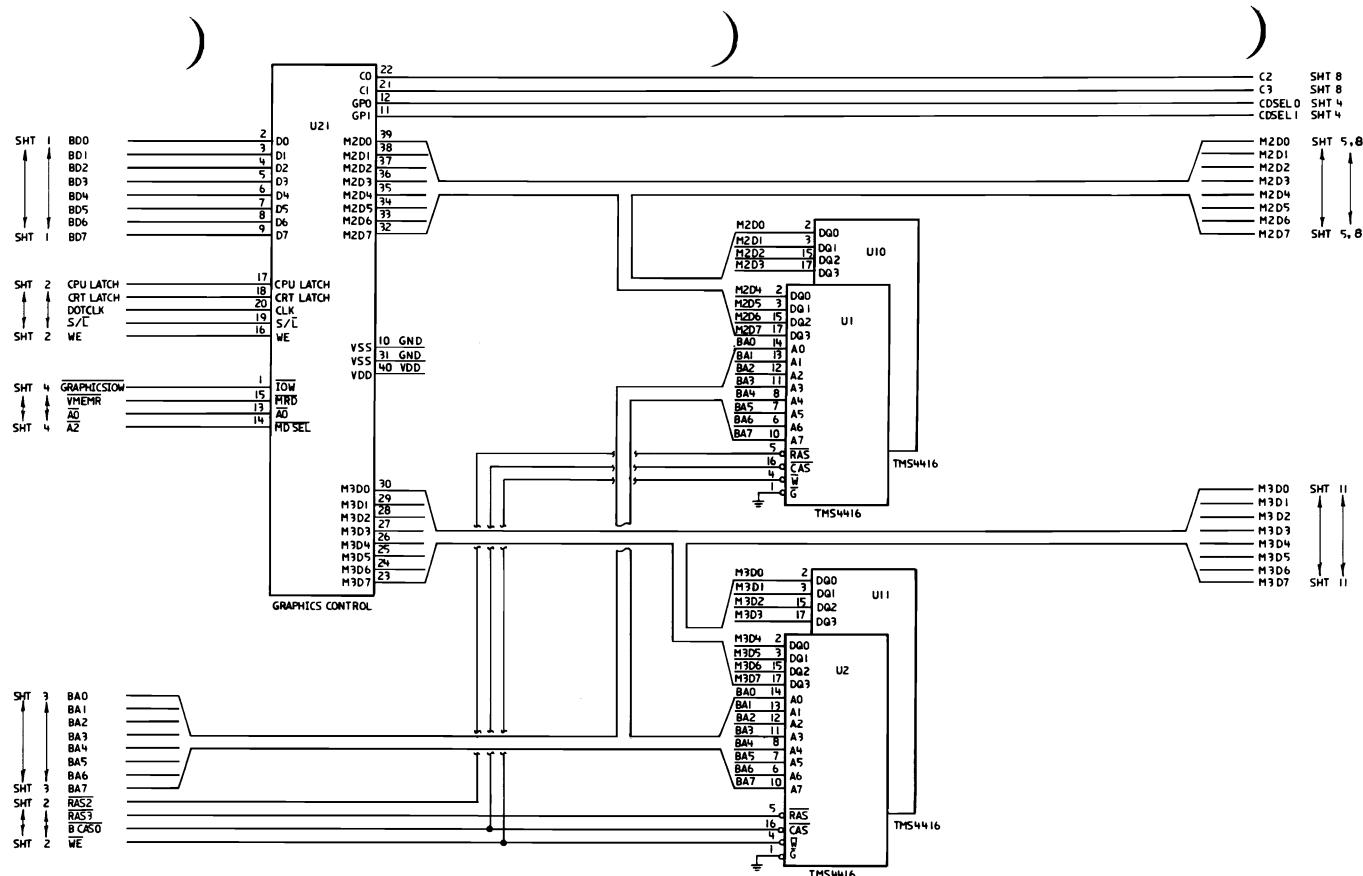


Enhanced Graphics Adapter Sheet 4 of 11

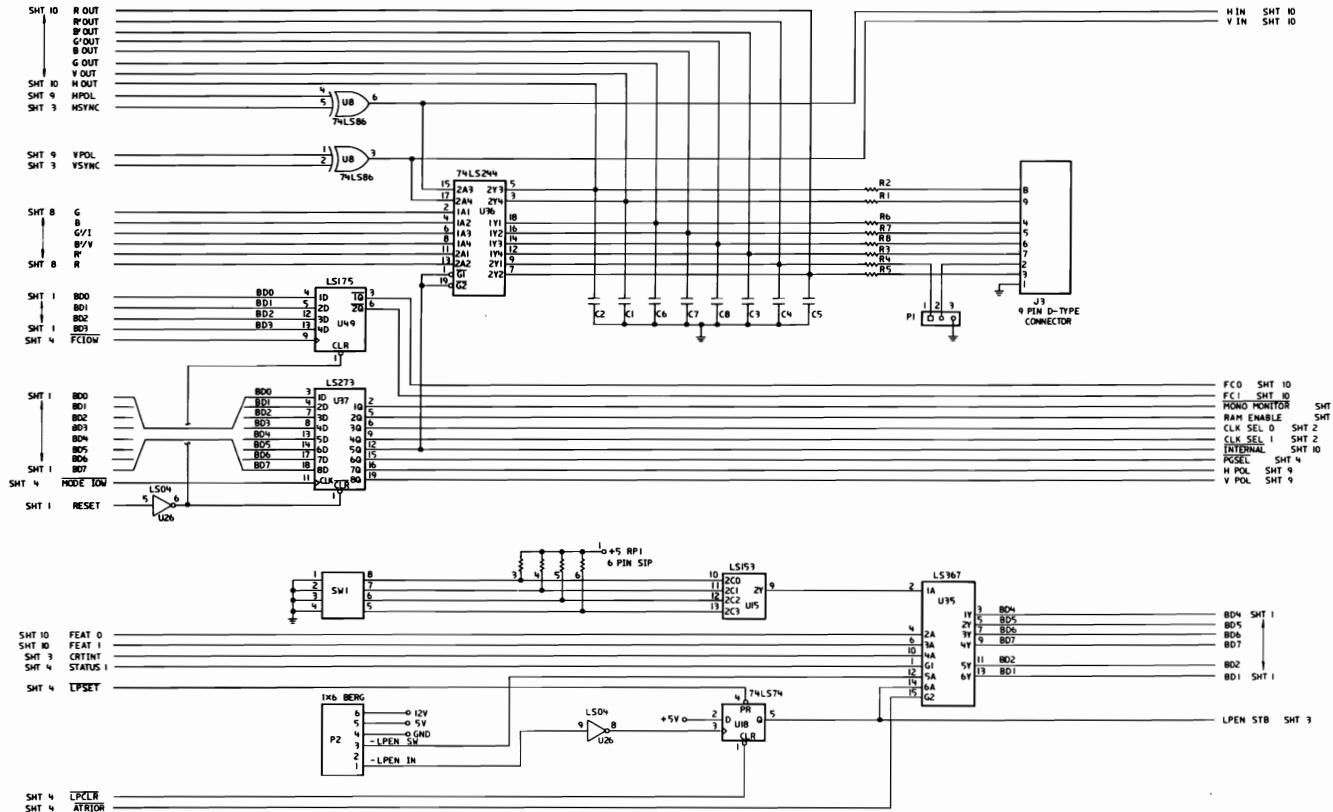


ENHANCED GRAPHICS ADAPTER

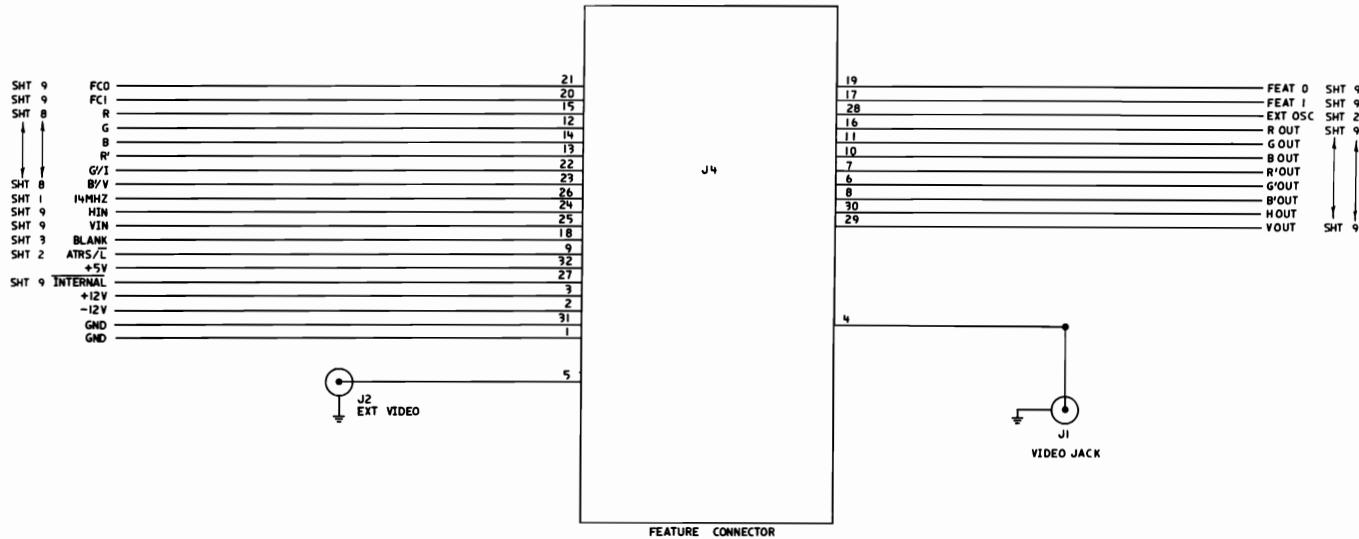


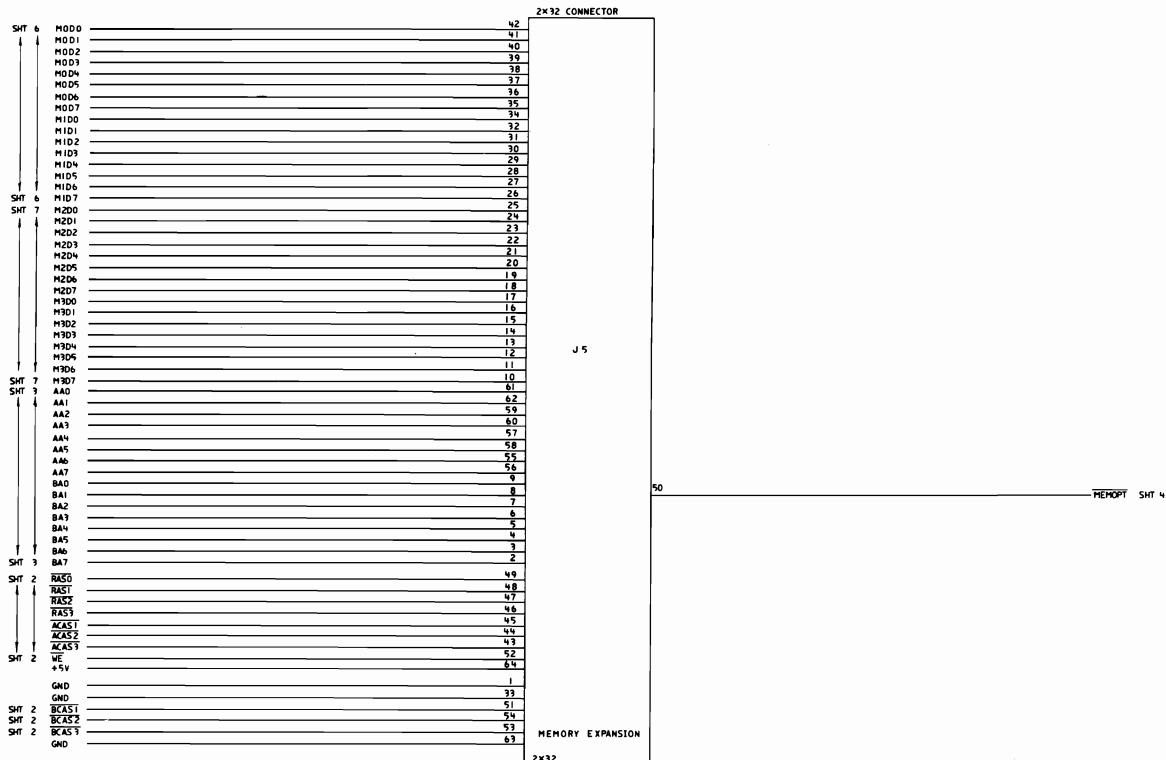


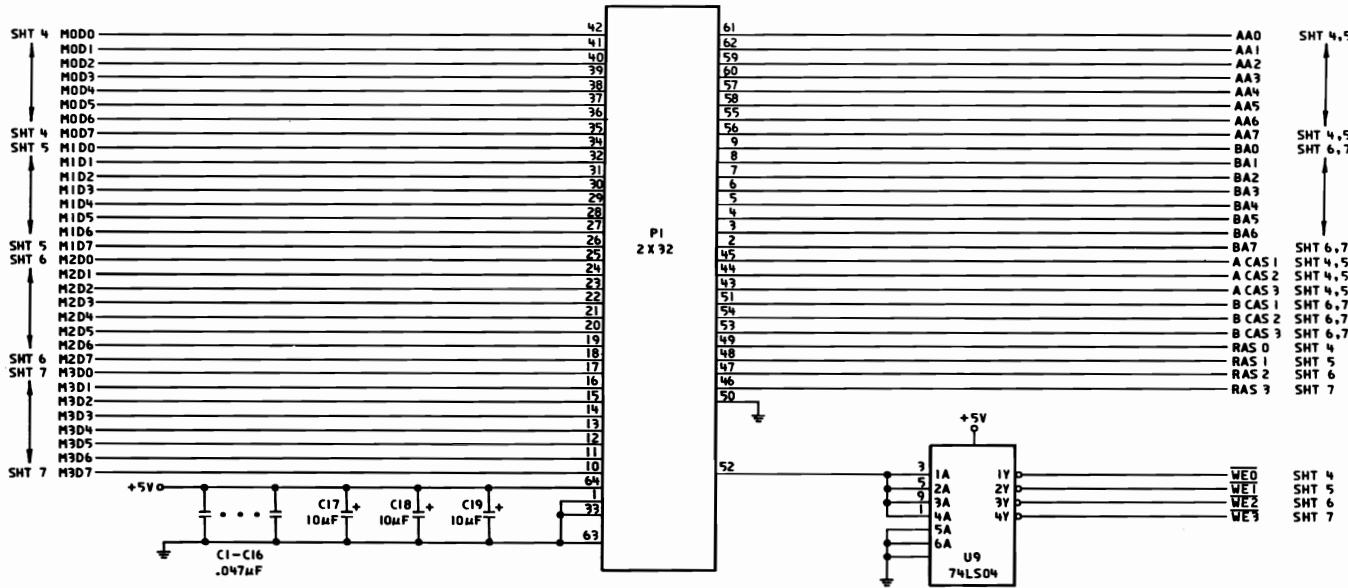
Enhanced Graphics Adapter Sheet 7 of 11



ENHANCED GRAPHICS ADAPTER

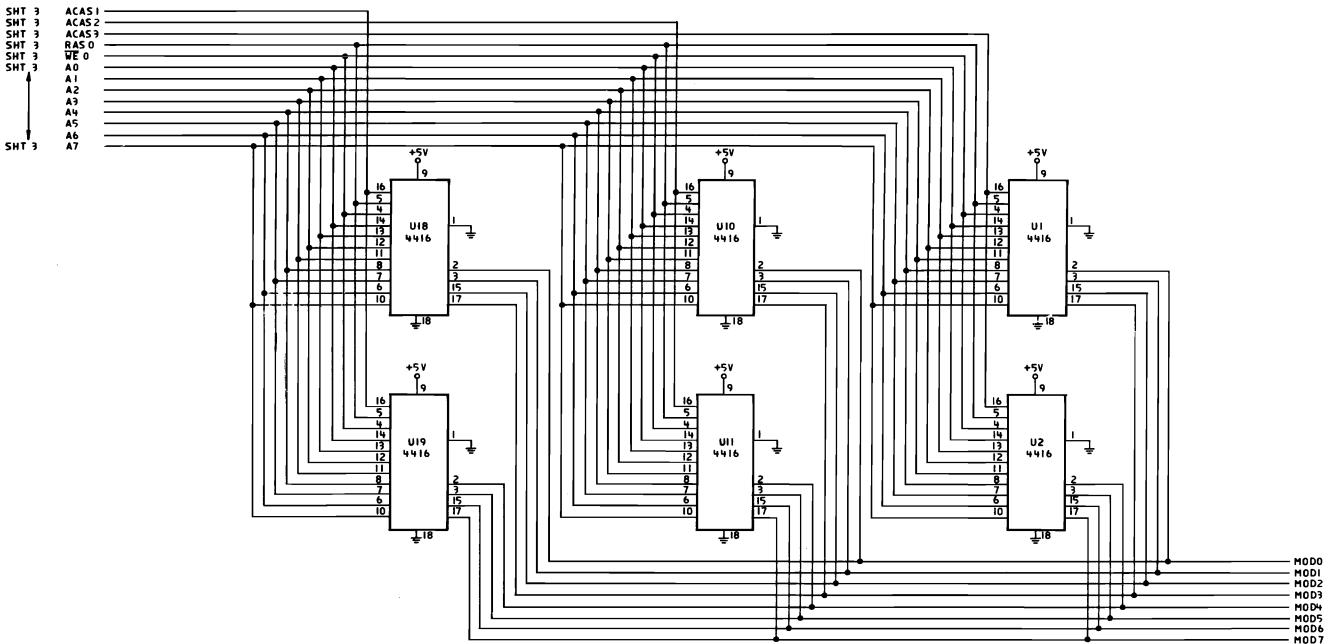






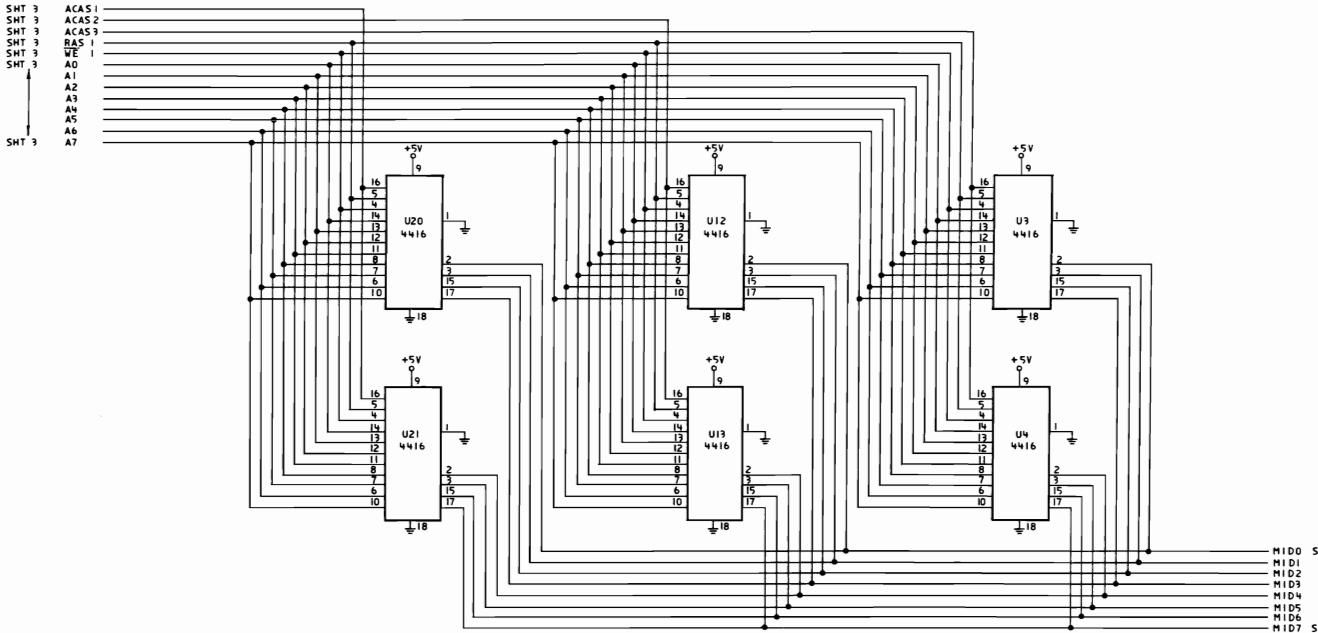
Graphics Memory Expansion Card Sheet 1 of 5

M0D0 SHT 3
 M0D1 SHT 3
 M0D2 SHT 3
 M0D3 SHT 3
 M0D4 SHT 3
 M0D5 SHT 3
 M0D6 SHT 3
 M0D7 SHT 3

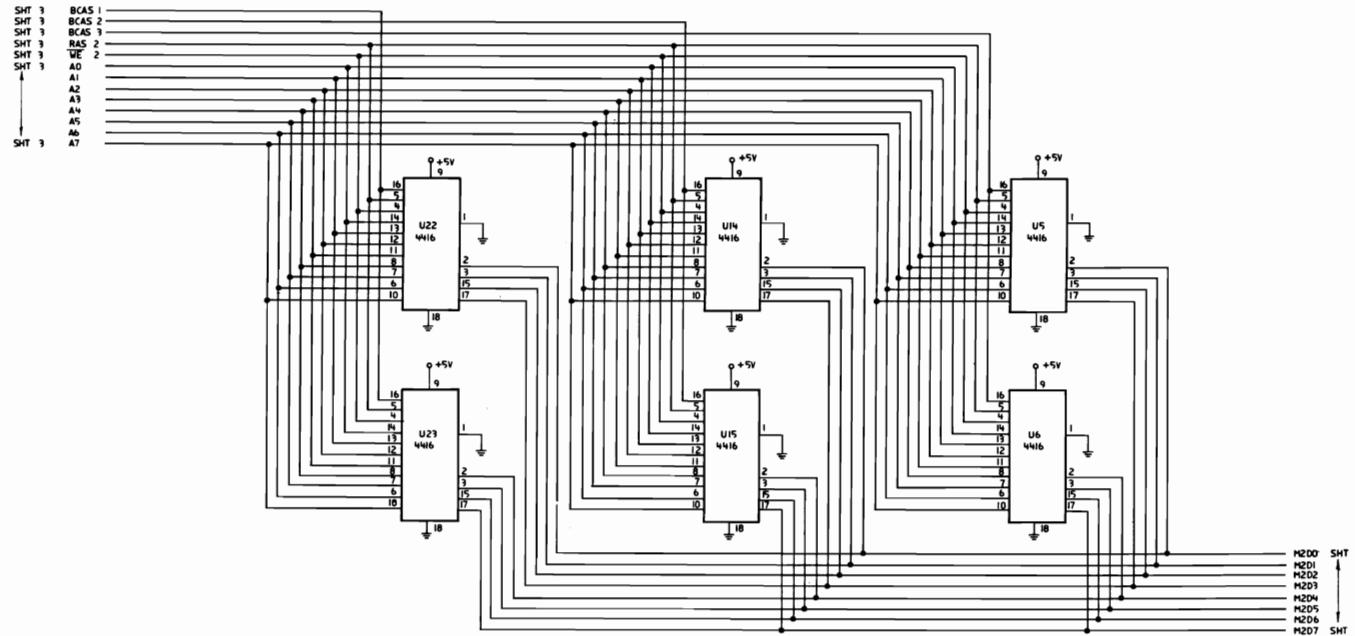


Graphics Memory Expansion Card Sheet 2 of 5

M100 SHT3
M101
M102
M103
M104
M105
M106
M107 SHT3

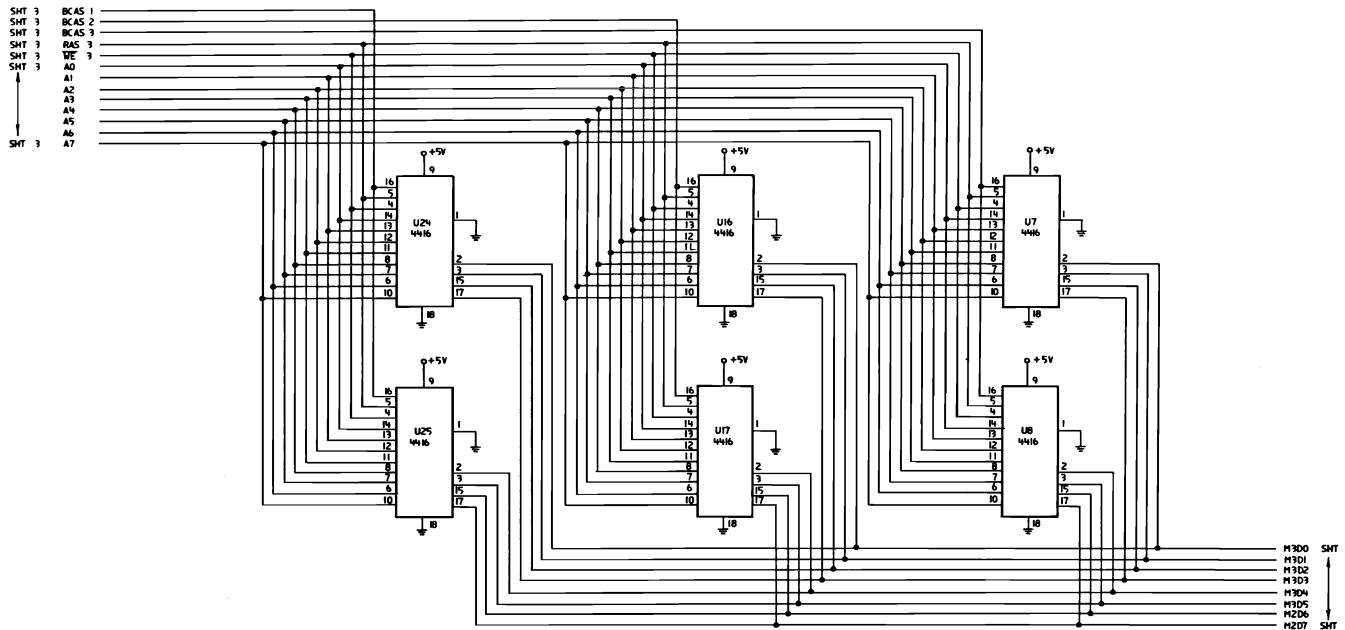


Graphics Memory Expansion Card Sheet 3 of 5



Graphics Memory Expansion Card Sheet 4 of 5

Graphics Memory Expansion Card Sheet 5 of 5



BIOS Listing

Vectors with Special Meanings

Interrupt Hex 42 - Reserved

When an IBM Enhanced Graphics Adapter is installed, the BIOS routines use interrupt 42 to revector the video pointer.

Interrupt Hex 43 - IBM Enhanced Graphics Video Parameters

When an IBM Enhanced Graphics Adapter is installed, the BIOS routines use this vector to point to a data region containing the parameters required for the initializing of the IBM Enhanced Graphics Adapter. Note that the format of the table must adhere to the BIOS conventions established in the listing. The power-on routines initialize this vector to point to the parameters contained in the IBM Enhanced Graphics Adapter ROM.

Interrupt Hex 44 - Graphics Character Table

When an IBM Enhanced Graphics Adapter is installed the BIOS routines use this vector to point to a table of dot patterns that will be used when graphics characters are to be displayed. This table will be used for the first 128 code points in video modes 4, 5, and 6. This table will be used for 256 characters in all additional graphics modes. See the appropriate BIOS interface for additional information on setting and using the graphics character table pointer.

```

1 PAGE, 120
2 TITLE ENHANCED GRAPHICS ADAPTER BIOS
3 EXTRN CGMN:NEAR, CGDDOT:NEAR, INT_1F_1:NEAR, CGMN_FDC:NEAR
4 EXTRN END_ADDRESS:NEAR
5
6
7 THE BIOS ROUTINES ARE MEANT TO BE ACCESSED THROUGH
8 SOFTWARE INTERRUPTS ONLY. ANY ADDRESSES PRESENT IN
9 THIS LISTING ARE INCLUDED FOR COMPLETENESS
10 NOT FOR REFERENCE. APPLICATIONS WHICH REFERENCE
11 ABSOLUTE ADDRESSES WITHIN THE CODE SEGMENT
12 VIOLATE THE STRUCTURE AND DESIGN OF BIOS.
13
14
15 .LIST
16 INCLUDE VFRONT.INC
17 SUBTTL VFRONT.INC
18 PAGE
19
20
21
22
23
24
25
26
27
28
29
30
31
32
33
34
35
36
37
38
39
40
41
42
43
44
45
46
47
48
49
50
51
52
53
54
55
56
57
58
59
60
61
62
63
64
65
66
67
68
69
70
71
72
73
74
75
76
77
78
79
80
81
82
83
84
85
86
87
88
89
90
91
92
93
94
95
96
97
98
99
100
101
102
103
104
105
106
107
108
109
110
111
112
113
114
115
116
117
118
119
120
121
122
123
124
125
126
----- INT 10 -----
VIDEO_10
THESE ROUTINES PROVIDE THE CRT INTERFACE
THE FOLLOWING FUNCTIONS ARE PROVIDED:
(AH)=0 SET MODE (AL) CONTAINS MODE VALUE
AL AD TYPE RES NOTES DF-DIM DISPLAY MAX PCS
* 0 BB ALPHA 640X200 40X25 COLOR - BW 8
* 1 BB ALPHA 640X200 40X25 COLOR 8
* 2 BB ALPHA 640X200 80X25 COLOR - BW 8
* 3 BB ALPHA 640X200 80X25 COLOR 8
5 BB GRPHX 320X200 40X25 COLOR - BW 1
6 BB GRPHX 320X200 40X25 COLOR - BW 1
* 7 BB ALPHA 640X200 80X25 COLOR - BW 1
7 BB ALPHA 720X350 80X25 MONOCHROME 8
8 RESERVED
9 RESERVED
A RESERVED
B RESERVED - INTERNAL USE
C RESERVED - INTERNAL USE
D AO GRPHX 320X200 40X25 COLOR 8
E AO GRPHX 640X200 80X25 COLOR 4
F AU GRPHX 640X350 80X25 MONOCHROME 2
10 AO GRPHX 640X350 80X25 HI RES 2
----- NOTE : HIGH BIT AL SET PREVENTS REGEN BUFFER CLEAR ON
HODES RUNNING ON THE COMBO VIDEO ADAPTER -----
*** NOTE BW MODES OPERATE SAME AS COLOR MODES, BUT
NOT COLOR BURST IS NOT ENABLED
(AH)=1 SET CURSOR POSITION
(CH) = BITS 4-0 = START LINE FOR CURSOR
*** HARDWARE WILL ALWAYS CAUSE BLINK
** SETTING BIT 5 OR 6 WILL CAUSE ERRATIC
** BEHAVIOR DURING OR NO CURSOR AT ALL
(CL) = BITS 5-0 END LINE FOR CURSOR
SET CURSOR POSITION
(DH,DL) = ROW,COLUMN (0,0) IS UPPER LEFT
(BH) = PAGE NUMBER
READ CURSOR POSITION
(DH) = PAGE NUMBER
ON EXIT (DH,DL) = ROW,COLUMN OF CURRENT CURSOR
(CH,CL) = CURSOR MODE CURRENTLY SET
(AH)=4 READ LIGHT PEN POSITION
ON EXIT:
(AH) = 0 -- LIGHT PEN SWITCH NOT DOWN/NOT_TRIGGERED
(AH) = 1 -- VALID LIGHT PEN VALUE IN REGISTERS
(DH,DL) = ROW,COLUMN OF CHARACTER LP POSN
(CX) = RASTER LINE NUMBER (0-199)
(CX) = RASTER LINE NUMBER OF NEW GRAPHICS MODES
(BX) = PIXEL COLUMN (0-319,639)
(AH)=5 SELECT ACTIVE DISPLAY PAGE
(AL) = PAGE VALUE, SEE AH=0 FOR PAGE INFO
(AH)=6 SCROLL ACTIVE PAGE UP
(AL) = NUMBER OF LINES, INPUT LINES BLANKED AT BOTTOM
OF WINDOW
AL = 0 MEANS BLANK ENTIRE WINDOW
(CH,CL) = ROW,COLUMN OF UPPER LEFT CORNER OF SCROLL
(DH,DL) = ROW,COLUMN OF LOWER RIGHT CORNER OF SCROLL
(BH) = ATTRIBUTE TO BE USED ON BLANK LINE
(AH)=7 SCROLL ACTIVE PAGE DOWN
(AL) = NUMBER OF LINES, INPUT LINES BLANKED AT TOP
OF WINDOW
AL = 0 MEANS BLANK ENTIRE WINDOW
(CH,CL) = ROW,COLUMN OF UPPER LEFT CORNER OF SCROLL
(DH,DL) = ROW,COLUMN OF LOWER RIGHT CORNER OF SCROLL
(BH) = ATTRIBUTE TO BE USED ON BLANK LINE
CHARACTER HANDLING ROUTINES
(AH) = 8 READ ATTRIBUTE/CHARACTER AT CURRENT CURSOR POSITION
(BH) = DISPLAY PAGE
ON EXIT:
(AL) = CHAR READ
(AH) = ATTRIBUTE OF CHARACTER READ (ALPHA MODES ONLY)
(AH) = 9 WRITE ATTRIBUTE/CHARACTER AT CURRENT CURSOR POSITION
(BH) = DISPLAY PAGE
(CX) = COUNT OF CHARACTERS TO WRITE
(AL) = CHAR TO WRITE
(BL) = ATTRIBUTE OF CHARACTER (ALPHA)/COLOR OF CHAR
(GRAPHICS)
SEE NOTE ON WRITE DOT FOR BIT 7 OF BL = 1.
(AH) = A WRITE CHARACTER ONLY AT CURRENT CURSOR POSITION
(BH) = DISPLAY PAGE
(CX) = COUNT OF CHARACTERS TO WRITE
(AL) = CHAR TO WRITE
FOR READ/WRITE CHARACTER INTERFACE WHILE IN GRAPHICS MODE, THE
CHARACTERS ARE FORMED FROM A CHARACTER GENERATOR IMAGE
MAP STORED IN THE SYSTEM ROM. ONLY THE FIRST 128 CHARS
ARE CONTAINED THERE. TO READ/WRITE THE SECOND 128
CHARS, THE USER MUST INITIALIZE THE POINTER AT
INTERRUPT 1FH (LOCATION 0007CH) TO POINT TO THE 1K BYTE
TABLE CONTAINING THE CODE POINTS FOR THE SECOND
128 CHARS (128-255).
FOR THE NEW GRAPHICS MODES 256 GRAPHICS CHARS ARE
SUPPLIED IN THE SYSTEM ROM.
FOR WRITE CHARACTER INTERFACE IN GRAPHICS MODE, THE REPLICATION
FACTOR CONTAINED IN (CX) ON ENTRY WILL PRODUCE VALID
RESULTS ONLY FOR CHARACTERS CONTAINED ON THE SAME ROW.
CONTINUATION TO SUCCEEDING LINES WILL NOT PRODUCE
CORRECTLY.
-----
```

```

127
128 C : GRAPHICS INTERFACE
129 C : (AH) = B SET COLOR PALETTE
130 C : FOR USE IN COMPATIBILITY MODES
131 C : (BH) = COLOR ID TO BE SET (0-197)
132 C : (BL) = COLOR VALUE TO BE USED WITH THAT COLOR ID
133 C : NOTE: FOR THE CURRENT COLOR CARD, THIS ENTRY POINT
134 C : HAS MEANING ONLY FOR 320X200 GRAPHICS.
135 C : COLOR ID = 0 SELLECTS THE COLOR (0-15).
136 C : COLOR ID = 1 SELLECTS THE PALETTE TO BE USED:
137 C : 0 = GREEN(1)/RED(2)/BROWN(3)
138 C : 1 = CYAN(1)/MAGENTA(2)/WHITE(3)
139 C : IN 40X25 OR 80X25 ALPHA MODES, THE VALUE SET
140 C : BY COLOR ID IS THE BORDER COLOR, IN 320X200 MODE
141 C : BORDER COLOR TO BE USED (VALUES 0-31,
142 C : WHERE 16-31 SELECT THE HIGH INTENSITY
143 C : BACKGROUND SET).
144 C : (AH) = C WRITE DOT
145 C : (BH) = PAGE
146 C : (DX) = ROW NUMBER
147 C : (CX) = COLUMN NUMBER
148 C : (AL) = COLOR VALUE
149 C : IF THE VALUE OF AL = 1, THEN THE COLOR VALUE IS
150 C : EXCLUSIVE OR'D WITH THE CURRENT CONTENTS OF
151 C : THE DOT
152 C : (AH) = D READ DOT
153 C : (BH) = PAGE
154 C : (DX) = ROW NUMBER
155 C : (CX) = COLUMN NUMBER
156 C : (AL) RETURNS THE DOT READ
157 C :
158 C : ASCII TELETYPE ROUTINE FOR OUTPUT
159 C :
160 C : (AH) = E WRITE TELETYPE TO ACTIVE PAGE
161 C : (AL) = CHAR TO WRITE
162 C : (BL) = FOREGROUND COLOR IN GRAPHICS MODE
163 C : NOTE -- SCREEN WIDTH IS CONTROLLED BY PREVIOUS MODE SET
164 C :
165 C : (AH) = F CURRENT VIDEO STATE
166 C : RETURNS THE CURRENT VIDEO STATE
167 C : (AL) = 0 NUMBER OF ROWS (SEE AH=0 FOR EXPLANATION)
168 C : (AH) = NUMBER OF CHARACTER COLUMNS ON SCREEN
169 C : (BH) = CURRENT ACTIVE DISPLAY PAGE
170 C :
171 C : (AH) = 10 SET PALETTE REGISTERS
172 C :
173 C : (AL) = 0 SET INDIVIDUAL PALETTE REGISTER
174 C : BL = PALETTE REGISTER TO BE SET
175 C : BH = VALUE TO SET
176 C :
177 C : AL = 1 SET OVERSCAN REGISTER
178 C : BH = VALUE TO SET
179 C :
180 C : AL = 2 SET ALL PALETTE REGISTERS AND OVERSCAN
181 C : ES:DX POINTS TO A 17 BYTE TABLE
182 C : BYTES 0 - 15 ARE THE PALETTE VALUES, RESPECTIVELY
183 C : BYTE 16 IS THE OVERSCAN VALUE
184 C :
185 C : AL = 3 TOGGLE INTENSIFY/BLINKING BIT
186 C : BL = 0 ENABLE INTENSIFY
187 C : BL = 1 ENABLE BLINKING
188 C :
189 C : (AH) = 11 CHARACTER GENERATOR ROUTINE
190 C : NOTE : THIS CALL WILL INITIATE A MODE SET, COMPLETELY
191 C : RESETTING THE VIDEO ENVIRONMENT BUT MAINTAINING
192 C : THE REGEN BUFFER.
193 C :
194 C : AL = 00 USER ALPHA LOAD
195 C : ES:BP - POINTER TO USER TABLE
196 C : CX - COUNT TO STORE
197 C : DX - CHARACTER OFFSET INTO TABLE
198 C : BL - BLOCK TO LOAD
199 C : BH - NUMBER OF BYTES PER CHARACTER
200 C :
201 C : AL = 01 ROM MONOCHROME SET
202 C : BL - BLOCK TO LOAD
203 C : BH - NUMBER OF BYTES PER CHARACTER
204 C :
205 C : AL = 02 ROM 8X8 DOUBLE DOT
206 C : BL - BLOCK TO LOAD
207 C : BH - NUMBER OF BYTES PER CHARACTER
208 C :
209 C : AL = 03 SET BLOCK SPECIFIER
210 C : BL - CHAR GEN BLOCK SPECIFIER
211 C : D3-D2 ATTR BIT 3 ONE, CHAR GEN 0-3
212 C : D1-D0 ATTR BIT 3 ZERO, CHAR GEN 0-3
213 C : NOTE : THIS MUST CALL AL = 03 AS A FUNCTION CALL
214 C : AX = 1000H
215 C : BX = 0712H
216 C : IS RECOMMENDED TO SET THE COLOR PLANES
217 C : REGIONS TO 5, 2 CHARACTERS AND EIGHT
218 C : CONSISTENT COLORS.
219 C :
220 C : NOTE : THE FOLLOWING INTERFACE (AL=1X) IS SIMILAR IN FUNCTION
221 C : TO (AL=0X) EXCEPT THAT:
222 C : - PAGE ZERO MUST BE ACTIVE
223 C : - POINTS (BYTES/CHAR) WILL BE RECALCULATED
224 C : - ROWS WILL BE CALCULATED FROM THE FOLLOWING:
225 C :   INT((200 OR 350) / POINTS) - 1
226 C : - CRT_LENGTH WILL BE CALCULATED AS:
227 C :   (ROWS + 1) * CRT_COLS + 2
228 C : - THE CRTC WILL BE REPROGRAMMED AS FOLLOWS :
229 C :   R09H = POINTS - 1
230 C :   R09H = POINTS - 1      MAX SCAN LINE
231 C :   R0AH = POINTS - 2      CURSOR START
232 C :   R0BH = 0                CURSOR END
233 C :   R12H = VERT DISP END
234 C :   R14H = POINTS          UNDERLINE LOC
235 C :
236 C : THE ABOVE REGISTER CALCULATIONS MUST BE CLOSE TO THE
237 C : ORIGINAL TABLE VALUES OR UNDETERMINED RESULTS WILL
238 C : OCCUR.
239 C :
240 C : NOTE : THE FOLLOWING INTERFACE IS DESIGNED TO BE
241 C : CALLED ONLY IMMEDIATELY AFTER A MODE SET HAS
242 C : BEEN ISSUED. FAILURE TO ADHERE TO THIS PRACTICE
243 C : MAY CAUSE UNDETERMINED RESULTS.
244 C :
245 C : AL = 10 USER ALPHA LOAD
246 C : ES:BP - POINTER TO USER TABLE
247 C : CX - COUNT TO STORE
248 C : DX - CHARACTER OFFSET INTO TABLE
249 C : BL - BLOCK TO LOAD
250 C : BH - NUMBER OF BYTES PER CHARACTER
251 C :
252 C :

```



```

0463 ???? 379 C ADDR_6845 DW ?
0465 ?? 380 C CRT_MODE_SET DB ?
0466 ?? 381 C CRT_PALETTE DB ?
382 C
0472 ?? 383 C
0472 ???? 384 C RESET_FLAG DW 0472H ?
0484 ?? 385 C ORG 0484H ?
0484 ?? 386 C ROWS DB ?
0485 ???? 387 C POINTS DW ? ; ROWS ON THE SCREEN
0485 ???? 388 C ; BYTES PER CHARACTER
0487 ?? 389 C
0487 ?? 390 C
391 C
392 C ; INFO
393 C ; D7 - HIGH BIT OF MODE SET, CLEAR/NOT CLEAR REGEN
394 C ; D6 - MEMORY D6 D5 = 0 0 - 064K 0 1 - 128K
395 C ; D5 - RESERVED 1 0 - 192K 1 1 - 256K
396 C ; D4 - RESERVED
397 C ; D3 - ACTIVE MONITOR (0), EGA NOT ACTIVE (1)
398 C ; D2 - WAIT FOR DISPLAY ENABLE (1)
399 C ; D1 - EGA HAS A MONOCHROME ATTACHED (1)
399 C ; D0 - SET C_TYPE EMULATE ACTIVE (0)
400 C
0488 ?? 401 C
402 C
403 C ; INFO_3
404 C ; D7-D4 FEATURE BITS
405 C ; D3-D0 SWITCHES
406 C
04AB ?? 407 C
04AB ?? 408 C ORG 04ABH
409 C
410 C ----- SAVE_PTR
411 C
412 C ; SAVE_PTR IS A POINTER TO A TABLE AS DESCRIBED AS FOLLOWS :
413 C
414 C ; DWORD_1
415 C ; DWORD_2 DYNAMIC SAVE AREA POINTER
416 C ; DWORD_3 ALPHA MODE AUXILIARY CHAR GEN POINTER
417 C ; DWORD_4 GRAPICS MODE AUXILIARY CHAR GEN POINTER
418 C ; DWORD_5 RESERVED
419 C ; DWORD_6 RESERVED
420 C ; DWORD_7 RESERVED
421 C
422 C ; DWORD_1 PARAMETER TABLE POINTER
423 C ; DWORD_2 INITIALIZED TO BIOS EGA PARAMETER TABLE.
424 C ; DWORD_3 THIS VALUE MUST EXIST.
425 C
426 C ; DWORD_2 PARAMETER SAVE AREA POINTER
427 C ; DWORD_3 INITIALIZED TO 0000:0000. THIS VALUE IS OPTIONAL.
428 C ; DWORD_4 WHEN NON-ZERO, THIS POINTER WILL BE USED AS A POINTER
429 C ; DWORD_5 TO A RAM AREA WHERE CERTAIN DYNAMIC VALUES ARE TO
430 C ; DWORD_6 BE SAVED. WHEN IN EGA OPERATION THIS RAM AREA WILL
431 C ; DWORD_7 HOLD THE EGA ALIASES. THE ALIASES ARE STORED IN THE
432 C ; DWORD_8 OVERSCAN VALUE IN BYTES 160, 160 RESPECTIVELY.
433 C ; DWORD_9 AT LEAST 256 BYTES MUST BE ALLOCATED FOR THIS AREA.
434 C
435 C ; DWORD_3 ALPHA MODE AUXILIARY POINTER
436 C ; DWORD_4 INITIALIZED TO 0000:0000. THIS VALUE IS OPTIONAL.
437 C ; DWORD_5 WHEN NON-ZERO, THIS POINTER IS USED AS A POINTER
438 C ; DWORD_6 TO A TABLES DESCRIBED AS FOLLOWS :
439 C
440 C ; DWORD_7 BYTES/CHARACTER
441 C ; DWORD_8 BLOCK TO LOAD, SHOULD BE ZERO FOR NORMAL
442 C ; DWORD_9 OPERATION
443 C ; DWORD_10 WORD COUNT TO STORE, SHOULD BE 256D FOR NORMAL
444 C ; DWORD_11 OPERATION
445 C ; DWORD_12 WORD OPERATOR OFFSET, SHOULD BE ZERO FOR NORMAL
446 C ; DWORD_13 OPERATION
447 C ; DWORD_14 WORD POINTER TO A FONT TABLE
448 C ; DWORD_15 DISPLAYABLE ROM
449 C ; DWORD_16 160 BYTES. THE MAXIMUM CALCULATED VALUE WILL BE
450 C ; DWORD_17 USED. ELSE THIS VALUE WILL BE USED.
451 C ; DWORD_18 CONSECUTIVE BYTES OF MODE VALUES FOR WHICH
452 C ; DWORD_19 THIS FONT DESCRIPTION IS TO BE USED.
453 C ; DWORD_20 THE END OF THIS STREAM IS INDICATED BY A
454 C ; DWORD_21 BYTE CODE OF 'FF'
455 C
456 C ; DWORD_22 NOTE : USE OF THIS POINTER MAY CAUSE UNEXPECTED
457 C ; DWORD_23 CURSOR TYPE OPERATION. FOR AN EXPLANATION
458 C ; DWORD_24 OF CURSOR TYPE SEE AH = 01 IN THE INTERFACE
459 C ; DWORD_25 SECTION.
460 C
461 C ; DWORD_4 GRAPHICS MODE AUXILIARY POINTER
462 C ; DWORD_5 INITIALIZED TO 0000:0000. THIS VALUE IS OPTIONAL.
463 C ; DWORD_6 WHEN NON-ZERO, THIS POINTER IS USED AS A POINTER
464 C ; DWORD_7 TO A TABLES DESCRIBED AS FOLLOWS :
465 C
466 C ; DWORD_8 BYTES/CHARACTER
467 C ; DWORD_9 DISPLAYABLE ROM
468 C ; DWORD_10 BYTES/CHARACTER
469 C ; DWORD_11 WORD POINTER TO A FONT TABLE
470 C ; DWORD_12 BYTES CONSECUTIVE BYTES OF MODE VALUES FOR WHICH
471 C ; DWORD_13 THIS FONT DESCRIPTION IS TO BE USED.
472 C ; DWORD_14 THE END OF THIS STREAM IS INDICATED BY A
473 C ; DWORD_15 BYTE CODE OF 'FF'
474 C
475 C ; DWORD_5 THRU DWORD_7 RESERVED AND SET TO 0000:0000.
476 C
477 C
0500 478 C STATUS_BYT E 0500H
0500 ?? 479 C ORG DB ?
0501 ?? 480 C ABS0 ENDS
481 C
= 0061 482 C PORT_B EQU 61H ; 8255 PORT B ADDR
= 0040 483 C TIMER EQU 40H
484 C
485 C ----- EQUATES FOR CARD PORT ADDRESSES
486 C
487 C ; SEQ_ADDR EQU 0C4H
488 C ; SEQ_DATA EQU 0C5H
489 C ; CRTC_ADDR EQU 0D0H
490 C ; CRTC_ADDR_B EQU 0B4H
= 0084 491 C CRTC_DATA EQU 0D5H ; OR 0B5H
= 0005 492 C GRAPH_1_POS EQU 0CCH
= 000C 493 C GRAPH_2_POS EQU 0CDH
= 000A 494 C GRAPH_3_POS EQU 0CEH
= 00CE 495 C GRAPH_DATA EQU 0CFH
= 00CF 496 C MISC_OUTPUT EQU 0C2H
= 0022 497 C MISC_STATUS EQU 0C3H
= 008A 498 C INPUT_STATUS_B EQU 0B4H
= 00DA 499 C INPUT_STATUS EQU 0DAH
= 00DA 500 C ATTR_READ EQU 0DAH
= 00CO 501 C ATTR_WRITE EQU 0COAH
502 C
503 C ; ----- EQUATES FOR ADDRESS REGISTER VALUES
504 C

```

```

= 0000      505      C      S_RESET      EQU 00H
= 0001      506      C      S_CLOCK      EQU 01H
= 0002      507      C      S_TMR       EQU 02H
= 0003      508      C      S_CGEN      EQU 04H
= 0004      509      C      S_MEM       EQU 04H
= 0005      510      C      S_HRZ_TOT    EQU 00H
= 0006      511      C      S_HRZ_CTRP   EQU 01H
= 0007      512      C      S_HRZ_BLK    EQU 02H
= 0008      513      C      S_HRZ_SYN    EQU 03H
= 0009      514      C      S_HRZ_SYNC   EQU 04H
= 000A      515      C      S_HRZ_SYNCH  EQU 05H
= 000B      516      C      S_OVERFLOW   EQU 06H
= 000C      517      C      S_OVERFLOWH  EQU 07H
= 000D      518      C      S_PRE_ROW    EQU 08H
= 000E      519      C      S_MAX_SCAN_LN EQU 09H
= 000F      520      C      S_CRSR_ART    EQU 0AH
= 0010      521      C      S_CRSR_END   EQU 0AH
= 0011      522      C      S_CRSR_HGH    EQU 0BH
= 0012      523      C      S_CRSR_LOW    EQU 0DH
= 0013      524      C      S_CRSR_LOC_HGH EQU 0EH
= 0014      525      C      S_CRSR_LOC_LOW EQU 0FH
= 0015      526      C      S_VRT_SYN_CTRP EQU 10H
= 0016      527      C      S_VRT_SYN_CTRH EQU 11H
= 0017      528      C      S_LGHT_PEN_HGH EQU 10H
= 0018      529      C      S_VRT_SYN_END   EQU 11H
= 0019      530      C      S_VRT_SYN_LOW   EQU 11H
= 0020      531      C      S_VRT_DBL_END  EQU 12H
= 0021      532      C      S_OFFSET      EQU 13H
= 0022      533      C      S_UNDERLN_LOC  EQU 14H
= 0023      534      C      S_CTR_VRT_BLK  EQU 15H
= 0024      535      C      S_CTR_VRT_BLKH EQU 16H
= 0025      536      C      S_MODE_CNTL   EQU 17H
= 0026      537      C      C_LN_COMP    EQU 18H
= 0027      538      C      G_SET_RESET   EQU 00H
= 0028      539      C      G_ENBL_SET    EQU 01H
= 0029      540      C      G_CLR_COMP    EQU 02H
= 0030      541      C      G_DATA_ROT   EQU 03H
= 0031      542      C      G_READ_MAP   EQU 04H
= 0032      543      C      G_WRITE_MAP  EQU 05H
= 0033      544      C      G_MISC        EQU 06H
= 0034      545      C      G_COLOR       EQU 07H
= 0035      546      C      G_BIT_MASK   EQU 08H
= 0036      547      C      P_MODE        EQU 10H
= 0037      548      C      P_OVERSC     EQU 11H
= 0038      549      C      P_CPLANE    EQU 12H
= 0039      550      C      P_HPEL       EQU 13H
= 0040      551      C      SUBTTL      ;----- CODE SEGMENT
= 0041      552      C      553      C      554      C
= 0042      555      C      556      C      557      C
0000      558      C      CODE      SEGMENT PUBLIC
= 0043      559      C      INCLUDE    VPOST.INC
= 0044      560      C      SUBTTL    VPOST.INC
= 0045      561      C      PAGE
= 0046      562      C      ;----- POST
= 0047      563      C      ;----- ASSUME CS:CODE,DS:ABSO
= 0048      564      C      ASSUME  CS:CODE,DS:ABSO
0000      565      C      ORG      OH
0001      566      C      DB      055H      ; SIGNATURE
0002      567      C      DB      0AAH      ; BYTES
0003      568      C      DB      020H      ; LENGTH INDICATOR
0004      569      C      ;----- NOTE : DO NOT USE THE SIGNATURE BYTES AS A PRESENCE TEST
0005      570      C      ;----- PLANAR VIDEO SWITCH SETTINGS
0006      571      C      ;----- 0 0 - UNUSED
0007      572      C      ;----- 0 1 - 40 X 25 COLOR
0008      573      C      ;----- 1 0 - 80 X 25 COLOR
0009      574      C      ;----- 1 1 - 80 X 25 MONOCHROME
0010      575      C      ;----- NOTE : 0 0 MUST BE SET WHEN THIS ADAPTER IS INSTALLED.
0011      576      C      ;----- VIDEO ADAPTER SWITCH SETTINGS
0012      577      C      ;----- 0 0 0 0 - MONOC PRIMARY, EGA COLOR, 40X25
0013      578      C      ;----- 0 0 0 1 - MONOC PRIMARY, EGA COLOR, 80X25
0014      579      C      ;----- 0 0 1 0 - MONOC PRIMARY, EGA HI RES EMULATE (SAME AS 0001)
0015      580      C      ;----- 0 0 1 1 - MONOC PRIMARY, EGA HI RES ENHANCED
0016      581      C      ;----- 0 1 0 0 - COLOR NO PRIMARY, EGA MONOCHROME
0017      582      C      ;----- 0 1 0 1 - COLOR 80 PRIMARY, EGA MONOCHROME
0018      583      C      ;----- 0 1 1 0 - MONOC SECONDARY, EGA COLOR, 40X25
0019      584      C      ;----- 0 1 1 1 - MONOC SECONDARY, EGA COLOR, 80X25
0020      585      C      ;----- 1 0 0 0 - MONOC SECONDARY, EGA HI RES EMULATE (SAME AS 0111)
0021      586      C      ;----- 1 0 0 1 - MONOC SECONDARY, EGA HI RES ENHANCED
0022      587      C      ;----- 1 0 1 0 - COLOR NO SECONDARY, EGA MONOCHROME
0023      588      C      ;----- 1 0 1 1 - COLOR 80 SECONDARY, EGA MONOCHROME
0024      589      C      ;----- 1 1 0 0 - RESERVED
0025      590      C      ;----- 1 1 0 1 - RESERVED
0026      591      C      ;----- 1 1 1 0 - RESERVED
0027      592      C      ;----- 1 1 1 1 - RESERVED
0028      593      C      ;----- 2 0 0 0 - SETUP ROUTINE FOR THIS MODULE
0029      594      C      VIDEO_SETUP  PROC  FAR
0030      595      C      VIDEO_SETUP  PROC  NEAR
0031      596      C      DB      '12001'
0032      597      C      DB      '6277356 (C)COPYRIGHT IBM 1984'
0033      598      C      DB      '9/13/84'
0034      599      C      ;----- SET UP VIDEO VECTORS
0035      600      C      ;----- L1:
0036      601      C      ;----- 0020      B6 03
0037      602      C      ;----- 0021      B2 DA
0038      603      C      ;----- 0031      EC
0039      604      C      ;----- 0032      B2 BA
0040      605      C      ;----- 0033      EC
0041      606      C      ;----- 0035      B2 CO
0042      607      C      ;----- 0037      B0 00
0043      608      C      ;----- 0039      EE
0044      609      C      ;----- 0020      B6 D2
0045      610      C      ;----- 0021      B2 DA
0046      611      C      ;----- 0031      EC
0047      612      C      ;----- 0032      B2 BA
0048      613      C      ;----- 0033      EC
0049      614      C      ;----- 0035      B2 CO
0050      615      C      ;----- 0037      B0 00
0051      616      C      ;----- 0039      EE
0052      617      C      ;----- 0020      B6 D2
0053      618      C      ;----- 0021      B2 DA
0054      619      C      ;----- 0031      EC
0055      620      C      ;----- 0032      B2 BA
0056      621      C      ;----- 0033      EC
0057      622      C      ;----- 0035      B2 CO
0058      623      C      ;----- 0037      B0 00
0059      624      C      ;----- 0039      EE
0060      625      C      ;----- 0020      B6 D2
0061      626      C      ;----- 0021      B2 DA
0062      627      C      ;----- 0031      EC
0063      628      C      ;----- 0032      B2 BA
0064      629      C      ;----- 0033      EC
0065      630      C      ;----- 0035      B2 CO
0066      631      C      ;----- 0037      B0 00
0067      632      C      ;----- 0039      EE
0068      633      C      ;----- 0020      B6 D2
0069      634      C      ;----- 0021      B2 DA
0070      635      C      ;----- 0031      EC
0071      636      C      ;----- 0032      B2 BA
0072      637      C      ;----- 0033      EC
0073      638      C      ;----- 0035      B2 CO
0074      639      C      ;----- 0037      B0 00
0075      640      C      ;----- 0039      EE
0076      641      C      ;----- 0020      B6 D2
0077      642      C      ;----- 0021      B2 DA
0078      643      C      ;----- 0031      EC
0079      644      C      ;----- 0032      B2 BA
0080      645      C      ;----- 0033      EC
0081      646      C      ;----- 0035      B2 CO
0082      647      C      ;----- 0037      B0 00
0083      648      C      ;----- 0039      EE
0084      649      C      ;----- 0020      B6 D2
0085      650      C      ;----- 0021      B2 DA
0086      651      C      ;----- 0031      EC
0087      652      C      ;----- 0032      B2 BA
0088      653      C      ;----- 0033      EC
0089      654      C      ;----- 0035      B2 CO
0090      655      C      ;----- 0037      B0 00
0091      656      C      ;----- 0039      EE
0092      657      C      ;----- 0020      B6 D2
0093      658      C      ;----- 0021      B2 DA
0094      659      C      ;----- 0031      EC
0095      660      C      ;----- 0032      B2 BA
0096      661      C      ;----- 0033      EC
0097      662      C      ;----- 0035      B2 CO
0098      663      C      ;----- 0037      B0 00
0099      664      C      ;----- 0039      EE
0100      665      C      ;----- 0020      B6 D2
0101      666      C      ;----- 0021      B2 DA
0102      667      C      ;----- 0031      EC
0103      668      C      ;----- 0032      B2 BA
0104      669      C      ;----- 0033      EC
0105      670      C      ;----- 0035      B2 CO
0106      671      C      ;----- 0037      B0 00
0107      672      C      ;----- 0039      EE
0108      673      C      ;----- 0020      B6 D2
0109      674      C      ;----- 0021      B2 DA
0110      675      C      ;----- 0031      EC
0111      676      C      ;----- 0032      B2 BA
0112      677      C      ;----- 0033      EC
0113      678      C      ;----- 0035      B2 CO
0114      679      C      ;----- 0037      B0 00
0115      680      C      ;----- 0039      EE
0116      681      C      ;----- 0020      B6 D2
0117      682      C      ;----- 0021      B2 DA
0118      683      C      ;----- 0031      EC
0119      684      C      ;----- 0032      B2 BA
0120      685      C      ;----- 0033      EC
0121      686      C      ;----- 0035      B2 CO
0122      687      C      ;----- 0037      B0 00
0123      688      C      ;----- 0039      EE
0124      689      C      ;----- 0020      B6 D2
0125      690      C      ;----- 0021      B2 DA
0126      691      C      ;----- 0031      EC
0127      692      C      ;----- 0032      B2 BA
0128      693      C      ;----- 0033      EC
0129      694      C      ;----- 0035      B2 CO
0130      695      C      ;----- 0037      B0 00
0131      696      C      ;----- 0039      EE
0132      697      C      ;----- 0020      B6 D2
0133      698      C      ;----- 0021      B2 DA
0134      699      C      ;----- 0031      EC
0135      700      C      ;----- 0032      B2 BA
0136      701      C      ;----- 0033      EC
0137      702      C      ;----- 0035      B2 CO
0138      703      C      ;----- 0037      B0 00
0139      704      C      ;----- 0039      EE
0140      705      C      ;----- 0020      B6 D2
0141      706      C      ;----- 0021      B2 DA
0142      707      C      ;----- 0031      EC
0143      708      C      ;----- 0032      B2 BA
0144      709      C      ;----- 0033      EC
0145      710      C      ;----- 0035      B2 CO
0146      711      C      ;----- 0037      B0 00
0147      712      C      ;----- 0039      EE
0148      713      C      ;----- 0020      B6 D2
0149      714      C      ;----- 0021      B2 DA
0150      715      C      ;----- 0031      EC
0151      716      C      ;----- 0032      B2 BA
0152      717      C      ;----- 0033      EC
0153      718      C      ;----- 0035      B2 CO
0154      719      C      ;----- 0037      B0 00
0155      720      C      ;----- 0039      EE
0156      721      C      ;----- 0020      B6 D2
0157      722      C      ;----- 0021      B2 DA
0158      723      C      ;----- 0031      EC
0159      724      C      ;----- 0032      B2 BA
0160      725      C      ;----- 0033      EC
0161      726      C      ;----- 0035      B2 CO
0162      727      C      ;----- 0037      B0 00
0163      728      C      ;----- 0039      EE
0164      729      C      ;----- 0020      B6 D2
0165      730      C      ;----- 0021      B2 DA
0166      731      C      ;----- 0031      EC
0167      732      C      ;----- 0032      B2 BA
0168      733      C      ;----- 0033      EC
0169      734      C      ;----- 0035      B2 CO
0170      735      C      ;----- 0037      B0 00
0171      736      C      ;----- 0039      EE
0172      737      C      ;----- 0020      B6 D2
0173      738      C      ;----- 0021      B2 DA
0174      739      C      ;----- 0031      EC
0175      740      C      ;----- 0032      B2 BA
0176      741      C      ;----- 0033      EC
0177      742      C      ;----- 0035      B2 CO
0178      743      C      ;----- 0037      B0 00
0179      744      C      ;----- 0039      EE
0180      745      C      ;----- 0020      B6 D2
0181      746      C      ;----- 0021      B2 DA
0182      747      C      ;----- 0031      EC
0183      748      C      ;----- 0032      B2 BA
0184      749      C      ;----- 0033      EC
0185      750      C      ;----- 0035      B2 CO
0186      751      C      ;----- 0037      B0 00
0187      752      C      ;----- 0039      EE
0188      753      C      ;----- 0020      B6 D2
0189      754      C      ;----- 0021      B2 DA
0190      755      C      ;----- 0031      EC
0191      756      C      ;----- 0032      B2 BA
0192      757      C      ;----- 0033      EC
0193      758      C      ;----- 0035      B2 CO
0194      759      C      ;----- 0037      B0 00
0195      760      C      ;----- 0039      EE
0196      761      C      ;----- 0020      B6 D2
0197      762      C      ;----- 0021      B2 DA
0198      763      C      ;----- 0031      EC
0199      764      C      ;----- 0032      B2 BA
0200      765      C      ;----- 0033      EC
0201      766      C      ;----- 0035      B2 CO
0202      767      C      ;----- 0037      B0 00
0203      768      C      ;----- 0039      EE
0204      769      C      ;----- 0020      B6 D2
0205      770      C      ;----- 0021      B2 DA
0206      771      C      ;----- 0031      EC
0207      772      C      ;----- 0032      B2 BA
0208      773      C      ;----- 0033      EC
0209      774      C      ;----- 0035      B2 CO
0210      775      C      ;----- 0037      B0 00
0211      776      C      ;----- 0039      EE
0212      777      C      ;----- 0020      B6 D2
0213      778      C      ;----- 0021      B2 DA
0214      779      C      ;----- 0031      EC
0215      780      C      ;----- 0032      B2 BA
0216      781      C      ;----- 0033      EC
0217      782      C      ;----- 0035      B2 CO
0218      783      C      ;----- 0037      B0 00
0219      784      C      ;----- 0039      EE
0220      785      C      ;----- 0020      B6 D2
0221      786      C      ;----- 0021      B2 DA
0222      787      C      ;----- 0031      EC
0223      788      C      ;----- 0032      B2 BA
0224      789      C      ;----- 0033      EC
0225      790      C      ;----- 0035      B2 CO
0226      791      C      ;----- 0037      B0 00
0227      792      C      ;----- 0039      EE
0228      793      C      ;----- 0020      B6 D2
0229      794      C      ;----- 0021      B2 DA
0230      795      C      ;----- 0031      EC
0231      796      C      ;----- 0032      B2 BA
0232      797      C      ;----- 0033      EC
0233      798      C      ;----- 0035      B2 CO
0234      799      C      ;----- 0037      B0 00
0235      800      C      ;----- 0039      EE
0236      801      C      ;----- 0020      B6 D2
0237      802      C      ;----- 0021      B2 DA
0238      803      C      ;----- 0031      EC
0239      804      C      ;----- 0032      B2 BA
0240      805      C      ;----- 0033      EC
0241      806      C      ;----- 0035      B2 CO
0242      807      C      ;----- 0037      B0 00
0243      808      C      ;----- 0039      EE
0244      809      C      ;----- 0020      B6 D2
0245      810      C      ;----- 0021      B2 DA
0246      811      C      ;----- 0031      EC
0247      812      C      ;----- 0032      B2 BA
0248      813      C      ;----- 0033      EC
0249      814      C      ;----- 0035      B2 CO
0250      815      C      ;----- 0037      B0 00
0251      816      C      ;----- 0039      EE
0252      817      C      ;----- 0020      B6 D2
0253      818      C      ;----- 0021      B2 DA
0254      819      C      ;----- 0031      EC
0255      820      C      ;----- 0032      B2 BA
0256      821      C      ;----- 0033      EC
0257      822      C      ;----- 0035      B2 CO
0258      823      C      ;----- 0037      B0 00
0259      824      C      ;----- 0039      EE
0260      825      C      ;----- 0020      B6 D2
0261      826      C      ;----- 0021      B2 DA
0262      827      C      ;----- 0031      EC
0263      828      C      ;----- 0032      B2 BA
0264      829      C      ;----- 0033      EC
0265      830      C      ;----- 0035      B2 CO
0266      831      C      ;----- 0037      B0 00
0267      832      C      ;----- 0039      EE
0268      833      C      ;----- 0020      B6 D2
0269      834      C      ;----- 0021      B2 DA
0270      835      C      ;----- 0031      EC
0271      836      C      ;----- 0032      B2 BA
0272      837      C      ;----- 0033      EC
0273      838      C      ;----- 0035      B2 CO
0274      839      C      ;----- 0037      B0 00
0275      840      C      ;----- 0039      EE
0276      841      C      ;----- 0020      B6 D2
0277      842      C      ;----- 0021      B2 DA
0278      843      C      ;----- 0031      EC
0279      844      C      ;----- 0032      B2 BA
0280      845      C      ;----- 0033      EC
0281      846      C      ;----- 0035      B2 CO
0282      847      C      ;----- 0037      B0 00
0283      848      C      ;----- 0039      EE
0284      849      C      ;----- 0020      B6 D2
0285      850      C      ;----- 0021      B2 DA
0286      851      C      ;----- 0031      EC
0287      852      C      ;----- 0032      B2 BA
0288      853      C      ;----- 0033      EC
0289      854      C      ;----- 0035      B2 CO
0290      855      C      ;----- 0037      B0 00
0291      856      C      ;----- 0039      EE
0292      857      C      ;----- 0020      B6 D2
0293      858      C      ;----- 0021      B2 DA
0294      859      C      ;----- 0031      EC
0295      860      C      ;----- 0032      B2 BA
0296      861      C      ;----- 0033      EC
0297      862      C      ;----- 0035      B2 CO
0298      863      C      ;----- 0037      B0 00
0299      864      C      ;----- 0039      EE
0300      865      C      ;----- 0020      B6 D2
0301      866      C      ;----- 0021      B2 DA
0302      867      C      ;----- 0031      EC
0303      868      C      ;----- 0032      B2 BA
0304      869      C      ;----- 0033      EC
0305      870      C      ;----- 0035      B2 CO
0306      871      C      ;----- 0037      B0 00
0307      872      C      ;----- 0039      EE
0308      873      C      ;----- 0020      B6 D2
0309      874      C      ;----- 0021      B2 DA
0310      875      C      ;----- 0031      EC
0311      876      C      ;----- 0032      B2 BA
0312      877      C      ;----- 0033      EC
0313      878      C      ;----- 0035      B2 CO
0314      879      C      ;----- 0037      B0 00
0315      880      C      ;----- 0039      EE
0316      881      C      ;----- 0020      B6 D2
0317      882      C      ;----- 0021      B2 DA
0318      883      C      ;----- 0031      EC
0319      884      C      ;----- 0032      B2 BA
0320      885      C      ;----- 0033      EC
0321      886      C      ;----- 0035      B2 CO
0322      887      C      ;----- 0037      B0 00
0323      888      C      ;----- 0039      EE
0324      889      C      ;----- 0020      B6 D2
0325      890      C      ;----- 0021      B2 DA
0326      891      C      ;----- 0031      EC
0327      892      C      ;----- 0032      B2 BA
0328      893      C      ;----- 0033      EC
0329      894      C      ;----- 0035      B2 CO
0330      895      C      ;----- 0037      B0 00
0331      896      C      ;----- 0039      EE
0332      897      C      ;----- 0020      B6 D2
0333      898      C      ;----- 0021      B2 DA
0334      899      C      ;----- 0031      EC
0335      900      C      ;----- 0032      B2 BA
0336      901      C      ;----- 0033      EC
0337      902      C      ;----- 0035      B2 CO
0338      903      C      ;----- 0037      B0 00
0339      904      C      ;----- 0039      EE
0340      905      C      ;----- 0020      B6 D2
0341      906      C      ;----- 0021      B2 DA
0342      907      C      ;----- 0031      EC
0343      908      C      ;----- 0032      B2 BA
0344      909      C      ;----- 0033      EC
0345      910      C      ;----- 0035      B2 CO
0346      911      C      ;----- 0037      B0 00
0347      912      C      ;----- 0039      EE
0348      913      C      ;----- 0020      B6 D2
0349      914      C      ;----- 0021      B2 DA
0350      915      C      ;----- 0031      EC
0351      916      C      ;----- 0032      B2 BA
0352      917      C      ;----- 0033      EC
0353      918      C      ;----- 0035      B2 CO
0354      919      C      ;----- 0037      B0 00
0355      920      C      ;----- 0039      EE
0356      921      C      ;----- 0020      B6 D2
0357      922      C      ;----- 0021      B2 DA
0358      923      C      ;----- 0031      EC
0359      924      C      ;----- 0032      B2 BA
0360      925      C      ;----- 0033      EC
0361      926      C      ;----- 0035      B2 CO
0362      927      C      ;----- 0037      B0 00
0363      928      C      ;----- 0039      EE
0364      929      C      ;----- 0020      B6 D2
0365      930      C      ;----- 0021      B2 DA
0366      931      C      ;----- 0031      EC
0367      932      C      ;----- 0032      B2 BA
0368      933      C      ;----- 0033      EC
0369      934      C      ;----- 0035      B2 CO
0370      935      C      ;----- 0037      B0 00
0371      936      C      ;----- 0039      EE
0372      937      C      ;----- 0020      B6 D2
0373      938      C      ;----- 0021      B2 DA
0374      939      C      ;----- 0031      EC
0375      940      C      ;----- 0032      B2 BA
0376      941      C      ;----- 0033      EC
0377      942      C      ;----- 0035      B2 CO
0378      943      C      ;----- 0037      B0 00
0379      944      C      ;----- 0039      EE
0380      945      C      ;----- 0020      B6 D2
0381      946      C      ;----- 0021      B2 DA
0382      947      C      ;----- 0031      EC
0383      948      C      ;----- 0032      B2 BA
0384      949      C      ;----- 0033      EC
0385      950      C      ;----- 0035      B2 CO
0386      951      C      ;----- 0037      B0 00
0387      952      C      ;----- 0039      EE
0388      953      C      ;----- 0020      B6 D2
0389      954      C      ;----- 0021      B2 DA
0390      955      C      ;----- 0031      EC
0391      956      C      ;----- 0032      B2 BA
0392      957      C      ;----- 0033      EC
0393      958      C      ;----- 0035      B2 CO
0394      959      C      ;----- 0037      B0 00
0395      960      C      ;----- 0039      EE
0396      961      C      ;----- 0020      B6 D2
0397      962      C      ;----- 0021      B2 DA
0398      963      C      ;----- 0031      EC
0399      964      C      ;----- 0032      B2 BA
0400      965      C      ;----- 0033      EC
0401      966      C      ;----- 0035      B2 CO
0402      967      C      ;----- 0037      B0 00
0403      968      C      ;----- 0039      EE
0404      969      C      ;----- 0020      B6 D2
0405      970      C      ;----- 0021      B2 DA
0406      971      C      ;----- 0031      EC
0407      972      C      ;----- 0032      B2 BA
0408      973      C      ;----- 0033      EC
0409      974      C      ;----- 0035      B2 CO
0410      975      C      ;----- 0037      B0 00
0411      976      C      ;----- 0039      EE
0412      977      C      ;----- 0020      B6 D2
0413      978      C      ;----- 0021      B2 DA
0414      979      C      ;----- 0031      EC
0415      980      C      ;----- 0032      B2 BA
0416      981      C      ;----- 0033      EC
0417      982      C      ;----- 0035      B2 CO
0418      983      C      ;----- 0037      B0 00
0419      984      C      ;----- 0039      EE
0420      985      C      ;----- 0020      B6 D2
0421      986      C      ;----- 0021      B2 DA
0422      987      C      ;----- 0031      EC
0423      988      C      ;----- 0032      B2 BA
0424      989      C      ;----- 0033      EC
0425      990      C      ;----- 0035      B2 CO
0426      991      C      ;----- 0037      B0 00
0427      992      C      ;----- 0039      EE
0428      993      C      ;----- 0020      B6 D2
0429      994      C      ;----- 0021      B2 DA
0430      995      C      ;----- 0031      EC
0431      996      C      ;----- 0032      B2 BA
0432      997      C      ;----- 0033      EC
0433      998      C      ;----- 0035      B2 CO
0434      999      C      ;----- 0037      B0 00
0435      1000      C      ;----- 0039      EE
0436      1001      C      ;----- 0020      B6 D2
0437      1002      C      ;----- 0021      B2 DA
0438      1003      C      ;----- 0031      EC
0439      1004      C      ;----- 0032      B2 BA
0440      1005      C      ;----- 0033      EC
0441      1006      C      ;----- 0035      B2 CO
0442      1007      C      ;----- 0037      B0 00
0443      1008      C      ;----- 0039      EE
0444      1009      C      ;----- 0020      B6 D2
0445      1010      C      ;----- 0021      B2 DA
0446      1011      C      ;----- 0031      EC
0447      1012      C      ;----- 0032      B2 BA
0448      1013      C      ;----- 0033      EC
0449      1014      C      ;----- 0035      B2 CO
0450      1015      C      ;----- 0037      B0 00
0451      1016      C      ;----- 0039      EE
0452      1017      C      ;----- 0020      B6 D2
0453      1018      C      ;----- 0021      B2 DA
```


011A 0000 757 C DW 0 ; GRAPHICS TABLES
 011C 0000 758 C DW 0
 011E 0000 759 C DW 0
 0120 0000 760 C DW 0
 0122 0000 761 C DW 0
 0124 0000 762 C DW 0
 0126 0000 763 C DW 0
 0126 0000 764 C DW 0
 0126 0000 765 C DW 0
 0128 0171 R 766 C T5 LABEL WORD
 0128 0171 R 767 C DW OFFSET PST_0
 0128 0171 R 768 C DW OFFSET PST_1
 0128 0171 R 769 C DW OFFSET PST_2
 0128 0171 R 770 C DW OFFSET PST_3
 0130 0194 R 771 C DW OFFSET PST_4
 0132 01A8 R 772 C DW OFFSET PST_5
 0134 01BC R 773 C DW OFFSET PST_6
 0136 01C7 R 774 C DW OFFSET PST_7
 0138 01C7 R 775 C DW OFFSET PST_8
 0138 01C7 R 776 C DW OFFSET PST_9
 013C 01DD R 777 C DW OFFSET PST_A
 013E 01F1 R 778 C DW OFFSET PST_B
 0140 0204 R 779 C DW OFFSET PST_OUT
 0142 0204 R 780 C DW OFFSET PST_OUT
 0144 0204 R 781 C DW OFFSET PST_OUT
 0146 0204 R 782 C DW OFFSET PST_OUT
 0146 0204 R 783 C DW OFFSET PST_OUT
 0148 0000 784 C
 0148 80 26 0410 R CF 785 C ENV_X PROC NEAR ; SET 40X25 COLOR ALPHA
 0148 80 26 0410 R CF 786 C AND EQUIP_LOW,0CFH
 0148 80 26 0410 R CF 787 C OR EQUIP_LOW,010H
 0152 88 0001 788 C MOV AX,1H
 0155 CD 10 789 C INT 10H
 0157 C3 790 C RET
 0158 791 C ENDP
 0158 0000 792 C
 0158 80 26 0410 R CF 793 C ENV_0 PROC NEAR ; SET 80X25 COLOR ALPHA
 0158 80 26 0410 R CF 794 C AND EQUIP_LOW,0CFH
 0158 80 26 0410 R CF 795 C OR EQUIP_LOW,020H
 0162 88 0003 796 C MOV AX,03H
 0165 CD 10 797 C INT 10H
 0167 C3 798 C RET
 0168 799 C ENDP
 0000 800 C
 0168 80 0E 0410 R 30 801 C ENV_3 PROC NEAR ; SET MONOCHROME ALPHA
 0169 88 0007 802 C AND EQUIP_LOW,030H
 0170 CD 10 803 C MOV AX,07H
 0172 C3 804 C INT 10H
 0173 805 C RET
 0173 806 C ENDP
 0000 807 C
 0173 808 C PST_0:
 0173 809 C
 0177 E8 0148 R 810 C AND INFO_AH
 0178 E8 0168 R 811 C CALL ENV_X
 0178 E8 0168 R 812 C CALL ENV_3
 0178 E8 0168 R 813 C RET
 0178 E8 0168 R 814 C
 017E 20 26 0487 R 815 C PST_1:
 0182 E8 0154 R 816 C AND INFO_AH
 0182 E8 0168 R 817 C CALL ENV_0
 0182 E8 0168 R 818 C CALL ENV_3
 0188 C3 819 C RET
 0189 820 C
 0189 20 26 0487 R 821 C PST_3:
 0189 822 C AND INFO_AH
 0189 823 C CALL ENV_0
 0193 C3 824 C CALL ENV_3
 0194 825 C PST_4:
 0194 B6 03 826 C MOV DH,3
 0196 B2 C2 827 C MOV DL,MISC_OUTPUT
 0198 B0 00 828 C MOV AL,0
 0198 EE 829 C OUT DX,AL
 0198 F6 D4 830 C NOT AH
 0198 00 06 0487 R 831 C INT INFO_AH
 01A1 E8 0168 R 832 C CALL ENV_3
 01A4 E8 0144 R 833 C CALL ENV_X
 01A7 C3 834 C RET
 01A8 835 C
 01AA B6 03 836 C PST_5:
 01AA B2 C2 837 C MOV DH,3
 01AC B0 00 838 C MOV DL,MISC_OUTPUT
 01AE EE 839 C MOV AL,0
 01AE F6 D4 840 C OUT DX,AL
 0181 08 26 0487 R 841 C NOT AH
 0185 E8 0168 R 842 C INT INFO_AH
 0188 E8 0158 R 843 C CALL ENV_3
 0188 E8 0158 R 844 C CALL ENV_X
 018C C3 845 C RET
 018C 20 26 0487 R 846 C PST_6:
 01C0 E8 0168 R 847 C AND INFO_AH
 01C3 E8 0144 R 848 C CALL ENV_3
 01C3 E8 0144 R 849 C CALL ENV_X
 01C7 850 C
 01C7 20 26 0487 R 851 C PST_7:
 01C7 852 C PST_8:
 01C7 E8 0168 R 853 C AND INFO_AH
 01CE E8 0158 R 854 C CALL ENV_3
 01D1 C3 855 C CALL ENV_X
 01D2 856 C
 01D2 20 26 0487 R 857 C PST_9:
 01D2 E8 0168 R 858 C AND INFO_AH
 01D9 E8 0158 R 859 C CALL ENV_3
 01DC C3 860 C CALL ENV_X
 01DD 861 C
 01DD B6 03 862 C PST_A:
 01DF B2 C2 863 C MOV DH,3
 01E1 B0 00 864 C MOV DL,MISC_OUTPUT
 01E3 EE 865 C MOV AL,0
 01E4 F6 D4 866 C OUT DX,AL
 01E4 08 26 0487 R 867 C NOT AH
 01EA E8 0144 R 868 C INT INFO_AH
 01ED E8 0168 R 869 C CALL ENV_X
 01FO C3 870 C CALL ENV_3
 01F1 871 C RET
 01F1 B6 03 872 C PST_B:
 01F3 B2 C2 873 C MOV DH,3
 01F5 B0 00 874 C MOV DL,MISC_OUTPUT
 01F5 B0 00 875 C MOV AL,0
 01F8 F6 D4 876 C OUT DX,AL
 01FA 08 26 0487 R 877 C NOT AH
 01FE E8 0158 R 878 C INT INFO_AH
 0201 E8 0168 R 879 C CALL ENV_0
 0201 E8 0168 R 880 C CALL ENV_3
 0204 C3 881 C MK_ENV RET
 0205 882 C ENDP


```

0294 74 07          1009      JE      E10      ; YES - SKIP VIDEO RAM TEST
0296 8E DB          1010      MOV     DS,BX      ; POINT DS TO VIDEO RAM STG
0298 E8 02DF R      1011      ASSUME DS:NOTHING, ES:NOTHING
0298 75 2E          1012      CALL    STGTST_CNT      ; GO TEST VIDEO R/W STG
0298 1013      JNE    E17      ; R/W STG FAILURE - BEEP SPK
0298 1014
0298 1020
0298 1021      CCCCCC:      ; SETUP VIDEO DATA ON SCREEN FOR VIDEO LINE TEST.
0298 1022      CCCCCC:      ; DESCRIPTION:  ENABLE VIDEO SIGNAL AND SET MODE,
0298 1023      CCCCCC:      ; DISPLAY A HORIZONTAL BAR ON SCREEN.
0298 1024      CCCCCC:
0298 1025      CCCCCC:
0298 1026      CCCCCC:
0298 1027      CCCCCC:
0298 1028      CCCCCC:      ; CRT INTERFACE LINES TEST
0298 1029      CCCCCC:      ; DESCRIPTION:  SENSE ON/OFF TRANSITION OF THE VIDEO ENABLE
0298 1030      CCCCCC:      ; AND HORIZONTAL SYNC LINES.
0298 1031      CCCCCC:
0298 1032      CCCCCC:
0298 1033      CCCCCC:      ; GET VIDEO SENSE SWS (AH)
0298 1034      CCCCCC:      ; SAVE IT
0298 1035      CCCCCC:      ; WRT BLANKS IN REVERSE VIDEO
0298 1036      CCCCCC:      ; SETUP STARTING LOC
0298 1037      CCCCCC:      ; NO. OF BLANKS TO DISPLAY
0298 1038      CCCCCC:      ; WRITE VIDEO STORAGE
0298 1039      CCCCCC:
0298 1040      CCCCCC:
0298 1041      CCCCCC:
0298 1042      CCCCCC:
0298 1043      CCCCCC:
0298 1044      CCCCCC:      ; GET VIDEO SENSE SW INFO
0298 1045      CCCCCC:      ; B/W CARD ATTACHED?
0298 1046      CCCCCC:      ; SETUP ADDR OF BW STATUS PORT
0298 1047      CCCCCC:      ; YES - GO TEST LINES
0298 1048      CCCCCC:      ; COLOR CARD IS ATTACHED
0298 1049      CCCCCC:      ; LINE_TST:
0298 1050      CCCCCC:
0298 1051      CCCCCC:
0298 1052      CCCCCC:      ; OFLLOOP_CNT:
0298 1053      CCCCCC:
0298 1054      CCCCCC:      ; READ CRT STATUS PORT
0298 1055      CCCCCC:      ; CHECK VIDEO/HORZ LINE
0298 1056      CCCCCC:      ; ITS ON - CHECK IF IT GOES OFF
0298 1057      CCCCCC:      ; LOOP TILL ON OR TIMEOUT
0298 1058      CCCCCC:      ; GO PRINT ERROR MSG
0298 1059      CCCCCC:
0298 1060      CCCCCC:
0298 1061      CCCCCC:
0298 1062      CCCCCC:
0298 1063      CCCCCC:
0298 1064      CCCCCC:
0298 1065      CCCCCC:      ; READ CRT STATUS PORT
0298 1066      CCCCCC:      ; CHECK VIDEO/HORZ LINE
0298 1067      CCCCCC:      ; ITS ON - CHECK NEXT LINE
0298 1068      CCCCCC:      ; LOOP IF OFF TILL IT GOES ON
0298 1069      CCCCCC:      ; CRT_ERR
0298 1070      CCCCCC:
0298 1071      CCCCCC:      ; THIS SUBROUTINE PERFORMS A READ/WRITE STORAGE TEST ON
0298 1072      CCCCCC:      ; A 16K BLOCK OF STORAGE.
0298 1073      CCCCCC:      ; ENTRY REQUIREMENTS:
0298 1074      CCCCCC:      ;   ES = ADDRESS OF STORAGE SEGMENT BEING TESTED
0298 1075      CCCCCC:      ;   DS = ADDRESS OF STORAGE SEGMENT BEING TESTED
0298 1076      CCCCCC:      ;   NH = NUMBER OF BYTES AT STGTST_CNT, CX MUST BE LOADED WITH
0298 1077      CCCCCC:      ;   THE BYTE COUNT.
0298 1078      CCCCCC:      ;   EXIT PARAMETERS:
0298 1079      CCCCCC:      ;   ZERO FLAG = 0 IF STORAGE ERROR (DATA COMPARE OR PARITY CHECK).
0298 1080      CCCCCC:      ;   AL = 0 FOR NO PARITY CHECK. ELSE AL=XOR ED BIT
0298 1081      CCCCCC:      ;   PATTERN OF THE EXPECTED DATA PATTERN VS THE
0298 1082      CCCCCC:      ;   ACTUAL DATA READ.
0298 1083      CCCCCC:      ; AX, BX, CX, DX, DI, AND SI ARE ALL DESTROYED.
0298 1084      CCCCCC:
0298 1085      CCCCCC:      ; STGTST PROC NEAR
0298 1086      CCCCCC:      ; MOV CX,4000H      ; SETUP CNT TO TEST A 16K BLK
0298 1087      CCCCCC:      ; STGTST_CNT:
0298 1088      CCCCCC:      ; CLD      ; SET DIR FLAG TO INCREMENT
0298 1089      CCCCCC:      ; MOV BX,CX      ; SAVE CNT (4K OF VIDEO OR 16K)
0298 1090      CCCCCC:      ; MOV AX,0AAAHH      ; GET DATA PATTERN TO WRITE
0298 1091      CCCCCC:      ; SUB DX,0FF55H      ; SETUP OTHER DATA PATTERNS TO REG
0298 1092      CCCCCC:      ; REP STOSB      ; DI = OFFSET O RELATIVE TO ES REG
0298 1093      CCCCCC:      ;           ; WRITE STORAGE LOCATIONS
0298 1094      CCCCCC:      ;           ; SI = CNT
0298 1095      CCCCCC:      ;           ; POINT TO LAST BYTE JUST WRITTEN
0298 1096      CCCCCC:      ;           ; SET DIR FLAG TO GO BACKWARDS
0298 1097      CCCCCC:
0298 1098      CCCCCC:
0298 1099      CCCCCC:
0298 1100      CCCCCC:      ; LODSB      ; SETUP BYTE CNT
0298 1101      CCCCCC:      ; XOR AX,DX      ; INNER TEST LOOP
0298 1102      CCCCCC:      ; JNE C7      ; READ OLD TEST BYTE [SI] +
0298 1103      CCCCCC:      ; MOV AL,DL      ; DATA READ AS EXPECTED ?
0298 1104      CCCCCC:      ; STOSB      ; NO - SET BYTE TO END READ
0298 1105      CCCCCC:      ; LOOP C5      ; GET NEXT DATA PATTERN TO WRITE
0298 1106      CCCCCC:      ;           ; WRITE INTO LOCATION JUST READ
0298 1107      CCCCCC:      ;           ; DECREMENT COUNT AND LOOP CX
0298 1108      CCCCCC:      ; AND AH,AH      ; ENDING O PATTERN WRITTEN TO STG?
0298 1109      CCCCCC:      ; JZ C6      ; YES - RETURN TO CALLER WITH AL=0
0298 1110      CCCCCC:      ; MOV AH,AL      ; SETUP NEW VALUE FOR COMPARE
0298 1111      CCCCCC:      ; XCHG DH,AL      ; MOVE NEXT DATA PATTERN TO DL
0298 1112      CCCCCC:      ; AND AH,AH      ; READ ZERO BYTE END TEST THIS PASS ?
0298 1113      CCCCCC:      ; JNZ C6      ; CONTINUE TEST SEQUENCE TILL 0
0298 1114      CCCCCC:      ; MOV DL,AH      ; ELSE SET 0 FOR END READ PATTERN
0298 1115      CCCCCC:      ; JMP C3      ; AND MAKE FINALS BACKWARDS PASS
0298 1116      CCCCCC:      ; C6:      ; SET DIR FLAG TO GO FORWARD
0298 1117      CCCCCC:      ; CLD      ; SET POINTER TO BEG LOCATION
0298 1118      CCCCCC:      ; INC DI      ; READ/WRITE FORWARD IN STG
0298 1119      CCCCCC:      ; DEC DI      ; ADJUST POINTER
0298 1120      CCCCCC:      ; JMP C3      ; READ/WRITE BACKWARD IN STG
0298 1121      CCCCCC:      ; C6X:      ; MOV AL,000H      ; AL=0 DATA COMPARE OK
0298 1122      CCCCCC:
0298 1123      CCCCCC:
0298 1124      CCCCCC:
0298 1125      CCCCCC:
0298 1126      CCCCCC:
0298 1127      CCCCCC:      ; STGTST ENDP
0298 1128      CCCCCC:      ; CCCCCC:      ; EGA CRT ATTACHMENT TEST
0298 1129      CCCCCC:
0298 1130      CCCCCC:      ; 1. INIT CRT TO 40X25 - BW ****SET TO MODE****      ; .
0298 1131      CCCCCC:      ; 2. CIRCUIT VERTICAL AND VIDEO ENABLES, AND CHECK      ; .
0298 1132      CCCCCC:      ; TIMING OF VERT. AND HORIZONTAL SYNC.      ; .
0298 1133      CCCCCC:      ; 3. CHECK VERTICAL INTERRUPT      ; .
0298 1134      CCCCCC:      ; 4. CHECK RED, BLUE, GREEN, AND INTENSIFY DOTS      ; .

```



```

03C5 1261 C POD14_7:
03C5 1262 C INC BX
03C6 1263 C JZ POD14_75
03C6 1264 C TEST AL,000001000B
03C8 1265 C JZ POD14_3
03C8 1266 C JZ POD14_COMPLETE
03CA 1267 C ;----- HAVE HAD COMPLETE VERTICAL-VERTICAL CYCLE
03CA 1268 C
03CC 1270 C POD14_75:
03CC 1271 C MOV AL,00
03CE 1272 C OUT TIM_CTL,AL
03D0 1273 C CMP BX,WORD PTR[BP][2]
03D0 1274 C
03D3 1275 C JE POD14_8
03D5 1276 C MOV BL,05H
03D7 1277 C JMP SHORT POD14_ERR
03D9 1278 C
03D9 1279 C POD14_8:
03D9 1280 C IN AL,TIMERO
03D9 1281 C MOV AH,AL
03D9 1282 C NOP
03D9 1283 C IN AL,TIMERO
03D9 1284 C XCHG AH,AL
03D9 1285 C NOP
03D9 1286 C CMP AX,WORD PTR[BP][4]
03E7 1287 C JGE POD14_9
03E9 1288 C MOV BL,06H
03E9 1289 C JMP SHORT POD14_ERR
03ED 1290 C
03ED 1291 C CMP AX,WORD PTR[BP][6]
03F0 1292 C JLE POD14_10
03F2 1293 C MOV BL,07H
03F4 1294 C JMP SHORT POD14_ERR
03F4 1295 C
03F4 1296 C ;----- SEE IF RED, GREEN, BLUE AND INTENSIFY DOTS WORK
03F4 1297 C
03F6 1298 C ;----- FIRST, SET A LINE OF REVERSE VIDEO, INTENSIFIED BLANKS INTO BUFFER
03F6 1299 C POD14_10:
03F6 1300 C MOV AX,09DBH
03F9 1301 C MOV BX,000FFH
03F9 1302 C
03FC 1303 C MOV CX,80
03FF 1304 C INT 10H
0401 1305 C IN AL,DX
0402 1306 C PUSH DX
0403 1307 C MOV AH,0FH
0405 1308 C MOV DL,ATTR_WRITE
0407 1309 C CALL OUT_DK
0409 1310 C MOV AX,0FH
0409 1311 C POP DX
0409 1312 C POP CX
0410 1313 C
0410 1314 C PUSH AX
0410 1315 C PUSH DX
0412 1316 C MOV AH,0FH
0414 1317 C MOV AH,32H
0416 1318 C CALL OUT_DK
0419 1319 C POP DX
0419 1320 C POP AX
0418 1321 C SUB CX,CX
0418 1322 C
0418 1323 C ;----- SEE IF DOT COMES ON
0418 1324 C IN AL,DX
0418 1325 C TEST AL,00110000B
0420 1326 C JNZ POD14_15
0422 1327 C LOOP POD14_14
0424 1328 C MOV BL,10H
0426 1329 C OR BL,AH
0428 1330 C JMP POD14_ERR
0428 1331 C
0428 1332 C ;----- SEE IF DOT GOES OFF
0428 1333 C SUB CX,CX
0428 1334 C
0420 1335 C IN AL,DX
0424 1336 C TEST AL,00110000B
0430 1337 C JE POD14_17
0432 1338 C LOOP POD14_16
0438 1339 C
0434 1340 C MOV BL,20H
0436 1341 C OR BL,AH
0438 1342 C JMP SHORT POD14_ERR
0438 1343 C
0438 1344 C ;----- ADJUST TO POINT TO NEXT DOT
0438 1345 C
0434 1346 C POD14_17:
0434 1347 C INC AH
0434 1348 C CMP AH,030H
0434 1349 C JE POD14_18
0441 1350 C OR AH,0FH
0444 1351 C MOV AL,AH
0446 1352 C JMP POD14_13
0448 1353 C
0448 1354 C MOV CX,6
0448 1355 C MOV DX,0103H
0448 1356 C CALL OUT_DK
0451 1357 C ADD SP,0AH
0454 1358 C MOV AL,00110110B
0456 1359 C OUT TIM_CTL,AL
0458 1360 C SUB AL,AL
0459 1361 C OUT TIMERO,AL
045C 1362 C NOP
045D 1363 C NOP
045E 1364 C OUT TIMERO,AL
0460 1365 C MOV BP,0
0463 1366 C JMP SK1P
0463 1367 C ASSUME DS:ABSO
0466 1368 C
0465 1369 C POD14_18:
0465 1370 C CALL DS
0465 1371 C MOV AX,0500H
0466 1372 C INT 10H
0466 1373 C MOV AL,00110110B
0471 1374 C OUT TIM_CTL,AL
0474 1375 C SUB AL,AL
0476 1376 C OUT TIMERO,AL
0477 1377 C NOP
0478 1378 C OUT TIMERO,AL
0478 1379 C ADD SP,0AH
047D 1380 C MOV BP,0
0480 1381 C JMP ENDP
0480 1382 C
0480 1383 C ;----- TEST STORAGE
0480 1384 C
0480 1385 C MEM_TEST:
0480 1386 C PUSH DS

```

```

0481 E8 0C FFE R 1387 C CALL DDS
0482 F6 06 0487 R 02 1388 C ASSUM DS:ABSO
0483 7F 12 0487 R 02 1389 C TEST INFO_2
0484 80 00 0410 R 30 1390 C JZ D_COLOR_M
0485 80 00 0410 R 30 1391 C OR ED10H,030H
0490 88 000F 1392 C MOV AX,0FH
0493 80 0E 0487 R 60 1393 C OR INFO_060H
0498 80 000F 1394 C MOV AX,0FH
0499 EB 0D 1395 C JMP SHORT D_OUT_M
049D 80 00 0000 1396 C
049D 80 26 0410 R CF 1397 C D_COLOR_M
04A2 80 0E 0410 R 20 1398 C AND EQUIP_L0W,0CFH
04A7 80 000E 1399 C OR EQUIP_L0W,020H
04A8 CD 10 1400 C MOV AX,0E0H
04AC 83 EC 06 1401 C D_OUT_M
04AF 8B EC 1402 C INT 10H
04B1 AB A000 1403 C SUB SP,6
04C4 80 02 0000 1404 C MOV BP,SP
04C4 80 02 0000 1405 C MOV AX,0A000H
04C4 80 02 0000 1406 C SUB DS,AX
04C4 80 02 0000 1407 C MOV ES,AX
04C4 80 02 0000 1408 C MOV WORD PTR [BP][2],0
04C4 80 02 0000 1409 C MOV WORD PTR [BP][4],0
04C4 B2 C4 1410 C MOV DL,3
04C4 B2 C4 1411 C MOV DL,SEQ_ADDR
04C6 B2 0201 1412 C MOV AX,0202H
04C9 80 0015 R 1413 C CALL OUT_DX
04C9 80 0015 R 1414 C MOV DL,GRAPH_ADDR
04CE B2 CE 1415 C MOV AX,04000H
04D1 E8 0015 R 1416 C CALL OUT_DX
04D4 52 1417 C PUSH DX
04D4 52 1418 C MOV DL,ATTR_READ
04D7 EC 1419 C IN AL,DX
04D8 B2 CO 1420 C MOV DL,ATTR_WRITE
04D8 B2 3200 1421 C MOV AX,3200H
04D8 B2 0000 1422 C CALL OUT_DX
04E0 88 0000 R 1423 C MOV DL,HOW_01G
04E3 B2 FC 00 1424 C CALL OUT_DX
04E6 74 03 1425 C CMP AH,0
04E8 E9 05CD R 1426 C JZ AA1
04E8 E9 05CD R 1427 C JMP EGA_MEM_ERROR
04EB E8 05D9 R 1428 C AA1:
04EE 88 FC 00 1429 C CALL MEMORY_OK
04F1 74 03 1430 C CMP AH,0
04F1 E8 0015 R 1431 C JZ AA2
04F6 5A 1432 C CALL EGA_MEM_ERROR
04F7 B2 C4 1433 C AA2:
04F7 B2 0202 1434 C POP DX
04F7 B2 0015 R 1435 C MOV DL,SEQ_ADDR
04F8 B2 CO 1436 C MOV AX,0202H
04F8 B2 3200 1437 C CALL OUT_DX
04F8 B2 0000 R 1438 C MOV DL,GRAPH_ADDR
0501 B2 0401 1439 C MOV AX,0401H
0504 E8 0015 R 1440 C CALL OUT_DX
0508 52 DA 1441 C PUSH DX
0508 52 DA 1442 C MOV DL,ATTR_READ
0508 EC 1443 C IN AL,DX
0508 B2 CO 1444 C MOV DL,ATTR_WRITE
0508 B2 3200 1445 C MOV AX,3200H
0508 B2 0000 R 1446 C CALL OUT_DX
0513 C7 46 04 0000 1447 C MOV WORD PTR [BP][4],0
0518 E8 068F R 1448 C CALL HOW_BIG
0518 88 FC 00 1449 C CMP AH,0
0518 74 03 1450 C JZ AA3
0520 E9 05CD R 1451 C JMP EGA_MEM_ERROR
0523 E8 05D9 R 1452 C AA3:
0526 88 FC 00 1453 C CALL MEMORY_OK
0529 74 03 1454 C CMP AH,0
0529 E9 05CD R 1455 C JZ AA4
052E 5A 1456 C CALL EGA_MEM_ERROR
052F B2 C4 1457 C AA4:
052F B2 0204 1458 C POP DX
0534 E8 0015 R 1459 C MOV DL,SEQ_ADDR
0537 52 1460 C CALL OUT_DX
0538 B2 CE 1461 C PUSH DX
0538 B2 0002 1462 C MOV DL,GRAPH_ADDR
0538 B2 0002 1463 C CALL OUT_DX
0538 E8 0015 R 1464 C MOV DL,ATTR_READ
0540 B2 DA 1465 C IN AL,DX
0542 EC 1466 C MOV DL,ATTR_WRITE
0542 B2 CO 1467 C MOV AX,3200H
0545 B2 3200 1468 C CALL OUT_DX
0548 E8 0015 R 1469 C MOV WORD PTR [BP][4],0
0550 E8 068F R 1470 C CALL HOW_BIG
0550 88 0000 R 1471 C CMP AH,0
0556 74 03 1472 C JZ AA5
0556 E8 73 90 1473 C JMP EGA_MEM_ERROR
0558 E8 05D9 R 1474 C AA5:
0558 88 FC 00 1475 C CALL MEMORY_OK
0561 74 03 1476 C CMP AH,0
0563 E8 6B 90 1477 C JZ AA6
0563 E8 6B 90 1478 C JMP EGA_MEM_ERROR
0567 5A 1479 C AA6:
0567 B2 C4 1480 C POP DX
0569 0208 1481 C MOV DL,SEQ_ADDR
0566 E8 0015 R 1483 C MOV AX,0208H
0566 E8 0015 R 1484 C CALL OUT_DX
0571 B6 0403 1485 C MOV DL,GRAPH_ADDR
0574 E8 0015 R 1486 C MOV AX,0403H
0577 52 1487 C CALL OUT_DX
0578 B2 DA 1488 C PUSH DX
0578 EC 1489 C MOV DL,ATTR_READ
0578 B2 CO 1490 C IN AL,DX
0578 B2 CO 1491 C MOV DL,ATTR_WRITE
0570 3200 1492 C MOV AX,3200H
0580 E8 0015 R 1493 C CALL OUT_DX
0580 C7 46 04 0000 1494 C MOV WORD PTR [BP][4],0
0586 E8 068F R 1495 C CALL HOW_BIG
0586 B2 FC 00 1496 C CMP AH,0
0586 75 3D 1497 C JNZ EGA_MEM_ERROR
0590 E8 05D9 R 1498 C CALL MEMORY_OK
0590 88 0000 R 1499 C CMP AH,0
0596 75 35 1500 C JNZ EGA_MEM_ERROR
0599 BD 0000 1501 C PUSH BP
0599 BD 0000 1502 C MOV BP,0
059C 5E 1503 C EGA_MEM_ERROR
059C 5E 1504 C INT 10H
059D 5A 1505 C POP SI
059E E8 0C FFE R 1506 C POP DX
059E E8 0C FFE R 1507 C CALL DDS
059E E8 0C FFE R 1508 C ASSUM DS:ABSO
059E E8 0C FFE R 1509 C MOV BX,WORD PTR SS:[SI][2]
059E E8 0C FFE R 1510 C MOV CL,DGH
059F D3 EB 1511 C SHR BX,CL
059F D3 EB 1512 C DEC BX
059F D3 EB 1513 C AND BX,0FH
05A1 36 8B 5C 02 1514 C JNZ EGA_MEM_ERROR
05A1 B1 06 1515 C CALL DDS
05A7 D3 EB 1516 C ASSUM DS:ABSO
05A9 B1 06 1517 C MOV BX,WORD PTR SS:[SI][2]
05A9 B1 06 1518 C MOV CL,DGH
05A9 B1 06 1519 C SHR BX,CL
05A9 B1 06 1520 C DEC BX
05A9 B1 06 1521 C AND BX,0FH
05A9 B1 06 1522 C JNZ EGA_MEM_ERROR
05A9 B1 06 1523 C CALL DDS
05A9 B1 06 1524 C ASSUM DS:ABSO
05A9 B1 06 1525 C MOV BX,WORD PTR SS:[SI][2]
05A9 B1 06 1526 C MOV CL,DGH
05A9 B1 06 1527 C SHR BX,CL
05A9 B1 06 1528 C DEC BX
05A9 B1 06 1529 C AND BX,0FH
05A9 B1 06 1530 C JNZ EGA_MEM_ERROR
05A9 B1 06 1531 C CALL DDS
05A9 B1 06 1532 C ASSUM DS:ABSO
05A9 B1 06 1533 C MOV BX,WORD PTR SS:[SI][2]
05A9 B1 06 1534 C MOV CL,DGH
05A9 B1 06 1535 C SHR BX,CL
05A9 B1 06 1536 C DEC BX
05A9 B1 06 1537 C AND BX,0FH
05A9 B1 06 1538 C JNZ EGA_MEM_ERROR
05A9 B1 06 1539 C CALL DDS
05A9 B1 06 1540 C ASSUM DS:ABSO
05A9 B1 06 1541 C MOV BX,WORD PTR SS:[SI][2]
05A9 B1 06 1542 C MOV CL,DGH
05A9 B1 06 1543 C SHR BX,CL
05A9 B1 06 1544 C DEC BX
05A9 B1 06 1545 C AND BX,0FH
05A9 B1 06 1546 C JNZ EGA_MEM_ERROR
05A9 B1 06 1547 C CALL DDS
05A9 B1 06 1548 C ASSUM DS:ABSO
05A9 B1 06 1549 C MOV BX,WORD PTR SS:[SI][2]
05A9 B1 06 1550 C MOV CL,DGH
05A9 B1 06 1551 C SHR BX,CL
05A9 B1 06 1552 C DEC BX
05A9 B1 06 1553 C AND BX,0FH
05A9 B1 06 1554 C JNZ EGA_MEM_ERROR
05A9 B1 06 1555 C CALL DDS
05A9 B1 06 1556 C ASSUM DS:ABSO
05A9 B1 06 1557 C MOV BX,WORD PTR SS:[SI][2]
05A9 B1 06 1558 C MOV CL,DGH
05A9 B1 06 1559 C SHR BX,CL
05A9 B1 06 1560 C DEC BX
05A9 B1 06 1561 C AND BX,0FH
05A9 B1 06 1562 C JNZ EGA_MEM_ERROR
05A9 B1 06 1563 C CALL DDS
05A9 B1 06 1564 C ASSUM DS:ABSO
05A9 B1 06 1565 C MOV BX,WORD PTR SS:[SI][2]
05A9 B1 06 1566 C MOV CL,DGH
05A9 B1 06 1567 C SHR BX,CL
05A9 B1 06 1568 C DEC BX
05A9 B1 06 1569 C AND BX,0FH
05A9 B1 06 1570 C JNZ EGA_MEM_ERROR
05A9 B1 06 1571 C CALL DDS
05A9 B1 06 1572 C ASSUM DS:ABSO
05A9 B1 06 1573 C MOV BX,WORD PTR SS:[SI][2]
05A9 B1 06 1574 C MOV CL,DGH
05A9 B1 06 1575 C SHR BX,CL
05A9 B1 06 1576 C DEC BX
05A9 B1 06 1577 C AND BX,0FH
05A9 B1 06 1578 C JNZ EGA_MEM_ERROR
05A9 B1 06 1579 C CALL DDS
05A9 B1 06 1580 C ASSUM DS:ABSO
05A9 B1 06 1581 C MOV BX,WORD PTR SS:[SI][2]
05A9 B1 06 1582 C MOV CL,DGH
05A9 B1 06 1583 C SHR BX,CL
05A9 B1 06 1584 C DEC BX
05A9 B1 06 1585 C AND BX,0FH
05A9 B1 06 1586 C JNZ EGA_MEM_ERROR
05A9 B1 06 1587 C CALL DDS
05A9 B1 06 1588 C ASSUM DS:ABSO
05A9 B1 06 1589 C MOV BX,WORD PTR SS:[SI][2]
05A9 B1 06 1590 C MOV CL,DGH
05A9 B1 06 1591 C SHR BX,CL
05A9 B1 06 1592 C DEC BX
05A9 B1 06 1593 C AND BX,0FH
05A9 B1 06 1594 C JNZ EGA_MEM_ERROR
05A9 B1 06 1595 C CALL DDS
05A9 B1 06 1596 C ASSUM DS:ABSO
05A9 B1 06 1597 C MOV BX,WORD PTR SS:[SI][2]
05A9 B1 06 1598 C MOV CL,DGH
05A9 B1 06 1599 C SHR BX,CL
05A9 B1 06 1600 C DEC BX
05A9 B1 06 1601 C AND BX,0FH
05A9 B1 06 1602 C JNZ EGA_MEM_ERROR
05A9 B1 06 1603 C CALL DDS
05A9 B1 06 1604 C ASSUM DS:ABSO
05A9 B1 06 1605 C MOV BX,WORD PTR SS:[SI][2]
05A9 B1 06 1606 C MOV CL,DGH
05A9 B1 06 1607 C SHR BX,CL
05A9 B1 06 1608 C DEC BX
05A9 B1 06 1609 C AND BX,0FH
05A9 B1 06 1610 C JNZ EGA_MEM_ERROR
05A9 B1 06 1611 C CALL DDS
05A9 B1 06 1612 C ASSUM DS:ABSO
05A9 B1 06 1613 C MOV BX,WORD PTR SS:[SI][2]
05A9 B1 06 1614 C MOV CL,DGH
05A9 B1 06 1615 C SHR BX,CL
05A9 B1 06 1616 C DEC BX
05A9 B1 06 1617 C AND BX,0FH
05A9 B1 06 1618 C JNZ EGA_MEM_ERROR
05A9 B1 06 1619 C CALL DDS
05A9 B1 06 1620 C ASSUM DS:ABSO
05A9 B1 06 1621 C MOV BX,WORD PTR SS:[SI][2]
05A9 B1 06 1622 C MOV CL,DGH
05A9 B1 06 1623 C SHR BX,CL
05A9 B1 06 1624 C DEC BX
05A9 B1 06 1625 C AND BX,0FH
05A9 B1 06 1626 C JNZ EGA_MEM_ERROR
05A9 B1 06 1627 C CALL DDS
05A9 B1 06 1628 C ASSUM DS:ABSO
05A9 B1 06 1629 C MOV BX,WORD PTR SS:[SI][2]
05A9 B1 06 1630 C MOV CL,DGH
05A9 B1 06 1631 C SHR BX,CL
05A9 B1 06 1632 C DEC BX
05A9 B1 06 1633 C AND BX,0FH
05A9 B1 06 1634 C JNZ EGA_MEM_ERROR
05A9 B1 06 1635 C CALL DDS
05A9 B1 06 1636 C ASSUM DS:ABSO
05A9 B1 06 1637 C MOV BX,WORD PTR SS:[SI][2]
05A9 B1 06 1638 C MOV CL,DGH
05A9 B1 06 1639 C SHR BX,CL
05A9 B1 06 1640 C DEC BX
05A9 B1 06 1641 C AND BX,0FH
05A9 B1 06 1642 C JNZ EGA_MEM_ERROR
05A9 B1 06 1643 C CALL DDS
05A9 B1 06 1644 C ASSUM DS:ABSO
05A9 B1 06 1645 C MOV BX,WORD PTR SS:[SI][2]
05A9 B1 06 1646 C MOV CL,DGH
05A9 B1 06 1647 C SHR BX,CL
05A9 B1 06 1648 C DEC BX
05A9 B1 06 1649 C AND BX,0FH
05A9 B1 06 1650 C JNZ EGA_MEM_ERROR
05A9 B1 06 1651 C CALL DDS
05A9 B1 06 1652 C ASSUM DS:ABSO
05A9 B1 06 1653 C MOV BX,WORD PTR SS:[SI][2]
05A9 B1 06 1654 C MOV CL,DGH
05A9 B1 06 1655 C SHR BX,CL
05A9 B1 06 1656 C DEC BX
05A9 B1 06 1657 C AND BX,0FH
05A9 B1 06 1658 C JNZ EGA_MEM_ERROR
05A9 B1 06 1659 C CALL DDS
05A9 B1 06 1660 C ASSUM DS:ABSO
05A9 B1 06 1661 C MOV BX,WORD PTR SS:[SI][2]
05A9 B1 06 1662 C MOV CL,DGH
05A9 B1 06 1663 C SHR BX,CL
05A9 B1 06 1664 C DEC BX
05A9 B1 06 1665 C AND BX,0FH
05A9 B1 06 1666 C JNZ EGA_MEM_ERROR
05A9 B1 06 1667 C CALL DDS
05A9 B1 06 1668 C ASSUM DS:ABSO
05A9 B1 06 1669 C MOV BX,WORD PTR SS:[SI][2]
05A9 B1 06 1670 C MOV CL,DGH
05A9 B1 06 1671 C SHR BX,CL
05A9 B1 06 1672 C DEC BX
05A9 B1 06 1673 C AND BX,0FH
05A9 B1 06 1674 C JNZ EGA_MEM_ERROR
05A9 B1 06 1675 C CALL DDS
05A9 B1 06 1676 C ASSUM DS:ABSO
05A9 B1 06 1677 C MOV BX,WORD PTR SS:[SI][2]
05A9 B1 06 1678 C MOV CL,DGH
05A9 B1 06 1679 C SHR BX,CL
05A9 B1 06 1680 C DEC BX
05A9 B1 06 1681 C AND BX,0FH
05A9 B1 06 1682 C JNZ EGA_MEM_ERROR
05A9 B1 06 1683 C CALL DDS
05A9 B1 06 1684 C ASSUM DS:ABSO
05A9 B1 06 1685 C MOV BX,WORD PTR SS:[SI][2]
05A9 B1 06 1686 C MOV CL,DGH
05A9 B1 06 1687 C SHR BX,CL
05A9 B1 06 1688 C DEC BX
05A9 B1 06 1689 C AND BX,0FH
05A9 B1 06 1690 C JNZ EGA_MEM_ERROR
05A9 B1 06 1691 C CALL DDS
05A9 B1 06 1692 C ASSUM DS:ABSO
05A9 B1 06 1693 C MOV BX,WORD PTR SS:[SI][2]
05A9 B1 06 1694 C MOV CL,DGH
05A9 B1 06 1695 C SHR BX,CL
05A9 B1 06 1696 C DEC BX
05A9 B1 06 1697 C AND BX,0FH
05A9 B1 06 1698 C JNZ EGA_MEM_ERROR
05A9 B1 06 1699 C CALL DDS
05A9 B1 06 1700 C ASSUM DS:ABSO
05A9 B1 06 1701 C MOV BX,WORD PTR SS:[SI][2]
05A9 B1 06 1702 C MOV CL,DGH
05A9 B1 06 1703 C SHR BX,CL
05A9 B1 06 1704 C DEC BX
05A9 B1 06 1705 C AND BX,0FH
05A9 B1 06 1706 C JNZ EGA_MEM_ERROR
05A9 B1 06 1707 C CALL DDS
05A9 B1 06 1708 C ASSUM DS:ABSO
05A9 B1 06 1709 C MOV BX,WORD PTR SS:[SI][2]
05A9 B1 06 1710 C MOV CL,DGH
05A9 B1 06 1711 C SHR BX,CL
05A9 B1 06 1712 C DEC BX
05A9 B1 06 1713 C AND BX,0FH
05A9 B1 06 1714 C JNZ EGA_MEM_ERROR
05A9 B1 06 1715 C CALL DDS
05A9 B1 06 1716 C ASSUM DS:ABSO
05A9 B1 06 1717 C MOV BX,WORD PTR SS:[SI][2]
05A9 B1 06 1718 C MOV CL,DGH
05A9 B1 06 1719 C SHR BX,CL
05A9 B1 06 1720 C DEC BX
05A9 B1 06 1721 C AND BX,0FH
05A9 B1 06 1722 C JNZ EGA_MEM_ERROR
05A9 B1 06 1723 C CALL DDS
05A9 B1 06 1724 C ASSUM DS:ABSO
05A9 B1 06 1725 C MOV BX,WORD PTR SS:[SI][2]
05A9 B1 06 1726 C MOV CL,DGH
05A9 B1 06 1727 C SHR BX,CL
05A9 B1 06 1728 C DEC BX
05A9 B1 06 1729 C AND BX,0FH
05A9 B1 06 1730 C JNZ EGA_MEM_ERROR
05A9 B1 06 1731 C CALL DDS
05A9 B1 06 1732 C ASSUM DS:ABSO
05A9 B1 06 1733 C MOV BX,WORD PTR SS:[SI][2]
05A9 B1 06 1734 C MOV CL,DGH
05A9 B1 06 1735 C SHR BX,CL
05A9 B1 06 1736 C DEC BX
05A9 B1 06 1737 C AND BX,0FH
05A9 B1 06 1738 C JNZ EGA_MEM_ERROR
05A9 B1 06 1739 C CALL DDS
05A9 B1 06 1740 C ASSUM DS:ABSO
05A9 B1 06 1741 C MOV BX,WORD PTR SS:[SI][2]
05A9 B1 06 1742 C MOV CL,DGH
05A9 B1 06 1743 C SHR BX,CL
05A9 B1 06 1744 C DEC BX
05A9 B1 06 1745 C AND BX,0FH
05A9 B1 06 1746 C JNZ EGA_MEM_ERROR
05A9 B1 06 1747 C CALL DDS
05A9 B1 06 1748 C ASSUM DS:ABSO
05A9 B1 06 1749 C MOV BX,WORD PTR SS:[SI][2]
05A9 B1 06 1750 C MOV CL,DGH
05A9 B1 06 1751 C SHR BX,CL
05A9 B1 06 1752 C DEC BX
05A9 B1 06 1753 C AND BX,0FH
05A9 B1 06 1754 C JNZ EGA_MEM_ERROR
05A9 B1 06 1755 C CALL DDS
05A9 B1 06 1756 C ASSUM DS:ABSO
05A9 B1 06 1757 C MOV BX,WORD PTR SS:[SI][2]
05A9 B1 06 1758 C MOV CL,DGH
05A9 B1 06 1759 C SHR BX,CL
05A9 B1 06 1760 C DEC BX
05A9 B1 06 1761 C AND BX,0FH
05A9 B1 06 1762 C JNZ EGA_MEM_ERROR
05A9 B1 06 1763 C CALL DDS
05A9 B1 06 1764 C ASSUM DS:ABSO
05A9 B1 06 1765 C MOV BX,WORD PTR SS:[SI][2]
05A9 B1 06 1766 C MOV CL,DGH
05A9 B1 06 1767 C SHR BX,CL
05A9 B1 06 1768 C DEC BX
05A9 B1 06 1769 C AND BX,0FH
05A9 B1 06 1770 C JNZ EGA_MEM_ERROR
05A9 B1 06 1771 C CALL DDS
05A9 B1 06 1772 C ASSUM DS:ABSO
05A9 B1 06 1773 C MOV BX,WORD PTR SS:[SI][2]
05A9 B1 06 1774 C MOV CL,DGH
05A9 B1 06 1775 C SHR BX,CL
05A9 B1 06 1776 C DEC BX
05A9 B1 06 1777 C AND BX,0FH
05A9 B1 06 1778 C JNZ EGA_MEM_ERROR
05A9 B1 06 1779 C CALL DDS
05A9 B1 06 1780 C ASSUM DS:ABSO
05A9 B1 06 1781 C MOV BX,WORD PTR SS:[SI][2]
05A9 B1 06 1782 C MOV CL,DGH
05A9 B1 06 1783 C SHR BX,CL
05A9 B1 06 1784 C DEC BX
05A9 B1 06 1785 C AND BX,0FH
05A9 B1 06 1786 C JNZ EGA_MEM_ERROR
05A9 B1 06 1787 C CALL DDS
05A9 B1 06 1788 C ASSUM DS:ABSO
05A9 B1 06 1789 C MOV BX,WORD PTR SS:[SI][2]
05A9 B1 06 1790 C MOV CL,DGH
05A9 B1 06 1791 C SHR BX,CL
05A9 B1 06 1792 C DEC BX
05A9 B1 06 1793 C AND BX,0FH
05A9 B1 06 1794 C JNZ EGA_MEM_ERROR
05A9 B1 06 1795 C CALL DDS
05A9 B1 06 1796 C ASSUM DS:ABSO
05A9 B1 06 1797 C MOV BX,WORD PTR SS:[SI][2]
05A9 B1 06 1798 C MOV CL,DGH
05A9 B1 06 1799 C SHR BX,CL
05A9 B1 06 1800 C DEC BX
05A9 B1 06 1801 C AND BX,0FH
05A9 B1 06 1802 C JNZ EGA_MEM_ERROR
05A9 B1 06 1803 C CALL DDS
05A9 B1 06 1804 C ASSUM DS:ABSO
05A9 B1 06 1805 C MOV BX,WORD PTR SS:[SI][2]
05A9 B1 06 1806 C MOV CL,DGH
05A9 B1 06 1807 C SHR BX,CL
05A9 B1 06 1808 C DEC BX
05A9 B1 06 1809 C AND BX,0FH
05A9 B1 06 1810 C JNZ EGA_MEM_ERROR
05A9 B1 06 1811 C CALL DDS
05A9 B1 06 1812 C ASSUM DS:ABSO
05A9 B1 06 1813 C MOV BX,WORD PTR SS:[SI][2]
05A9 B1 06 1814 C MOV CL,DGH
05A9 B1 06 1815 C SHR BX,CL
05A9 B1 06 1816 C DEC BX
05A9 B1 06 1817 C AND BX,0FH
05A9 B1 06 1818 C JNZ EGA_MEM_ERROR
05A9 B1 06 1819 C CALL DDS
05A9 B1 06 1820 C ASSUM DS:ABSO
05A9 B1 06 1821 C MOV BX,WORD PTR SS:[SI][2]
05A9 B1 06 1822 C MOV CL,DGH
05A9 B1 06 1823 C SHR BX,CL
05A9 B1 06 1824 C DEC BX
05A9 B1 06 1825 C AND BX,0FH
05A9 B1 06 1826 C JNZ EGA_MEM_ERROR
05A9 B1 06 1827 C CALL DDS
05A9 B1 06 1828 C ASSUM DS:ABSO
05A9 B1 06 1829 C MOV BX,WORD PTR SS:[SI][2]
05A9 B1 06 1830 C MOV CL,DGH
05A9 B1 06 1831 C SHR BX,CL
05A9 B1 06 1832 C DEC BX
05A9 B1 06 1833 C AND BX,0FH
05A9 B1 06 1834 C JNZ EGA_MEM_ERROR
05A9 B1 06 1835 C CALL DDS
05A9 B1 06 1836 C ASSUM DS:ABSO
05A9 B1 06 1837 C MOV BX,WORD PTR SS:[SI][2]
05A9 B1 06 1838 C MOV CL,DGH
05A9 B1 06 1839 C SHR BX,CL
05A9 B1 06 1840 C DEC BX
05A9 B1 06 1841 C AND BX,0FH
05A9 B1 06 1842 C JNZ EGA_MEM_ERROR
05A9 B1 06 1843 C CALL DDS
05A9 B1 06 1844 C ASSUM DS:ABSO
05A9 B1 06 1845 C MOV BX,WORD PTR SS:[SI][2]
05A9 B1 06 1846 C MOV CL,DGH
05A9 B1 06 1847 C SHR BX,CL
05A9 B1 06 1848 C DEC BX
05A9 B1 06 1849 C AND BX,0FH
05A9 B1 06 1850 C JNZ EGA_MEM_ERROR
05A9 B1 06 1851 C CALL DDS
05A9 B1 06 1852 C ASSUM DS:ABSO
05A9 B1 06 1853 C MOV BX,WORD PTR SS:[SI][2]
05A9 B1 06 1854 C MOV CL,DGH
05A9 B1 06 1855 C SHR BX,CL
05A9 B1 06 1856 C DEC BX
05A9 B1 06 1857 C AND BX,0FH
05A9 B1 06 1858 C JNZ EGA_MEM_ERROR
05A9 B1 06 1859 C CALL DDS
05A9 B1 06 1860 C ASSUM DS:ABSO
05A9 B1 06 1861 C MOV BX,WORD PTR SS:[SI][2]
05A9 B1 06 1862 C MOV CL,DGH
05A9 B1 06 1863 C SHR BX,CL
05A9 B1 06 1864 C DEC BX
05A9 B1 06 1865 C AND BX,0FH
05A9 B1 06 1866 C JNZ EGA_MEM_ERROR
05A9 B1 06 1867 C CALL DDS
05A9 B1 06 1868 C ASSUM DS:ABSO
05A9 B1 06 1869 C MOV BX,WORD PTR SS:[SI][2]
05A9 B1 06 1870 C MOV CL,DGH
05A9 B1 06 1871 C SHR BX,CL
05A9 B1 06 1872 C DEC BX
05A9 B1 06 1873 C AND BX,0FH
05A9 B1 06 1874 C JNZ EGA_MEM_ERROR
05A9 B1 06 1875 C CALL DDS
05A9 B1 06 1876 C ASSUM DS:ABSO
05A9 B1 06 1877 C MOV BX,WORD PTR SS:[SI][2]
05A9 B1 06 1878 C MOV CL,DGH
05A9 B1 06 1879 C SHR BX,CL
05A9 B1 06 1880 C DEC BX
05A9 B1 06 1881 C AND BX,0FH
05A9 B1 06 1882 C JNZ EGA_MEM_ERROR
05A9 B1 06 1883 C CALL DDS
05A9 B1 06 1884 C ASSUM DS:ABSO
05A9 B1 06 1885 C MOV BX,WORD PTR SS:[SI][2]
05A9 B1 06 1886 C MOV CL,DGH
05A9 B1 06 1887 C SHR BX,CL
05A9 B1 06 1888 C DEC BX
05A9 B1 06 1889 C AND BX,0FH
05A9 B1 06 1890 C JNZ EGA_MEM_ERROR
05A9 B1 06 1891 C CALL DDS
05A9 B1 06 1892 C ASSUM DS:ABSO
05A9 B1 06 1893 C MOV BX,WORD PTR SS:[SI][2]
05A9 B1 06 1894 C MOV CL,DGH
05A9 B1 06 1895 C SHR BX,CL
05A9 B1 06 1896 C DEC BX
05A9 B1 06 1897 C AND BX,0FH
05A9 B1 06 1898 C JNZ EGA_MEM_ERROR
05A9 B1 06 1899 C CALL DDS
05A9 B1 06 1900 C ASSUM DS:ABSO
05A9 B1 06 1901 C MOV BX,WORD PTR SS:[SI][2]
05A9 B1 06 1902 C MOV CL,DGH
05A9 B1 06 1903 C SHR BX,CL
05A9 B1 06 1904 C DEC BX
05A9 B1 06 1905 C AND BX,0FH
05A9 B1 06 1906 C JNZ EGA_MEM_ERROR
05A9 B1 06 1907 C CALL DDS
05A9 B1 06 1908 C ASSUM DS:ABSO
05A9 B1 06 1909 C MOV BX,WORD PTR SS:[SI][2]
05A9 B1 06 1910 C MOV CL,DGH
05A9 B1 06 1911 C SHR BX,CL
05A9 B1 06 1912 C DEC BX
05A9 B1 06 1913 C AND BX,0FH
05A9 B1 06 1914 C JNZ EGA_MEM_ERROR
05A9 B1 06 1915 C CALL DDS
05A9 B1 06 1916 C ASSUM DS:ABSO
05A9 B1 06 1917 C MOV BX,WORD PTR SS:[SI][2]
05A9 B1 06 1918 C MOV CL,DGH
05A9 B1 06 1919 C SHR BX,CL
05A9 B1 06 1920 C DEC BX
05A9 B1 06 1921 C AND BX,0FH
05A9 B1 06 1922 C JNZ EGA_MEM_ERROR
05A9 B1 06 1923 C CALL DDS
05A9 B1 06 1924 C ASSUM DS:ABSO
05A9 B1 06 1925 C MOV BX,WORD PTR SS:[SI][2]
05A9 B1 06 1926 C MOV CL,DGH
05A9 B1 06 1927 C SHR BX,CL
05A9 B1 06 1928 C DEC BX
05A9 B1 06 1929 C AND BX,0FH
05A9 B1 06 1930 C JNZ EGA_MEM_ERROR
05A9 B1 06 1931 C CALL DDS
05A9 B1 06 1932 C ASSUM DS:ABSO
05A9 B1 06 1933 C MOV BX,WORD PTR SS:[SI][2]
05A9 B1 06 1934 C MOV CL,DGH
05A9 B1 06 1935 C SHR BX,CL
05A9 B1 06 1936 C DEC BX
05A9 B1 06 1937 C AND BX,0FH
05A9 B1 06 1938 C JNZ EGA_MEM_ERROR
05A9 B1 06 1939 C CALL DDS
05A9 B1 06 1940 C ASSUM DS:ABSO
05A9 B1 06 1941 C MOV BX,WORD PTR SS:[SI][2]
05A9 B1 06 1942 C MOV CL,DGH
05A9 B1 06 1943 C SHR BX,CL
05A9 B1 06 1944 C DEC BX
05A9 B1 06 1945 C AND BX,0FH
05A9 B1 06 1946 C JNZ EGA_MEM_ERROR
05A9 B1 06 1947 C CALL DDS
05A9 B1 06 1948 C ASSUM DS:ABSO
05A9 B1 06 1949 C MOV BX,WORD PTR SS:[SI][2]
05A9 B1 06 1950 C MOV CL,DGH
05A9 B1 06 1951 C SHR BX,CL
05A9 B1 06 1952 C DEC BX
05A9 B1 06 1953 C AND BX,0FH
05A9 B1 06 1954 C JNZ EGA_MEM_ERROR
05A9 B1 06 1955 C CALL DDS
05A9 B1 06 1956 C ASSUM DS:ABSO
05A9 B1 06 1957 C MOV BX,WORD PTR SS:[SI][2]
05A9 B1 06 1958 C MOV CL,DGH
05A9 B1 06 1959 C SHR BX,CL
05A9 B1 06 1960 C DEC BX
05A9 B1 06 1961 C AND BX,0FH
05A9 B1 06 1962 C JNZ EGA_MEM_ERROR
05A9 B1 06 1963 C CALL DDS
05A9 B1 06 1964 C ASSUM DS:ABSO
05A9 B1 06 1965 C MOV BX,WORD PTR SS:[SI][2]
05A9 B1 06 1966 C MOV CL,DGH
05A9 B1 06 1967 C SHR BX,CL
05A9 B1 06 1968 C DEC BX
05A9 B1 06 1969 C AND BX,0FH
05
```

```

05AC D3 E3 1513 SHL BX, CL ; ISOLATE BITS 5 AND 6
05AC 88 E3 60 1514 AND BL, 01000000B
05B1 80 26 0487 R 9F 1515 AND INFO,10011111B
05B6 00 1E 0487 R 1516 OR INFO,BL
05B6 00 1E 0487 R 1517
05B6 00 1E 0487 R 1518 OR INFO,BL
05B6 00 1E 0487 R 1519 OR INFO,0000000100B ; 04H SET 3XX ACTIVE
05BF 8A 1E 0488 R 1520 MOV BL,INFO_3
05C3 E8 00F3 R 1521 CALL MUL_ENV
05C6 83 C4 06 1522 ADD SP,76 ; RESTORE STACK
05C9 1F 1523 POP DS
05C9 E9 0091 R 1524 JMP DS
05D0 BA 0103 1525 EGA_MEM_ERROR: ; SKIP
05D0 BA 0103 1526 MOV DX,0103H ; GO TO END
05D0 E8 06C8 R 1527 CALL ERR_BEEP ; ONE LONG AND THREE SHORT
05D3 55 1528 PUSH BP ; SAVE SCRATCH PAD POINTER
05D4 B8 0001 1529 MOV BP,1 ; INDICATE ERROR FOR XT
05D7 EB C3 1530 JMP EGA_MEM_EXIT ; ONE LONG AND THREE SHORT
05D7 EB C3 1531
05D7 EB C3 1532
05D7 EB C3 1533 ;----- THIS ROUTINE FINDS AMOUNT OF MEMORY GOOD
05D7 EB C3 1534
05D9 BB A000 1535 MEMORY_OK PROC NEAR ; SET PTR. TO BUFFER SEG
05D9 BB A000 1536 MOV BX,04000H ; SET SEC.REG.
05D9 BB A000 1537 MOV DS,BX
05D9 BB A000 1538 MOV ES,BX ; SET COUNT FOR 32K WORDS
05D9 BB A000 04 1539 MOV AX,WORD PTR[BP|4] ; SET LENGTH OF BUFFER
05D9 BB A000 1540 ADD AX,WORD PTR[BP|4] ; TO BE TESTED
05E5 2A C9 1541 SUB CL,CL ; MULTIPLY BY TWO
05E7 D1 E1 1542 SHL CX,1
05E7 E8 05FB R 1543 CALL PODSTG ; TEST FOR ERROR
05E7 E8 05FB R 1544 CMP AH,0 ; IF ERROR GO PRINT IT
05F1 79 09 1545 JNZ MEMORY_OK_ERR ;----- AMOUNT OF MEMORY FOUND
05F1 81 46 04 1546 MOV AX,WORD PTR[BP|4]
05F1 81 46 02 1547 ADD WORD PTR[BP|2],AX ;----- AMOUNT OF MEMORY GOOD
05F1 81 46 02 1548 MOV AX,0 ;----- AMOUNT OF MEMORY GOOD
05FA C3 1549
05FA C3 1550
05FB C3 1551
05FB C3 1552
05FB C3 1553
05FB C3 1554
05FB 55 1555 THIS ROUTINE PERFORMS A READ/WRITE TEST ON A BLOCK OF STORAGE ;----- MAX. SIZE = 32KM.
05FC FC 1556 (MAX. SIZE = 32KM). IF "WARM START", FILL BLOCK WITH 0000 AND ;----- RETAIN.
05FD 2B FF 1557
05FF 2B C0 1558
0601 E0 0CFF R 1559
0604 88 1E 0472 R 1560
0608 81 FB 1234 1561
0608 80 C2 1562
0608 8E DA 1563
0610 74 62 1564
0612 81 FB 4321 1565
0616 74 5C 1566
0618 88 05 1567
061A 8A 05 1568
061C 32 C4 1569
061E 7E 04 1570
0622 8A C4 1571
0624 75 F2 1572
0626 88 E9 1573
0628 88 A555 1574
0628 88 D8 1575
062D BA 55AA 1576
0630 F3 / AB 1577
0632 4F 1578
0633 4F 1579
0634 88 F7 1580
0637 88 CD 1581
0639 AD 1582
0639 33 C3 1583
063C 22 1584
063E BB C2 1585
0640 AB 1586
0641 E2 F6 1587
0645 FC 1588
0646 46 1589
0647 46 1590
0648 BB FE 1591
0649 46 1592
064A AD 1593
064A 33 C2 1594
064B 33 C2 1595
064D 75 11 1596
064F AB 1597
0650 E2 F8 1598
0652 FD 1599
0653 4E 1600
0654 4E 1601
0655 BB CD 1602
0656 4E 1603
0657 AD 1604
0658 80 C0 1605
0658 75 04 1606
065E EB F9 1607
0660 4E 1608
0661 4E 1609
0662 E2 F8 1610
0663 4E 1611
0664 4E 1612
0665 4E 1613
0666 4E 1614
0667 4E 1615
0668 4E 1616
0669 4E 1617
0670 4E 1618
0671 4E 1619
0672 4E 1620
0673 4E 1621
0674 4E 1622
0675 4E 1623
0676 4E 1624
0677 4E 1625
0678 4E 1626
0679 4E 1627
0680 4E 1628
0681 4E 1629
0682 4E 1630
0683 4E 1631
0684 4E 1632
0685 4E 1633
0686 4E 1634
0687 4E 1635
0688 4E 1636
0689 4E 1637
0690 4E 1638
0691 4E 1639
0692 4E 1640
0693 4E 1641
0694 4E 1642
0695 4E 1643
0696 4E 1644
0697 4E 1645
0698 4E 1646
0699 4E 1647
069A 4E 1648
069B 4E 1649
069C 4E 1650
069D 4E 1651
069E 4E 1652
069F 4E 1653
06A0 4E 1654
06A1 4E 1655
06A2 4E 1656
06A3 4E 1657
06A4 4E 1658
06A5 4E 1659
06A6 4E 1660
06A7 4E 1661
06A8 4E 1662
06A9 4E 1663
06A0 4E 1664
06A1 4E 1665
06A2 4E 1666
06A3 4E 1667
06A4 4E 1668
06A5 4E 1669
06A6 4E 1670
06A7 4E 1671
06A8 4E 1672
06A9 4E 1673
06A0 4E 1674
06A1 4E 1675
06A2 4E 1676
06A3 4E 1677
06A4 4E 1678
06A5 4E 1679
06A6 4E 1680
06A7 4E 1681
06A8 4E 1682
06A9 4E 1683
06A0 4E 1684
06A1 4E 1685
06A2 4E 1686
06A3 4E 1687
06A4 4E 1688
06A5 4E 1689
06A6 4E 1690
06A7 4E 1691
06A8 4E 1692
06A9 4E 1693
06A0 4E 1694
06A1 4E 1695
06A2 4E 1696
06A3 4E 1697
06A4 4E 1698
06A5 4E 1699
06A6 4E 169A
06A7 4E 169B
06A8 4E 169C
06A9 4E 169D
06A0 4E 169E
06A1 4E 169F
06A2 4E 169A
06A3 4E 169B
06A4 4E 169C
06A5 4E 169D
06A6 4E 169E
06A7 4E 169F
06A8 4E 169A
06A9 4E 169B
06A0 4E 169C
06A1 4E 169D
06A2 4E 169E
06A3 4E 169F
06A4 4E 169A
06A5 4E 169B
06A6 4E 169C
06A7 4E 169D
06A8 4E 169E
06A9 4E 169F
06A0 4E 169A
06A1 4E 169B
06A2 4E 169C
06A3 4E 169D
06A4 4E 169E
06A5 4E 169F
06A6 4E 169A
06A7 4E 169B
06A8 4E 169C
06A9 4E 169D
06A0 4E 169E
06A1 4E 169F
06A2 4E 169A
06A3 4E 169B
06A4 4E 169C
06A5 4E 169D
06A6 4E 169E
06A7 4E 169F
06A8 4E 169A
06A9 4E 169B
06A0 4E 169C
06A1 4E 169D
06A2 4E 169E
06A3 4E 169F
06A4 4E 169A
06A5 4E 169B
06A6 4E 169C
06A7 4E 169D
06A8 4E 169E
06A9 4E 169F
06A0 4E 169A
06A1 4E 169B
06A2 4E 169C
06A3 4E 169D
06A4 4E 169E
06A5 4E 169F
06A6 4E 169A
06A7 4E 169B
06A8 4E 169C
06A9 4E 169D
06A0 4E 169E
06A1 4E 169F
06A2 4E 169A
06A3 4E 169B
06A4 4E 169C
06A5 4E 169D
06A6 4E 169E
06A7 4E 169F
06A8 4E 169A
06A9 4E 169B
06A0 4E 169C
06A1 4E 169D
06A2 4E 169E
06A3 4E 169F
06A4 4E 169A
06A5 4E 169B
06A6 4E 169C
06A7 4E 169D
06A8 4E 169E
06A9 4E 169F
06A0 4E 169A
06A1 4E 169B
06A2 4E 169C
06A3 4E 169D
06A4 4E 169E
06A5 4E 169F
06A6 4E 169A
06A7 4E 169B
06A8 4E 169C
06A9 4E 169D
06A0 4E 169E
06A1 4E 169F
06A2 4E 169A
06A3 4E 169B
06A4 4E 169C
06A5 4E 169D
06A6 4E 169E
06A7 4E 169F
06A8 4E 169A
06A9 4E 169B
06A0 4E 169C
06A1 4E 169D
06A2 4E 169E
06A3 4E 169F
06A4 4E 169A
06A5 4E 169B
06A6 4E 169C
06A7 4E 169D
06A8 4E 169E
06A9 4E 169F
06A0 4E 169A
06A1 4E 169B
06A2 4E 169C
06A3 4E 169D
06A4 4E 169E
06A5 4E 169F
06A6 4E 169A
06A7 4E 169B
06A8 4E 169C
06A9 4E 169D
06A0 4E 169E
06A1 4E 169F
06A2 4E 169A
06A3 4E 169B
06A4 4E 169C
06A5 4E 169D
06A6 4E 169E
06A7 4E 169F
06A8 4E 169A
06A9 4E 169B
06A0 4E 169C
06A1 4E 169D
06A2 4E 169E
06A3 4E 169F
06A4 4E 169A
06A5 4E 169B
06A6 4E 169C
06A7 4E 169D
06A8 4E 169E
06A9 4E 169F
06A0 4E 169A
06A1 4E 169B
06A2 4E 169C
06A3 4E 169D
06A4 4E 169E
06A5 4E 169F
06A6 4E 169A
06A7 4E 169B
06A8 4E 169C
06A9 4E 169D
06A0 4E 169E
06A1 4E 169F
06A2 4E 169A
06A3 4E 169B
06A4 4E 169C
06A5 4E 169D
06A6 4E 169E
06A7 4E 169F
06A8 4E 169A
06A9 4E 169B
06A0 4E 169C
06A1 4E 169D
06A2 4E 169E
06A3 4E 169F
06A4 4E 169A
06A5 4E 169B
06A6 4E 169C
06A7 4E 169D
06A8 4E 169E
06A9 4E 169F
06A0 4E 169A
06A1 4E 169B
06A2 4E 169C
06A3 4E 169D
06A4 4E 169E
06A5 4E 169F
06A6 4E 169A
06A7 4E 169B
06A8 4E 169C
06A9 4E 169D
06A0 4E 169E
06A1 4E 169F
06A2 4E 169A
06A3 4E 169B
06A4 4E 169C
06A5 4E 169D
06A6 4E 169E
06A7 4E 169F
06A8 4E 169A
06A9 4E 169B
06A0 4E 169C
06A1 4E 169D
06A2 4E 169E
06A3 4E 169F
06A4 4E 169A
06A5 4E 169B
06A6 4E 169C
06A7 4E 169D
06A8 4E 169E
06A9 4E 169F
06A0 4E 169A
06A1 4E 169B
06A2 4E 169C
06A3 4E 169D
06A4 4E 169E
06A5 4E 169F
06A6 4E 169A
06A7 4E 169B
06A8 4E 169C
06A9 4E 169D
06A0 4E 169E
06A1 4E 169F
06A2 4E 169A
06A3 4E 169B
06A4 4E 169C
06A5 4E 169D
06A6 4E 169E
06A7 4E 169F
06A8 4E 169A
06A9 4E 169B
06A0 4E 169C
06A1 4E 169D
06A2 4E 169E
06A3 4E 169F
06A4 4E 169A
06A5 4E 169B
06A6 4E 169C
06A7 4E 169D
06A8 4E 169E
06A9 4E 169F
06A0 4E 169A
06A1 4E 169B
06A2 4E 169C
06A3 4E 169D
06A4 4E 169E
06A5 4E 169F
06A6 4E 169A
06A7 4E 169B
06A8 4E 169C
06A9 4E 169D
06A0 4E 169E
06A1 4E 169F
06A2 4E 169A
06A3 4E 169B
06A4 4E 169C
06A5 4E 169D
06A6 4E 169E
06A7 4E 169F
06A8 4E 169A
06A9 4E 169B
06A0 4E 169C
06A1 4E 169D
06A2 4E 169E
06A3 4E 169F
06A4 4E 169A
06A5 4E 169B
06A6 4E 169C
06A7 4E 169D
06A8 4E 169E
06A9 4E 169F
06A0 4E 169A
06A1 4E 169B
06A2 4E 169C
06A3 4E 169D
06A4 4E 169E
06A5 4E 169F
06A6 4E 169A
06A7 4E 169B
06A8 4E 169C
06A9 4E 169D
06A0 4E 169E
06A1 4E 169F
06A2 4E 169A
06A3 4E 169B
06A4 4E 169C
06A5 4E 169D
06A6 4E 169E
06A7 4E 169F
06A8 4E 169A
06A9 4E 169B
06A0 4E 169C
06A1 4E 169D
06A2 4E 169E
06A3 4E 169F
06A4 4E 169A
06A5 4E 169B
06A6 4E 169C
06A7 4E 169D
06A8 4E 169E
06A9 4E 169F
06A0 4E 169A
06A1 4E 169B
06A2 4E 169C
06A3 4E 169D
06A4 4E 169E
06A5 4E 169F
06A6 4E 169A
06A7 4E 169B
06A8 4E 169C
06A9 4E 169D
06A0 4E 169E
06A1 4E 169F
06A2 4E 169A
06A3 4E 169B
06A4 4E 169C
06A5 4E 169D
06A6 4E 169E
06A7 4E 169F
06A8 4E 169A
06A9 4E 169B
06A0 4E 169C
06A1 4E 169D
06A2 4E 169E
06A3 4E 169F
06A4 4E 169A
06A5 4E 169B
06A6 4E 169C
06A7 4E 169D
06A8 4E 169E
06A9 4E 169F
06A0 4E 169A
06A1 4E 169B
06A2 4E 169C
06A3 4E 169D
06A4 4E 169E
06A5 4E 169F
06A6 4E 169A
06A7 4E 169B
06A8 4E 169C
06A9 4E 169D
06A0 4E 169E
06A1 4E 169F
06A2 4E 169A
06A3 4E 169B
06A4 4E 169C
06A5 4E 169D
06A6 4E 169E
06A7 4E 169F
06A8 4E 169A
06A9 4E 169B
06A0 4E 169C
06A1 4E 169D
06A2 4E 169E
06A3 4E 169F
06A4 4E 169A
06A5 4E 169B
06A6 4E 169C
06A7 4E 169D
06A8 4E 169E
06A9 4E 169F
06A0 4E 169A
06A1 4E 169B
06A2 4E 169C
06A3 4E 169D
06A4 4E 169E
06A5 4E 169F
06A6 4E 169A
06A7 4E 169B
06A8 4E 169C
06A9 4E 169D
06A0 4E 169E
06A1 4E 169F
06A2 4E 169A
06A3 4E 169B
06A4 4E 169C
06A5 4E 169D
06A6 4E 169E
06A7 4E 169F
06A8 4E 169A
06A9 4E 169B
06A0 4E 169C
06A1 4E 169D
06A2 4E 169E
06A3 4E 169F
06A4 4E 169A
06A5 4E 169B
06A6 4E 169C
06A7 4E 169D
06A8 4E 169E
06A9 4E 169F
06A0 4E 169A
06A1 4E 169B
06A2 4E 169C
06A3 4E 169D
06A4 4E 169E
06A5 4E 169F
06A6 4E 169A
06A7 4E 169B
06A8 4E 169C
06A9 4E 169D
06A0 4E 169E
06A1 4E 169F
06A2 4E 169A
06A3 4E 169B
06A4 4E 169C
06A5 4E 169D
06A6 4E 169E
06A7 4E 169F
06A8 4E 169A
06A9 4E 169B
06A0 4E 169C
06A1 4E 169D
06A2 4E 169E
06A3 4E 169F
06A4 4E 169A
06A5 4E 169B
06A6 4E 169C
06A7 4E 169D
06A8 4E 169E
06A9 4E 169F
06A0 4E 169A
06A1 4E 169B
06A2 4E 169C
06A3 4E 169D
06A4 4E 169E
06A5 4E 169F
06A6 4E 169A
06A7 4E 169B
06A8 4E 169C
06A9 4E 169D
06A0 4E 169E
06A1 4E 169F
06A2 4E 169A
06A3 4E 169B
06A4 4E 169C
06A5 4E 169D
06A6 4E 169E
06A7 4E 169F
06A8 4E 169A
06A9 4E 169B
06A0 4E 169C
06A1 4E 169D
06A2 4E 169E
06A3 4E 169F
06A4 4E 169A
06A5 4E 169B
06A6 4E 169C
06A7 4E 169D
06A8 4E 169E
06A9 4E 169F
06A0 4E 169A
06A1 4E 169B
06A2 4E 169C
06A3 4E 169D
06A4 4E 169E
06A5 4E 169F
06A6 4E 169A
06A7 4E 169B
06A8 4E 169C
06A9 4E 169D
06A0 4E 169E
06A1 4E 169F
06A2 4E 169A
06A3 4E 169B
06A4 4E 169C
06A5 4E 169D
06A6 4E 169E
06A7 4E 169F
06A8 4E 169A
06A9 4E 169B
06A0 4E 169C
06A1 4E 169D
06A2 4E 169E
06A3 4E 169F
06A4 4E 169A
06A5 4E 169B
06A6 4E 169C
06A7 4E 169D
06A8 4E 169E
06A9 4E 169F
06A0 4E 169A
06A1 4E 169B
06A2 4E 169C
06A3 4E 169D
06A4 4E 169E
06A5 4E 169F
06A6 4E 169A
06A7 4E 169B
06A8 4E 169C
06A9 4E 169D
06A0 4E 169E
06A1 4E 169F
06A2 4E 169A
06A3 4E 169B
06A4 4E 169C
06A5 4E 169D
06A6 4E 169E
06A7 4E 169F
06A8 4E 169A
06A9 4E 169B
06A0 4E 169C
06A1 4E 169D
06A2 4E 169E
06A3 4E 169F
06A4 4E 169A
06A5 4E 169B
06A6 4E 169C
06A7 4E 169D
06A8 4E 169E
06A9 4E 169F
06A0 4E 169A
06A1 4E 169B
06A2 4E 169C
06A3 4E 169D
06A4 4E 169E
06A5 4E 169F
06A6 4E 169A
06A7 4E 169B
06A8 4E 169C
06A9 4E 169D
06A0 4E 169E
06A1 4E 169F
06A2 4E 169A
06A3 4E 169B
06A4 4E 169C
06A5 4E 169D
06A6 4E 169E
06A7 4E 169F
06A8 4E 169A
06A9 4E 169B
06A0 4E 169C
06A1 4E 169D
06A2 4E 169E
06A3 4E 169F
06A4 4E 169A
06A5 4E 169B
06A6 4E 169C
06A7 4E 169D
06A8 4E 169E
06A9 4E 169F
06A0 4E 169A
06A1 4E 169B
06A2 4E 169C
06A3 4E 169D
06A4 4E 169E
06A5 4E 169F
06A6 4E 169A
06A7 4E 169B
06A8 4E 169C
06A9 4E 169D
06A0 4E 169E
06A1 4E 169F
06A2 4E 169A
06A3 4E 169B
06A4 4E 169C
06A5 4E 169D
06A6 4E 169E
06A7 4E 169F
06A8 4E 169A
06A9 4E 169B
06A0 4E 169C
06A1 4E 169D
06A2 4E 169E
06A3 4E 169F
06A4 4E 169A
06A5 4E 169B
06A6 4E 169C
06A7 4E 169D
06A8 4E 169E
06A9 4E 169F
06A0 4E 169A
06A1 4E 169B
06A2 4E 169C
06A3 4E 169D
06A4 4E 169E
06A5 4E 169F
06A6 4E 169A
06A7 4E 169B
06A8 4E 169C
06A9 4E 169D
06A0 4E 169E
06A1 4E 169F
06A2 4E 169A
06A3 4E 169B
06A4 4E 169C
06A5 4E 169D
06A6 4E 169E
06A7 4E 169F
06A8 4E 169A
06A9 4E 169B
06A0 4E 169C
06A1 4E 169D
06A2 4E 169E
06A3 4E 169F
06A4 4E 169A
06A5 4E 169B
06A6 4E 169C
06A7 4E 169D
06A8 4E 169E
06A9 4E 169F
06A0 4E 169A
06A1 4E 169B
06A2 4E 169C
06A3 4E 169D
06A4 4E 169E
06A5 4E 169F
06A6 4E 169A
06A7 4E 169B
06A8 4E 169C
06A9 4E 169D
06A0 4E 169E
06A1 4E 169F
06A2 4E 169A
06A3 4E 169B
06A4 4E 169C
06A5 4E 169D
06A6 4E 169E
06A7 4E 169F
06A8 4E 169A
06A9 4E 169B
06A0 4E 169C
06A1 4E 169D
06A2 4E 169E
06A3 4E 169F
06A4 4E 169A
06A5 4E 169B
06A6 4E 169C
06A7 4E 169D
06A8 4E 169E
06A9 4E 169F
06A0 4E 169A
06A1 4E 169B
06A2 4E 169C
06A3 4E 169D
06A4 4E 169E
06A5 4E 169F
06A6 4E 169A
06A7 4E 169B
06A8 4E 169C
06A9 4E 169D
06A0 4E 169E
06A1 4E 169F
06A2 4E 169A
06A3 4E 169B
06A4 4E 169C
06A5 4E 169D
06A6 4E 169E
06A7 4E 169F
06A8 4E 169A
06A9 4E 169B
06A0 4E 169C
06A1 4E 169D
06A2 4E 169E
06A3 4E 169F
06A4 4E 169A
06A5 4E 169B
06A6 4E 169C
06A7 4E 169D
06A8 4E 169E
06A9 4E 169F
06A0 4E 169A
06A1 4E 169B
06A2 4E 169C
06A3 4E 169D
06A4 4E 169E
06A5 4E 169F
06A6 4E 169A
06A7 4E 169B
06A8 4E 169C
06A9 4E 169D
06A0 4E 169E
06A1 4E 169F
06A2 4E 169A
06A3 4E 169B
06A4 4E 169C
06A5 4E 169D
06A6 4E 169E
06A7 4E 169F
06A8 4E 169A
06A9 4E 169B
06A0 4E 169C
06A1 4E 169D
06A2 4E 169E
06A3 4E 169F
06A4 4E 169A
06A5 4E 169B
06A6 4E 169C
06A7 4E 169D
06A8 4E 169E
06A9 4E 169F
06A0 4E 169A
06A1 4E 169B
06A2 4E 169C
06A3 4E 169D
06A4 4E 169E
06A5 4E 169F
06A6 4E 169A
06A7 4E 169B
06A8 4E 169C
06A9 4E 169D
06A0 4E 169E
06A1 4E 169F
06A2 4E 169A
06A3 4E 169B
06A4 4E 169C
06A5 4E 169D
06A6 4E 169E
06A7 4E 169F
06A8 4E 169A
06A9 4E 169B
06A0 4E 169C
06A1 4E 169D
06A2 4E 169E
06A3 4E 169F
06A4 4E 169A
06A5 4E 169B
06A6 4E 169C
06A7 4E 169D
06A8 4E 169E
06A9 4E 169F
06A0 4E 169A
06A1 4E 169B
06A2 4E 169C
06A3 4E 169D
06A4 4E 169E
06A5 4E 169F
06
```



```

1765
1766 INCLUDE VPARMS.INC
1767 SUBTTL VPARMS.INC
1768 PAGE
1769
1770 VIDEO_PARMS LABEL BYTE
1771 ; STRUCTURE OF THIS TABLE
1772
1773 COLUMNS, ROWS, PELS PER CHARACTER
1774 PAGE LENGTH
1775 SUBSEQUENT PARAMETERS
1776 MISCELLANEOUS REGISTER
1777 CRTC PARAMETERS
1778 ATTRIBUTE PARAMETERS
1779 GRAPHICS PARAMETERS
1780
1781 C BASE_1 EQU $ - VIDEO_PARMS
1782 C BASE_1_L LABEL BYTE
1783
1784 ;---- DEFAULT MODES
1785
1786 C---0-- DB 40D,24D,08D
1787 C---0-- DW 00800H
1788
1789 C TFS_LEN EQU $ - BASE_1_L
1790
1791 C SEQ_PARAMS LABEL BYTE
1792 C DB 00B,003H,000H,003H
1793 M1 EQU $ - SEQ_PARAMS
1794
1795 C DB 023H
1796
1797 C CRT_PARAMS LABEL BYTE
1798 C DB 037H,027H,02D0,037H,031H,015H
1799 C DB 004H,011H,000H,007H,006H,007H
1800 C DB 000H,000H,000H,000H,001H,024H
1801 C DB 00FH,014H,008H,0E0H,0F0H,0A3H
1802 C DB 01FH,000H
1803
1804 M4 EQU S-CRT_PARAMS
1805
1806 C LN_4 EQU $ - BASE_1_L
1807
1808 C ATTR_PARAMS LABEL BYTE
1809 C DB 000H,001H,002H,003H,004H,005H
1810 C DB 006H,007H,010H,011H,012H,013H
1811 C DB 000H,001H,016H,017H,008H,000H
1812 C DB 00FH,000H
1813
1814 C M5 EQU S-ATTR_PARAMS
1815
1816 C LN_2 EQU $ - BASE_1_L
1817 C GRAPH_PARAMS LABEL BYTE
1818 C DB 000H,000H,000H,000H,000H,010H
1819 C DB 00EH,000H,0FFH
1820
1821 C M_TBL_LEN EQU $ - BASE_1_L
1822
1823 C---1-- DB 40D,24D,08D
1824 C---0-- DW 00800H
1825
1826 C DB 008H,003H,000H,003H
1827 C DB 023H
1828
1829 C DB 000H,000H,000H,000H,001H,010H
1830
1831 C DB 037H,027H,02D0,037H,031H,015H
1832 C DB 004H,011H,000H,007H,006H,007H
1833 C DB 000H,000H,000H,000H,001H,024H
1834 C DB 00FH,014H,008H,0E0H,0F0H,0A3H
1835 C DB 0FFH,000H
1836
1837 C DB 000H,001H,002H,003H,004H,005H
1838 C DB 006H,007H,010H,011H,012H,013H
1839 C DB 014H,015H,016H,017H,008H,000H
1840 C DB 00FH,000H
1841
1842 C DB 000H,000H,000H,000H,001H,010H
1843 C DB 00EH,000H,0FFH
1844
1845 C---2-- DB 80D,24D,08D
1846 C---0-- DW 01000H
1847
1848 C DB 001H,003H,000H,003H
1849
1850 C DB 023H
1851
1852 C DB 070H,04FH,05CH,02FH,05FH,007H
1853 C DB 004H,011H,000H,007H,006H,007H
1854 C DB 000H,000H,000H,000H,001H,024H
1855 C DB 00FH,028H,008H,0E0H,0F0H,0A3H
1856 C DB 0FFH,000H
1857
1858 C DB 000H,001H,002H,003H,004H,005H
1859 C DB 004H,007H,010H,011H,012H,013H
1860 C DB 014H,015H,016H,017H,008H,000H
1861 C DB 00FH,000H
1862
1863 C DB 000H,000H,000H,000H,001H,010H
1864 C DB 00EH,000H,0FFH
1865
1866 C---3-- DB 80D,24D,08D
1867 C---0-- DW 01000H
1868
1869 C DB 001H,003H,000H,003H
1870
1871 C DB 023H
1872
1873 C DB 070H,04FH,05CH,02FH,05FH,007H
1874 C DB 004H,011H,000H,007H,006H,007H
1875 C DB 000H,000H,000H,000H,001H,024H
1876 C DB 00FH,028H,008H,0E0H,0F0H,0A3H
1877 C DB 0FFH,000H
1878
1879 C DB 000H,001H,002H,003H,004H,005H
1880 C DB 004H,007H,010H,011H,012H,013H
1881 C DB 014H,015H,016H,017H,008H,000H
1882 C DB 00FH,000H
1883
1884 C DB 000H,000H,000H,000H,001H,010H
1885 C DB 00EH,000H,0FFH
1886
1887 C DB 000H,000H,000H,000H,001H,010H
1888 C DB 00EH,000H,0FFH
1889
1890 C---4-- DB 40D,24D,08D

```

081A 4000	1891	C	DW	04000H
081C 08 03 00 02	1892	C	DB	00BH, 003H, 000H, 002H
0820 23	1893	C	DB	023H
0821 37 27 20 37 30 14	1894	C	DB	037H, 027H, 020H, 037H, 030H, 014H
0827 04 11 00 01 00 00	1895	C	DB	004H, 011H, 000H, 001H, 000H, 000H
0820 00 00 00 00 E1 24	1896	C	DB	000H, 000H, 000H, 000H, 0E1H, 024H
0833 C7 14 08 EO FO A2	1897	C	DB	0C7H, 014H, 000H, 0E0H, 0FOH, 0A2H
0839 FF	1898	C	DB	0FFH
	1899	C	DB	
	1900	C	DB	
	1901	C	DB	
	1902	C	DB	
	1903	C	DB	000H, 013H, 015H, 017H, 002H, 004H
	1904	C	DB	006H, 007H, 010H, 011H, 012H, 013H
	1905	C	DB	014H, 015H, 016H, 017H, 001H, 000H
084C 03 00	1906	C	DB	003H, 000H
	1907	C	DB	
	1908	C	DB	000H, 000H, 000H, 000H, 000H, 030H
0854 0F 00 FF	1909	C	DB	00FH, 000H, 0FFH
	1910	C	DB	
0857 28 18 08	1911	C	DB	;--5--
0854 4000	1912	C	DB	40D, 24D, 0BD
	1913	C	DW	04000H
085C 08 03 00 02	1914	C	DB	00BH, 003H, 000H, 002H
0860 23	1915	C	DB	023H
0861 37 27 20 37 30 14	1916	C	DB	037H, 027H, 020H, 037H, 030H, 014H
0867 04 11 00 01 00 00	1917	C	DB	004H, 011H, 000H, 001H, 000H, 000H
0860 00 00 00 00 E1 24	1918	C	DB	000H, 000H, 000H, 0E1H, 024H
0873 C7 14 08 EO FO A2	1919	C	DB	0C7H, 014H, 000H, 0E0H, 0FOH, 0A2H
0879 FF	1920	C	DB	0FFH
	1921	C	DB	
	1922	C	DB	
	1923	C	DB	
087A 00 13 15 17 02 04	1924	C	DB	
0880 06 07 10 11 12 13	1925	C	DB	000H, 013H, 015H, 017H, 002H, 004H
0886 14 15 16 17 01 00	1926	C	DB	006H, 007H, 010H, 011H, 012H, 013H
088C 03 00	1927	C	DB	014H, 015H, 016H, 017H, 001H, 000H
088E 00 00 00 00 00 30	1928	C	DB	003H, 000H
0894 0F 00 FF	1929	C	DB	
	1930	C	DB	000H, 000H, 000H, 000H, 000H, 030H
	1931	C	DB	00FH, 000H, 0FFH
0897 50 18 08	1932	C	DB	;--6--
0894 4000	1933	C	DB	80D, 24D, 0BD
	1934	C	DW	04000H
089C 01 01 00 06	1935	C	DB	001H, 001H, 000H, 006H
08A0 23	1936	C	DB	023H
08A1 70 4F 59 2D 5E 06	1937	C	DB	070H, 04FH, 059H, 020H, 05EH, 006H
08A7 04 11 00 01 00 00	1938	C	DB	004H, 011H, 000H, 001H, 000H, 000H
08AD 00 00 00 00 E0 23	1939	C	DB	000H, 000H, 000H, 000H, 0E0H, 023H
08B3 C7 28 00 QF EF C2	1940	C	DB	0C7H, 028H, 000H, 0DFH, 0EFH, 0C2H
08B9 FF	1941	C	DB	0FFH
	1942	C	DB	
08B8 00 17 17 17 17 17	1943	C	DB	
08C0 17 17 17 17 17 17	1944	C	DB	017H, 017H, 017H, 017H, 017H, 017H
08C6 17 17 17 17 01 00	1945	C	DB	017H, 017H, 017H, 017H, 001H, 000H
08CC 01 00	1946	C	DB	
08D7 50 18 0E	1947	C	DB	000H, 017H, 017H, 017H, 017H, 017H
08D0 1000	1948	C	DB	017H, 017H, 017H, 017H, 017H, 017H
08DC 00 03 00 03	1949	C	DB	000H, 000H, 000H, 000H, 000H, 000H
08E0 A6	1950	C	DB	0A6H
08E1 60 4F 56 3A 51 60	1951	C	DB	060H, 04FH, 056H, 03AH, 051H, 060H
08E7 70 1F 00 00 0B 0C	1952	C	DB	070H, 01FH, 000H, 000H, 00BH, 00CH
08ED 00 00 00 00 5E 2E	1953	C	DB	000H, 000H, 000H, 000H, 05EH, 02EH
08F3 5D 28 00 5E 6E A3	1954	C	DB	05DH, 028H, 000H, 05EH, 06EH, 0A3H
08F9 FF	1955	C	DB	0FFH
	1956	C	DB	
08FA 00 08 08 08 08 08	1957	C	DB	;--7--
0900 08 08 10 18 18 00	1958	C	DB	80D, 24D, 1AD
0906 18 18 18 18 0E 00	1959	C	DW	01000H
090C 07 08	1960	C	DB	000H, 003H, 000H, 003H
09CE 00 00 00 00 00 00	1961	C	DB	0A6H
08D4 0D 00 FF	1962	C	DB	
	1963	C	DB	
0907 50 18 0E	1964	C	DB	
090A 1000	1965	C	DB	
091C 00 00 00 03	1966	C	DB	
0920 23	1967	C	DB	
0921 37 27 20 37 31 15	1968	C	DB	
0927 04 11 00 07 06 07	1969	C	DB	
0920 00 00 00 00 E1 24	1970	C	DB	
0933 C7 14 08 EO FO A3	1971	C	DB	
0939 FF	1972	C	DB	
	1973	C	DB	
090E 00 00 00 00 00 10	1974	C	DB	
0914 04 00 FF	1975	C	DB	000H, 000H, 000H, 000H, 000H, 010H
	1976	C	DB	0AAH, 000H, 0FFH
	1977	C	DB	
0917 28 18 08	1978	C	DB	;--8--
091A 4000	1979	C	DW	04000H
091C 00 00 00 03	1980	C	DB	000H, 000H, 000H, 003H
0920 23	1981	C	DB	023H
0921 37 27 20 37 31 15	1982	C	DB	037H, 027H, 020H, 037H, 031H, 015H
0927 04 11 00 07 06 07	1983	C	DB	004H, 011H, 000H, 007H, 006H, 007H
0920 00 00 00 00 E1 24	1984	C	DB	000H, 000H, 000H, 000H, 0E1H, 024H
0933 C7 14 08 EO FO A3	1985	C	DB	0C7H, 014H, 008H, 0E0H, 0FOH, 0A3H
0939 FF	1986	C	DB	0FFH, 000H
	1987	C	DB	
093A 00 01 02 03 04 05	1988	C	DB	
0940 06 07 10 11 12 13	1989	C	DB	
0946 14 15 16 17 08 00	1990	C	DB	
094C 0F 00	1991	C	DB	
094E 00 00 00 00 00 10	1992	C	DB	
0954 0E 00 FF	1993	C	DB	
	1994	C	DB	
0957 28 18 08	1995	C	DB	
095A 4000	1996	C	DB	
095C 00 00 00 03	1997	C	DB	
0960 23	1998	C	DB	
0961 37 27 20 37 31 15	1999	C	DB	
0967 04 11 00 07 06 07	2000	C	DB	;--9--
0960 00 00 00 00 E1 24	2001	C	DW	40D, 24D, 0BD
0973 C7 14 08 EO FO A3	2002	C	DB	04000H
0979 FF	2003	C	DB	000H, 000H, 000H, 003H
	2004	C	DB	
	2005	C	DB	
	2006	C	DB	
	2007	C	DB	
	2008	C	DB	
	2009	C	DB	
	2010	C	DB	
	2011	C	DB	
	2012	C	DB	
	2013	C	DB	
	2014	C	DB	
	2015	C	DB	
	2016	C	DB	

098E	00 00 00 00 00 10	2017	C	DB	000H, 000H, 000H, 000H, 000H, 010H
0994	OE 00 FF	2018	C	DB	00EH, 000H, OFFH
		2019	C		
		2020	C		
		2021	C	DB	40D, 24D, 08D
0997	28 18 08	2022	C	DB	0400H
099A	4000	2023	C	DB	000H, 000H, 000H, 003H
		2024	C		
099C	00 00 00 03	2025	C	DB	023H
09A0	23	2026	C		
		2027	C		
09A1	37 27 20 37 31 15	2028	C	DB	037H, 027H, 02DH, 037H, 031H, 015H
09A7	08 11 00 07 00 07	2029	C	DB	004H, 011H, 000H, 007H, 006H, 007H
09A9	00 00 00 00 E1 24	2030	C	DB	000H, 000H, 000H, 000H, 01H, 024H
09B3	C7 14 00 EO 10 A3	2031	C	DB	000H, 014H, 008H, 0E0H, 0F0H, 0A3H
09B9	FF	2032	C	DB	0FFH
		2033	C		
09B8	00 01 02 01 04 05	2034	C	DB	000H, 001H, 002H, 003H, 004H, 005H
09C0	05 07 10 11 12 13	2035	C	DB	000H, 005H, 010H, 011H, 012H, 013H
09C6	14 15 16 17 08 00	2036	C	DB	014H, 015H, 016H, 017H, 008H, 000H
09CC	00 FF	2037	C	DB	00FH, 000H
		2038	C		
09CE	00 00 00 00 10	2039	C		
09D4	00 00 00 00 00 10	2040	C	DB	000H, 000H, 000H, 000H, 010H
		2041	C	DB	00EH, 000H, OFFH
		2042	C		
		2043	C	DB	80D, 24D, 08D
09D7	50 18 08	2044	C	DB	01000H
09DA	1000	2045	C		
		2046	C	DB	001H, 004H, 000H, 007H
09DC	01 04 00 07	2047	C		
09E0	23	2048	C	DB	023H
		2049	C		
09E1	70 4F 5C 2F 5F 07	2050	C	DB	070H, 04FH, 05CH, 02FH, 05FH, 007H
09E7	08 11 00 07 00 07	2051	C	DB	004H, 011H, 000H, 007H, 006H, 007H
09E9	00 00 00 00 E1 24	2052	C	DB	000H, 000H, 000H, 000H, 01H, 024H
09F3	C7 28 00 EO F0 A3	2053	C	DB	0C7H, 028H, 008H, 0E0H, 0F0H, 0A3H
09F9	FF	2054	C	DB	0FFH
		2055	C		
09FA	00 00 00 00 00 00	2056	C	DB	000H, 000H, 000H, 000H, 000H, 000H
09A0	00 00 00 00 00 00	2057	C	DB	000H, 000H, 000H, 000H, 000H, 000H
09A6	00 00 00 00 00 00	2058	C	DB	000H, 000H, 000H, 000H, 000H, 000H
09AC	00 FF	2059	C	DB	00FH, 000H
		2060	C		
09E0	00 00 00 00 00 00	2061	C		
09E4	00 00 FF	2062	C	DB	000H, 000H, 000H, 000H, 000H, 000H
09A4	00 00 FF	2063	C	DB	004H, 000H, OFFH
		2064	C	DB	00FH, 000H
A117	50 18 0E	2065	C	DB	80D, 24D, 14D
A1A1	1000	2066	C	DB	01000H
A1C1	00 04 00 07	2067	C	DB	000H, 004H, 000H, 007H
A2A0	A6	2068	C	DB	0A6H
		2069	C		
		2070	C		
A2A1	60 4F 56 3A 51 60	2071	C	DB	060H, 04FH, 056H, 03AH, 051H, 060H
A2A7	70 1F 00 00 08 0C	2072	C	DB	070H, 01FH, 000H, 008H, 00CH
A2A9	00 00 00 00 5E 24	2073	C	DB	000H, 000H, 000H, 000H, 05EH, 02EH
A3A3	58 2B 5E 6E A3	2074	C	DB	050H, 028H, 00DH, 05EH, 06EH, 0A3H
A3A9	FF	2075	C	DB	0FFH
		2076	C		
A3A9	00 00 00 00 00 00	2077	C	DB	000H, 000H, 000H, 000H, 000H, 000H
A4A0	00 00 00 00 00 00	2078	C	DB	000H, 000H, 000H, 000H, 000H, 000H
A4A6	00 00 00 00 00 00	2079	C	DB	000H, 000H, 000H, 000H, 000H, 000H
A4AC	0F 08	2080	C	DB	000H, 000H, 000H, 000H, 00EH, 000H
		2081	C	DB	00FH, 000H
A4AE	00 00 00 00 00 00	2082	C	DB	000H, 000H, 000H, 000H, 000H, 000H
A5A4	00 00 FF	2083	C	DB	004H, 000H, OFFH
		2084	C	DB	004H, 000H, OFFH
A5A7	28 18 08	2085	C	DB	80D, 24D, 08D
A5A5	2000	2086	C	DB	02000H
A5C5	00 0F 00 06	2087	C	DB	00BH, 00FH, 000H, 006H
A6A0	23	2088	C	DB	023H
		2089	C		
A6A1	37 27 20 37 30 14	2090	C	DB	037H, 027H, 02DH, 037H, 030H, 014H
A6A7	08 11 00 07 00 14	2091	C	DB	004H, 011H, 000H, 007H, 006H, 000H
A6A9	00 00 00 00 1F 24	2092	C	DB	000H, 000H, 000H, 000H, 01H, 024H
A7A3	C7 14 00 EO F0 E3	2093	C	DB	0C7H, 014H, 000H, 0E0H, 0F0H, 0E3H
A7A9	FF	2094	C	DB	0FFH
		2095	C	DB	000H, 000H, 000H, 000H, 000H, 000H
A7A9	00 01 02 03 04 05	2096	C	DB	000H, 001H, 002H, 003H, 004H, 005H
A8A0	07 10 11 12 13	2097	C	DB	006H, 007H, 010H, 011H, 012H, 013H
A8A6	14 15 16 17 01 00	2098	C	DB	014H, 015H, 016H, 017H, 001H, 000H
A8AC	00	2099	C	DB	00FH, 000H
		2100	C	DB	000H, 000H, 000H, 000H, 000H, 000H
A9A4	05 FF FF	2101	C	DB	000H, 000H, 000H, 000H, 000H, 000H
		2102	C	DB	000H, 000H, 000H, 000H, 000H, 000H
A9A8	00 00 00 00 00 00	2103	C	DB	000H, 000H, 000H, 000H, 000H, 000H
A9A9	05 FF FF	2104	C	DB	005H, 00FH, OFFH
		2105	C	DB	000H, 000H, 000H, 000H, 000H, 000H
A9A7	50 18 08	2106	C	DB	80D, 24D, 08D
A9A9	4000	2107	C	DB	04000H
A9C1	01 0F 00 06	2108	C	DB	001H, 00FH, 000H, 006H
A9A0	23	2109	C	DB	023H
		2110	C		
A9A1	70 4F 59 2D 5E 06	2111	C	DB	070H, 04FH, 059H, 02DH, 05EH, 006H
A9A7	01 11 00 00 00 00	2112	C	DB	004H, 011H, 000H, 000H, 000H, 000H
A9A9	00 00 00 00 E0 23	2113	C	DB	000H, 000H, 000H, 000H, 0E0H, 023H
A9A9	FF	2114	C	DB	0FFH
		2115	C	DB	000H, 001H, 002H, 003H, 004H, 005H
A9A9	00 00 00 00 00 00	2116	C	DB	000H, 007H, 010H, 011H, 012H, 013H
A9A6	14 15 16 17 01 00	2117	C	DB	014H, 015H, 016H, 017H, 001H, 000H
A9A9	28 00 DF EF E0	2118	C	DB	00FH, 000H, OFFH
		2119	C	DB	000H, 000H, 000H, 000H, 000H, 000H
A9A9	FF	2120	C	DB	000H, 001H, 002H, 003H, 004H, 005H
		2121	C	DB	000H, 007H, 010H, 011H, 012H, 013H
A9A6	14 15 16 17 01 00	2122	C	DB	014H, 015H, 016H, 017H, 001H, 000H
A9A9	00 00 00 00 00 00	2123	C	DB	00FH, 000H, OFFH, 00FH, 0E0H, 0E3H
		2124	C	DB	000H, 000H, 000H, 000H, 000H, 000H
A9A4	05 0F FF	2125	C	DB	005H, 00FH, OFFH
		2126	C	DB	000H, 000H, 000H, 000H, 000H, 000H
A9A7	50 18 0E	2127	C	DB	80D, 24D, 14D
A9A8	8000	2128	C	DB	08000H
A9DC	05 0F 00 00	2129	C	DB	005H, 00FH, 000H, 000H
A9E0	A2	2130	C	DB	0A2H
		2131	C		
A9E1	60 4F 56 1A 50 EO	2132	C	DB	060H, 04FH, 056H, 01AH, 050H, 0EOH
A9E7	70 1F 00 00 00 00	2133	C	DB	070H, 01FH, 000H, 000H, 000H, 000H
A9E9	00 00 00 00 5E 2E	2134	C	DB	000H, 000H, 000H, 000H, 0E0H, 02EH
A9F3	5D 14 00 5E 6E 8B	2135	C	DB	05DH, 014H, 000H, 05EH, 06EH, 0BBH
A9F9	FF	2136	C	DB	0FFH
		2137	C	DB	000H, 000H, 000H, 000H, 000H, 000H
A9F1	00 08 00 00 00 00	2138	C	DB	000H, 008H, 000H, 000H, 018H, 018H
A9F0	00 00 00 00 00 00	2139	C	DB	000H, 000H, 000H, 000H, 008H, 000H
		2140	C		
A9F4	00 08 00 00 18 18	2141	C	DB	000H, 008H, 000H, 000H, 018H, 018H
A9F0	00 00 00 00 00 00	2142	C	DB	000H, 000H, 000H, 000H, 008H, 000H

```

0B06 00 18 00 00 0B 00 2183 C DB 000H,018H,000H,000H,00BH,000H
0B0C 05 00 2184 C DB 005H,000H
0B0E 00 00 00 00 00 10 2185 C DB 000H,000H,000H,000H,000H,010H
0B14 07 0F FF 2186 C ;--10-- DB 007H,00FH,0FFH
0B17 50 18 0E 2188 C DB 80D,24D,14D
0B1A 8000 2189 C DW 08000H
0B1C 05 0F 00 00 2190 C
0B20 A7 2191 C
0B21 5B 4F 53 17 50 BA 2192 C
0B27 6C 1F 00 00 00 00 2193 C
0B30 5D 28 0F 5F 0E 2B 2194 C
0B33 5D 14 0F 57 04 88 2195 C
0B39 FF 2196 C
0B3A 00 01 00 00 04 07 2197 C
0B40 00 00 00 01 00 00 2198 C
0B46 04 07 00 00 01 00 2199 C
0B4C 05 00 2200 C
0B4E 00 00 00 00 00 10 2201 C
0B54 07 0F FF 2202 C
= 0440 2203 C
2204 C BASE_2 EQU S - VIDEO_PARMS
2205 C ;----- > 16K MODE VALUES
2206 C ;--F-- DB 80D,24D,14D
2207 C DW 08000H
0B57 50 18 0E 2208 C
0B5A 8000 2209 C
0B5C 01 0F 00 06 2210 C
0B60 A2 2211 C
0B61 60 4F 56 3A 50 60 2212 C
0B67 70 1F 00 00 00 00 2213 C
0B73 00 00 00 00 5E 2E 2214 C
0B79 FF 2215 C
0B7A 00 08 00 00 18 18 2216 C
0B80 00 00 00 08 00 00 2217 C
0B86 00 18 00 00 0B 00 2218 C
0B8C 05 00 2219 C
0B8E 00 00 00 00 00 00 2220 C
0B94 05 0F FF 2221 C
0B97 50 18 0E 2222 C
0B9A 8000 2223 C
0B9C 01 0F 00 06 2224 C
0BA0 A7 2225 C
0BA1 5B 4F 53 37 52 00 2226 C
0B47 6C 1F 00 00 00 00 2227 C
0BAD 00 00 00 00 5E 2B 2228 C
0B83 5D 28 0F 5F 0A E3 2229 C
0B89 FF 2230 C
0BBA 00 01 02 03 04 05 2231 C
0BBD 14 07 38 39 3A 3B 2232 C
0BC6 3C 3D 3E 3F 01 00 2233 C
0BCC OF 00 2234 C
0BCE 00 00 00 00 00 00 2235 C
0BD4 05 0F FF 2236 C
= 04C0 2237 C
2238 C BASE_3 EQU S - VIDEO_PARMS
2239 C ;----- HI RES ALTERNATE VALUES
2240 C ;--0-- DB 40D,24D,14D
2241 C DW 08000H
0B07 28 18 0E 2242 C
0BDA 8000 2243 C
0BDC 0B 03 00 03 2244 C
0BE0 A7 2245 C
0BE1 2D 27 2B 2D 2B 60 2246 C
0BET 6C 1F 00 00 06 07 2247 C
0B80 00 00 00 00 5E 2B 2248 C
0BF3 5D 14 0F 5E 0A A3 2249 C
0BF9 FF 2250 C
0BFA 00 01 02 03 04 05 2251 C
0C00 14 07 38 39 3A 3B 2252 C
0C06 3C 3D 3E 3F 08 00 2253 C
0C0C OF 00 2254 C
0C0E 00 00 00 00 00 10 2255 C
0C14 0E 00 0F FF 2256 C
0C17 28 18 0E 2257 C
0C1A 8000 2258 C
0C1C 0B 03 00 03 2259 C
0C20 A7 2260 C
0C21 2D 27 2B 2D 2B 60 2261 C
0C27 6C 1F 00 00 06 07 2262 C
0C82 00 00 00 00 5E 2B 2263 C
0C33 5D 14 0F 5E 0A A3 2264 C
0C39 FF 2265 C
0C3A 00 01 02 03 04 05 2266 C
0C40 14 07 38 39 3A 3B 2267 C ;--1-- DB 80D,24D,14D
0C46 3C 3D 3E 3F 08 00 2268 C
0C4C OF 00
0C4E 00 00 00 00 00 10 2269 C
0C54 0E 00 FF 2270 C
0C57 50 18 0E 2271 C

```



```

0038 EC 2395 IN AL,DX ; GET SETTING OF PORT
0039 8A E0 2396 MOV AH,AL ; SAVE THAT SETTING
003B 0C 03 2397 OR AL,03 ; TURN SPEAKER ON
003D E8 001E R 2398 CALL BP_1 ; SET CNT TO WAIT 500 MS
003E 26 C9 2399 SUB CX,CX
0042 2400 G7: ; SET CNT TO WAIT 500 MS
0042 E2 FE 2401 LOOP G7 ; DELAY BEFORE TURNING OFF
0044 FE CB 2402 DEC BL ; DELAY CNT EXPIRED?
0046 75 FA 2403 JNZ G7 ; NO-CONTINUE BEEEPING SPK
0048 40 C4 2404 MOV AH,AL ; RECOVER VALUE OF PORT
004A E8 001E R 2405 CALL BP_1
004D 5A 2406 POP DX
004E C3 2407 RET
004F 2408 BEEP ENDP ; RETURN TO CALLER
004F 2409
004F 2410 ;----- FIND THE PARAMETER TABLE VECTOR IN THE SAVE TABLE
004F 2411
004F E8 00CF E 2412 SET_BASE PROC NEAR
0052 C4 1E 00AB R 2413 ASSUME DS:ABSO
0056 26: C4 1F 2414 CALL DDS
0059 C3 2415 LES BX,SAVE_PTR ; GET PTR TP PTR TABLE
005A 2416 LES BX,DWORD PTR ES:[BX] ; GET PARAMETER PTR
005A 2417 RET
005A 2418 SET_BASE ENDP
005A 2419
005A 2420 ;----- ESTABLISH ADDRESSING TO THE CORRECT MODE TABLE ENTRY
005A 2421
005A 2422 MAKE_BASE PROC NEAR
005A 51 2423 ASSUME DS:ABSO
005B 52 2424 PUSH CX
005C 00 2425 PUSH DX
005D 00 2426 CALL SET_BASE ; GET PARM TBL PTR
005E 8A 26 0049 R 2427 MOV AH,CRT_MODE ; TEST FOR BASE CARD
0063 F6 06 0087 R 60 2428 TEST INFO_060H ; MIN MEMORY
0068 74 18 2429 JZ B_M_1
0068 2430
0068 77 14 2431
0068 2432 ;----- WE HAVE A MEMORY EXPANSION OPTION HERE
0068 80 FC 0F 2433 CMP AH,0FH
0068 75 07 2434 JNE B_M_2
0068 00 2435 ADD BX,BASE_2 - BASE_1
0073 EB 33 90 2436 JMP B_M_OUT
0076 80 FC 10 2437
0079 75 07 2438 CMP AH,010H
0079 80 FC 00 2439 JNE B_M_1
0079 00 2440 ADD BX,BASE_2 + M_TBL_LEN - BASE_1
007F EB 27 90 2441 JMP B_M_OUT
0082 2442
0082 80 FC 03 2443 CMP AH,03H
0085 77 14 2444 JA B_M_3 ; SKIP ENHANCED PORTION
0085 2445
0085 2446 ;----- CHECK THE SWITCH SETTING FOR ENHANCEMENT
0085 2447
0085 2448 BRS: ADD BX,BASE_3 - BASE_1 ; VECTOR TO ENHANCEMENT TBL
0097 81 C3 04C0 2449
0098 2450
0098 3C 03 2451 ; SECONDARY EMULATE SETTING
0098 74 07 2452 JE BRS
0090 3C 09 2453 CMP AL,09H
0092 00 2454 JE BRS
0094 EB 09 90 2455 JMP B_M_3
0094 2456
0094 2457 ;----- WE WILL PERFORM ENHANCEMENT
0094 2458
0094 2459 B_M_3: ADD BX,BASE_3 - BASE_1 ; VECTOR TO ENHANCEMENT TBL
0098 2460
0098 2461 ; PRIMARY EMULATE SETTING
0098 2462 MOV CL,CRT_MODE
0098 2463 SUB CH,CH
0098 2464 JCXZ B_M_4
0098 2465
0098 2466 ;----- THIS LOOP WILL MOVE THE PTR TO THE INDIVIDUAL MODE ENTRY
0098 2467
0098 2468 B_M_5: ADD BX,M_TBL_LEN ; LENGTH OF ONE MODE ENTRY
0098 2469 LOOP B_M_5
0098 2470 ;----- B_M_OUT:
0098 2471 B_M_OUT: POP DX
0098 2472 POP CX
0098 2473 RET
0098 2474 MAKE_BASE ENDP
0098 2475
0098 2476 ;----- PROGRAM THE EGA REGISTERS FROM THE PARAMETER TABLE
0098 2477
0098 2478 ODAB SET_REGS PROC NEAR
0098 2479 ASSUME DS:ABSO,ES:NOTHING
0098 2480
0098 2481 ;----- PROGRAM THE SEQUENCER
0098 2482
0098 2483
0098 2484 CALL MAKE_BASE ; GET TABLE PTR
0098 2485 ADD BX,TFS_LEN ; MODE TO SEQUENCER PARMs
0098 2486 MOV DH,3
0098 2487 MOV DL,SEQ_ADDR
0098 2488 MOV AX,0001H ; RESET SEQUENCER
0098 2489 CLI ; DISABLE INTERRUPTS
0098 2490 CALL OUT_DX ;----- PROGRAM THE CRT CONTROLLER
0098 2491 MOV AL,ES:[BX] ; GET SEQUENCER VALUE
0098 2492 INC AH ; NEXT INDEX
0098 2493 CALL OUT_DX ; SET IT
0098 2494
0098 2495 D1: INC AH ; NEXT INDEX REGISTER
0098 2496 INC BX ; NEXT TABLE ENTRY
0098 2497 MOV AL,ES:[BX]
0098 2498 CALL OUT_DX ;----- PROGRAM THE CRT CONTROLLER
0098 2499 CMP AH,H1+1 ; GET CRTC INDEX REGISTER
0098 2500 JB D1 ; COUNTER
0098 2501
0098 2502 MOV AL,ES:[BX] ; GET VALUE FROM TABLE
0098 2503 INC BX ; SET CRTC REGISTER
0098 2504 MOV DL,MISC_OUTPUT ; NEXT TABLE ENTRY
0098 2505 OUT DX,AL ; GET SEQ_ADDR
0098 2506 MOV DL,SEQ_ADDR ; NEXT INDEX VALUE
0098 2507 MOV AX,003H ; TEST REGISTER COUNT
0098 2508 CALL OUT_DX ;----- PROGRAM THE CRT CONTROLLER
0098 2509 STI ; START SEQUENCER
0098 2510
0098 2511 ;----- PROGRAM THE CRT CONTROLLER
0098 2512
0098 2513 MOV DX,ADDR_6845 ; CRTC INDEX REGISTER
0098 2514 SUB AH,AH ; COUNTER
0098 2515
0098 2516 X1: MOV AL,ES:[BX] ; GET VALUE FROM TABLE
0098 2517 CALL OUT_DX ; SET CRTC REGISTER
0098 2518 INC BX ; NEXT TABLE ENTRY
0098 2519 INC AH ; NEXT INDEX VALUE
0098 2520 CMP AH,M4 ; TEST REGISTER COUNT

```

```

0DF4 72 F2 2521 JB X1 ; DO THE REST
0000 26: 8B 47 F1 2522 XUNG AX,ES:[BX](-0FH) ; GET CURSOR MODE
0DFA 00 0005 R 2523 AH,AL
0DFC A3 D460 R 2524 MOV CURSOR_MODE,AX ; SET LOW RAM VALUE
2525
2526 ;----- PROGRAM THE ATTRIBUTE CHIP
2527
0DFF 8B F3 2528 MOV SI,BX
0E01 E8 0D05 R 2529 CALL WHAT_BASE
0E04 EC 2530 IN AL,DX
0E05 B2 C0 2531 MOV DL,ATTR_WRITE
0E07 2A E4 2532 SUB AH,AH
0E09 2533
0E09 26: 8A 07 2534 MOV AL,ES:[BX] ; INDEX COUNTER
0E0C 86 E0 2535 XCHG AH,AL ; GET DATA VALUE
0E0D E6 2536 OUT DX,AL
0E0F 86 E0 2537 XCHG AH,AL ; NEXT DATA VALUE
0E11 E6 2538 OUT DX,AL ; NEXT INDEX VALUE
0E12 43 2539 INC BX
0E13 FE C4 2540 INC AH
0E15 80 FC 14 2541 CMP AH,M5 ; TEST REGISTER COUNT
0E18 72 E7 2542 JB D3 ; DO THE REST
2543
0E1A B0 00 2544 MOV AL,0
0E1C EE 2545 OUT DX,AL
2546
2547 ;----- CHECK IF PALETTE REGISTER VALUES ARE TO BE SAVED
2548
0E1D 1E 2549 PUSH DS
0E1E 06 2550 PUSH ES
0E1F C4 3E 04A8 R 2551 LES DS,SAVE_PTR ; GET TABLE PTR
0E23 26: C4 7D 04 2552 LES DI,DWORD PTR ES:[DI](4) ; GET PALETTE PTR
0E27 8C C0 2553 MOV AX,ES
0E28 08 C7 2554 OR AX,DI
0E2B 74 09 2555 JZ SAVE_OUT ; IF ZERO, NO SAVE OCCURS
2556
2557 ;----- STORE AWAY THE PALETTE VALUES IN RAM SAVE AREA
2558
0E2D 1F 2559 POP DS
0E2E 1E 2560 PUSH DS
0E2F B9 0010 2561 MOV CX,16D ; SAVE THE PALETTE REGS
0E32 F3/ A4 2562 REP MOVS B
0E34 46 2563 INC SI ; SAVE THE OVERSCAN REG
0E35 A4 2564 MOVS B
0E36 07 2565 POP ES
0E37 1F 2566 POP DS
2567
2568 ;----- PROGRAM THE GRAPHICS CHIPS
2569
0E38 B2 CC 2570 MOV DL,GRAPH_1_POS
0E39 B0 00 2571 MOV AL,0
0E3C E6 2572 OUT DX,AL
0E3D B2 CA 2573 MOV DL,GRAPH_2_POS
0E3F B0 01 2574 MOV AL,1
0E41 EE 2575 OUT DX,AL
0E42 B2 CE 2576 MOV DL,GRAPH_ADDR
0E44 2A E6 2577 SUB AH,AH
0E46 2578
0E46 26: 8A 07 2579
0E4C E8 0D15 R 2580 MOV AL,ES:[BX]
0E4C F3 2581 CALL OUT_DX ; PARAMETER BYTE
0E4D F6 2582 INC BX ; SET IT
0E4D FE C4 2583 INC AH ; NEXT BYTE
0E4F 80 FC 09 2584 CMP AH,M6 ; NEXT REGISTER
0E52 72 F2 2585 JB D4 ; CONTINUE
0E54 C3 2586 RET
0E55
2587 SET_REGS ENDP
2588
2589 ;----- MODE SET REGEN CLEAR ROUTINE
2590
0E55 PROC NEAR ; FILL REGEN WITH BLANKS
2591 ASSUME DS:ABSO,ES:NOTHING
2592 MOV AL,INFO ; SEE IF BLANK IS TO OCCUR
2593 TEST AL,080H ; MODE SET HIGH BIT
2594 JNZ OUT1 ; SKIP BLANK FOR REGEN
2595 MOV DX,08000H ; CURRENT REGEN ADDRESS
2596 MOV AL,CRT_MODE ; 0-6 ARE COLOR MODES
2597 CMP AL,6 ; MONOCHROME REGEN ADDRESS
0E62 3C 06 2598 JBE CG0 ; MONOCHROME MODE
0E63 80 DA 2599 INC AL,7 ; 0-6 ARE COLOR MODES
0E66 BA 0800 2600 CMP AL,7 ; REMAINING MODES
0E69 3C 07 2601 MOV DX,08000H
0E6B 76 03 2602 CMP AL,7 ; ALPHA BLANK VALUE
0E6D BA A000 2603 JE CG0 ; ALPHAMODES 0-3
0E70 2604 MOV DX,0A000H
0E70 BB 0720 2605 CMP AL,4 ; ALPHAMODES 0-3
0E73 3C 04 2606 CMP AL,4 ; ALPHAMODES 0-3
0E75 72 06 2607 JB WM1 ; ALPHA MODE
0E77 3C 07 2608 CMP AL,7 ; GRAPHICS BLANK VALUE
0E79 74 02 2609 JE WM1
0E7B 2B DB 2610 SUB BX,BX ; SET THE REGEN SEGMENT
0E7D 8E C2 2611
0E7F 88 DE 044C R 2612 + SRLOAD ES ; SET THE REGEN SEGMENT
2613 MOV ES,DX
2614 MOV CS,CRT_LEN
2615 JCXZ OUT1 ; CLEAR PINTER
2616 MOV CX,08000H ; CLEAR THE PAGE
2617 CMP DH,0A0H
2618 JNC H_BA ; RETURN TO CALLER
2619 MOV CR,0A0H
2620 N_BA: MOV AX,BX ; BLANK VALUE
2621 SUB DI,DI ; CLEAR PINTER
2622 REP STOSW ; CLEAR THE PAGE
2623
2624 OUT1_: RET ; RETURN TO CALLER
2625 BLANK ENDP
2626
0E8F BB C3 2627 PH_5 PROC NEAR ; BLANK VALUE
2628 2B FF 2629 CALL PAL_ON ; CLEAR PINTER
0E93 F3/ AB 2630 RET ; CLEAR THE PAGE
0E95 C3 2631 PH_5 ENDP ; RETURN TO CALLER
0E96 2632
0E96 E8 1DB7 R 2633 ;----- SEE IF WE ARE TO SUPPORT 640 X 350 ON A 640 X 200 MODE
0E96 2634
0E9A 2635 BRST_DET PROC NEAR
0E9A 50 2636 ASSUME DS:ABSO
0E9B 1E 2637 PUSH AX
0E9C E8 0CFE R 2638 CALL DDS
0E9F 80 0488 R 2639 POP DS
0EA2 1F 2640 MOV AL,INFO_3
0EA2 2641 POP DS
0EA3 2B 0F 2642 AND AL,0FH ; EMULATE MODE
0EA3 3C 03 2643 CMP AL,03H ; EMULATE MODE
0EA7 3C 07 2644 JE B_35
0EA9 3C 09 2645 CMP AL,0DH
0EAB 76 03 2646 JE B_YES

```



```

OFA2 80 FC 03 2773 CMP AH_03H
OFA5 77 35 2774 JA ENTRY_1
OFA7 E8 0E9A R 2775 CALL BRST_DET
OFAA 72 02 2776 JC ENTRY_2
OFAE BO 02 2777
OFAE 2778
OFAE E8 1EAE R 2780 MOV AL,2 ; COLOR ALPHA CHAR GEN
OFB1 E8 0CFC R 2781 CALL CH_GEN ; LOAD ALPHA CHAR GEN
OFB4 8A 26 0494 R 2782 CALL DDS
OFB4 8A 26 0494 R 2783 MOV AH,CRT_MODE ; GET CURRENT MODE
OFB5 8A 00 07 2784 CMP AH,7 ; IS IT MONOCHROME
OFB5 8A 00 03 2785 JZ FDG_IT ; 9X14 FONT
OFB5 EB 10 90 2786
OFC0 2787
OFC0 BB 0000 E 2788 MOV BP,OFFSET CGMN_FDG ; TABLE POINTER
OFC0 BB 0000 2789 MOV BX,0EO0H ; 14 BYTES PER CHAR
OFC6 2790
OFC6 0E 2791
OFC7 07 2792
OFC7 26 8B 56 00 2793 PUSH CS ; GET THE ROM SEGMENT
OFC7 26 8B 56 00 2794 POP ES ; INTO ES
OFC7 08 02 2794
OFC7 74 0C 2795 MOV DX,ES:[BP] ; GET THE CHAR HEX CODE
OFC7 74 0C 2796 OR DX,DX ; ZERO = NO MORE CHARS
OFC7 00 0001 2797 MOV IZ,ENTRY_1 ; NO MORE
OFC7 45 2797 INC BP ; DO ONE CHAR AT A TIME
OFC7 45 1E6F R 2798 CALL DO_MAP2 ; MOVE TO FIRST CODE POINT
OFC7 83 C5 0F 2799 CALL BP,014D ; STORE THE CODE POINT
OFC7 EB EA 2800 JMP FDG ; ADJUST BP TO NEXT CODE
OFC7 2801
OFC7 E8 0DAB R 2802 CALL SET_REGS ; DO ANOTHER
OFC7 E8 0E55 R 2803 CALL BLANK ; CLEAR OUT THE BUFFER
OFE2 EB 0E96 R 2804 CALL PH_5
OFE2 2805
OFE5 E8 00FE R 2806 ASSUME DS:ABSO
OFE8 80 3E 0494 R OF 2807 CALL DDS
OFE8 72 06 2808 MOV AH,0FH
OFEF C7 06 010C R 0000 E 2810 CALL CRT_MODE,0FH
OFEF 2811
OFEF 80 3E 0494 R 07 2811 MS_1: CMP CRT_MODE,7
OFEF 77 09 2812 JA SAVE_GRPB
OFEF 74 4B 2813 JE SAVE_ALPH
OFEF 80 3E 0494 R 03 2814 CMP CRT_MODE,3
OFEF 76 44 2815 JBE SAVE_ALPH
OFEF 2816
OFEF 0005 CH 1E 04AB R 2817 SAVE_GRPB: LES BX,SAVE_PTR
OFEF 83 C3 0C 2818 ADD BX,0CH
OFEF 26 8C 1F 2819 LES BX,DWORD PTR ES:[BX]
OFEF 000F CH 1E 04AB R 2820 MOV AX,ES
OFEF 1011 00 03 2821 OR AX,BX
OFEF 1013 74 32 2822 JZ J4J
OFEF 1015 BE 0007 2823 MOV SI,0TH ; JMP AHO_DONE
OFEF 2824
OFEF 1018 26: 8A 00 2825
OFEF 3C FF 2826 SG_1: MOV AL,ES:[BX][SI]
OFEF 1019 26: 8A 07 2827 CMP AL,0FH
OFEF 74 7A 2828 JE AHO_DONE
OFEF 101E 3A 06 0494 R 2829 CMP AL,CRT_MODE
OFEF 2830 00 03 2830 JE SG_2
OFEF 1025 46 2831 INC SI
OFEF 1026 EB F0 2832 JMP SG_1
OFEF 2833
OFEF 2834
OFEF 2835
OFEF 2836
OFEF 2837
OFEF 2838
OFEF 2839
OFEF 2840
OFEF 2841
OFEF 2842
OFEF 2843
OFEF 2844
OFEF 2845
OFEF 2846
OFEF 2847
OFEF 2848
OFEF 2849 CH 1E 04AB R ; SAVE ALPH
OFEF 83 C3 08 2849 SAVE_ALPH: LES BX,SAVE_PTR
OFEF 26: 8C 1F 2850 ADD BX,0SH
OFEF 1053 8C C0 2851 LES BX,DWORD PTR ES:[BX]
OFEF 1057 8C C3 2852 MOV AX,ES
OFEF 1059 8C 40 2853 OR AX,BX
OFEF 1059 BE 000B 2854 JZ AHO_DONE
OFEF 2855
OFEF 26: 8A 00 2856 MOV SI,0BH
OFEF 2857
OFEF 1061 74 36 2858 SG_2: MOV AL,ES:[BX][SI]
OFEF 1063 3A 06 0494 R 2859 CMP AL,0FH
OFEF 1067 74 03 2860 JE AHO_DONE
OFEF 46 2861 INC SI
OFEF EB F0 2862 JMP SG_1
OFEF 2863
OFEF 26: 8A 27 2864
OFEF 26: 8A 01 2865
OFEF 26: 8B 4F 02 2866
OFEF 26: 88 57 04 2867
OFEF 26: 88 5F 06 2868
OFEF 26: 8E 47 08 2869
OFEF 53 00 2870
OFEF 8B 08 2871
OFEF BB 1110 2872
OFEF CD 10 2873
OFEF 58 2874
OFEF 26: 8A 47 0A 2875
OFEF 3C FF 2876
OFEF 74 05 2877
OFEF FE C8 2878
OFEF A2 0484 R 2879
OFEF 2880
OFEF 2881
OFEF 2882
OFEF 2883 ;----- SET THE LOW RAM VALUES FOR COMPATIBILITY (308 AND 309 SAVE BYTES)
OFEF 2884
OFEF 2885 AHO_DONE: CALL DDS
OFEF 2886 CMP CRT_MODE,7
OFEF 2886 JA DNDCS
OFEF 2887 MOV BX,OFFSET COMPAT_MODE
OFEF 2888 MOV AL,CRT_MODE
OFEF 2889 SUB AL,0AH
OFEF 2890 ADD BX,AX
OFEF 2891 MOV AL,CS:[BX]
OFEF 2892 MOV CRT_MODE_SET,AL
OFEF 2893 ADD AL,0FH
OFEF 2894 CMP CRT_MODE,6
OFEF 2895 JNE DO_PAL
OFEF 2896 MOV AL,03FH
OFEF 2897 DO_PAL: MOV CRT_PALETTE,AL
OFEF 2898

```

```

10C1 2B 0E 0460 R 2699  NDICS: MOV CX_CURSOR_MODE
10C1 2B 0E 28 90 2900  JMP AH1
10C2 2902
10C8 2C 28 2D 29 2A 2E 2903  COMPAT_MODE LABEL BYTE
10C8 2C 28 2D 29 2A 2E 2904  DB 02CH,028H,02DH,029H,02AH,02EH
10CE 1E 29 2905  DB 01EH,029H
2906
C 2907  INCLUDE V1>5..INC
C 2908  SUBTLL V1>5..INC
C 2909  PAGE
2910
1000 2911  CALC_CURSOR PROC NEAR
1000 80 FD 00 2912  ASSUME DS:ABSO
1003 75 04 2913  CMP CH,0
1005 F5 C1 2914  JNE CC_1
1007 EE 0A 2915  INC CL
1009 2916  JMP SHORT CALC_OUT
2917  CC_1:  INC CL
2918  CMP CL, BYTE PTR POINTS
2919  JB CALC_OUT
2920  SUB CL,CL
2921  INC CL
2922  CALC_OUT:  PUSH CX
2923  SUB CL,CH
2924  CMP CL,010H
2925  POP CX
2926  JNE COMP_4
2927  INC CL
2928  COMP_4:  ADD CL,1
2929  RET
2930  CALC_CURSOR ENDP
2931
2932
2933 ;-----SET_CURSOR-----SET_CURSOR_TYPE
2934 ;-----THIS RUTINE SETS THE CURSOR VALUE
2935 ;-----INPUT----- (CX) HAS CURSOR VALUE CH-START LINE, CL-STOP LINE
2936 ;-----OUTPUT----- NONE
2937 2938
2939 2940
2941 2942  CUT_OFF EQU 4
2943
A1H: 2944  ASSUME DS:ABSO
2945  MOV AH,C_CSRK_START
2946  MOV CURSOR_MODE,CX
2947  TEST INFO_8
2948  JNZ DO_SET
2949
C 2950 ;-----THIS SECTION WILL EMULATE CURSOR OFF ON THE EGA
2951 2952  MOV AL,CH
2952  AND AL,0E0H
2953  CMP AL,020H
2954  JNE AH1_A
2955  MOV CX,01E00H
2956  JMP SHORT DO_SET
2957
2958 ;-----THIS SECTION : ADJUST THE CURSOR AND TEST FOR ENHANCED OPERATION
2959
A1H_A: 2960
2961  TEST INFO_1
2962  JNZ DO_SET
2963  CMP CRT_MODE,3
2964  JA AHT_S
2965  CALL BRST_DET
2966  INC AH
2967  CMP CH,CUT_OFF
2968  JBE AH1_B
2969  ADD CH,5
2970
A1H_B: 2971  CMP CL,CUT_OFF
2972  JBE AH1_S
2973  ADD CL,5
2974
AHT_S: 2975  CALL CALC_CURSOR
2976  DO_SET: CALL M16
2977  CALL OUT_DX
2978  JMP V_RET
2979
2980 ;-----THIS RUTINE OUTPUTS THE CX REGISTER TO THE CRTC REGS NAMED IN AH
2981
M16: 2982  MOV DX,ADDR_6845
2983  MOV AL,CH
2984  CALL OUT_DX
2985  INC AH
2986  MOV AL,CL
2987  CALL OUT_DX
2988
2989  RET
2990
2991
2992 ;-----POSITION-----THIS SERVICE ROUTINE CALCULATES THE REGEN BUFFER
2993 ;-----ADDRESS OF A CHARACTER IN THE ALPHA MODE----- ADDRESS
2994 ;-----INPUT----- AX = ROW, COLUMN POSITION----- ADDRESS
2995 ;-----OUTPUT----- AX = OFFSET OF CHAR POSITION IN REGEN BUFFER----- ADDRESS
2996
2997
2998
2999
1146 3000  POSITION PROC NEAR
1146 53 3001  PUSH BX
1147 88 D8 3002  MOV BX,AH
1149 8A C4 3003  MOV AL,AH
114B F6 26 044A R 3004  MUL BYTE PTR CRT_COLS
114C 35 FF 3005  XOR BH,BH
1151 03 00 3006  ADD AX,BX
1153 D1 E0 3007  SAL AX,1
1155 5B 3008  POP BX
1156 C3 3009  RET
1157 3010
3011
3012
3013
3014
3015
3016
3017
3018
3019
3020
3021
3022
3023
3024
POSITION ENDP
3012
3013
3014
3015
3016
3017
3018
3019
3020
3021
3022
3023
3024
SET_CPOS: 3012  SET_CURSOR_POSITION
3013  THIS RUTINE SETS THE CURRENT CURSOR POSITION TO THE
3014  NEW X-Y VALUES PASSED
3015
3016
3017
3018
3019
3020
3021
3022
3023
3024
INPUT: 3017  DX - ROW,COLUMN OF NEW CURSOR
3018  BH - DISPLAY PAGE OF CURSOR
3019
3020
3021
3022
3023
3024
OUTPUT: 3017  CURSOR IS SET AT CRTC IF DISPLAY PAGE IS CURRENT
3018  DISPLAY
3019
3020
3021
3022
3023
3024
AH2: 3017  CALL SET_CPOS

```

```

115A E9 219E R 3025 C JMP V_RET
115D 8A CF 3026 C
115D 32 ED 3027 C SET_CPOS:
1161 D1 E1 3028 C MOV CL,BH
1163 43 F1 3029 C XOR CH,CH
1165 89 94 0450 R 3030 C SAL CX,1
1169 38 3E 0462 R 3031 C ; ESTABLISH LOOP COUNT
116D 75 05 3032 C MOV SI,CX
116F 88 C2 3033 C CMP [SI+OFFSET_CURSOR_POSN],DX
1171 E8 1175 R 3034 C JNZ M17
1174 C3 3035 C MOV AX,DX
1174 3036 C CALL M18
1174 3037 C ; SET_CPOS_RETURN
1174 C3 3038 C ; GET_ROW_COLUMN_TO_AX
1174 3039 C ; CURSOR_SET
1174 3040 C ; SET_CPOS_RETURN
1175 E8 1146 R 3041 C ;----- SET CURSOR POSITION, AX HAS ROW/COLUMN FOR CURSOR
1178 8B C8 3042 C M18 PROC NEAR
117A 03 0E 044E R 3043 C POSITION
117E D1 F9 3044 C MOV CX,AX
1180 B4 0E 3045 C ADD CX,CRT_START
1182 E8 1135 R 3046 C ; ADD IN THE START ADDR
1185 C3 3047 C SAR CX,1
1186 3048 C MOV AH,CRSR_LOC_HGH
1186 3049 C CALL M16
1186 3050 C RET
1186 3051 C M18 ENDP
1186 3052 C ;----- READ_CURSOR
1186 3053 C ;----- ROUTINE READS THE CURRENT CURSOR VALUE FROM
1186 3054 C ;----- MEMORY AND SENDS IT BACK TO THE CALLER
1186 3055 C ;----- INPUT BH - PAGE OF CURSOR
1186 3056 C ;----- OUTPUT DX = ROW, COLUMN OF THE CURRENT CURSOR POSITION
1186 3057 C ;----- CX = CURRENT CURSOR MODE
1186 3058 C ;----- AH3:
1186 3059 C MOV BL,BH ; PAGE VALUE
1186 3060 C XOR BH,BH ; ZERO UPPER BYTE
1186 3061 C SAL BX,1 ; WORD OFFSET
1186 3062 C MOV [BX+OFFSET_CURSOR_POSN],DX ; GET CURSOR FOR THIS PAGE
1186 3063 C MOV CX,CURSOR_MODE ; GET THE CURSOR MODE
1186 3064 C POP DI
1186 3065 C POP SI
1186 3066 C POP BX
1186 3067 C POP AX ; DISCARD CX
1186 3068 C POP DS ; DISCARD DX
1186 3069 C POP ES
1186 3070 C POP BP
1186 3071 C IRET
1186 3072 C ;----- READ_LIGHT_PEN_POSITION
1186 3073 C ;----- AH4:
1186 3074 C MOV AL,CRT_MODE
1186 3075 C CMP AL,07H
1186 3076 C JA READ_LPEN
1186 3077 C ;----- TEST INFO_2
1186 3078 C JZ EGA_IS_COLOR
1186 3079 C ;----- MONOCHROME HERE (MONOC BIT 1)
1186 3080 C ;----- AH4:
1186 3081 C MOV AL,07H
1186 3082 C JE READ_LPEN
1186 3083 C JMP OLD_LPEN
1186 3084 C ;----- EGA IS COLOR HERE (MONOC BIT 0)
1186 3085 C ;----- AH4:
1186 3086 C CMP AL,06H
1186 3087 C JE READ_LPEN
1186 3088 C ;----- OLD_LPEN:
1186 3089 C ;----- EGA_IS_COLOR:
1186 3090 C CMP AL,06H
1186 3091 C JE READ_LPEN
1186 3092 C JMP OLD_LPEN
1186 3093 C ;----- ;----- EGA IS COLOR HERE (MONOC BIT 0)
1186 3094 C ;----- ;----- AH4:
1186 3095 C ;----- ;----- AH4:
1186 3096 C ;----- ;----- AH4:
1186 3097 C ;----- ;----- AH4:
1186 3098 C ;----- ;----- AH4:
1186 3099 C ;----- ;----- AH4:
1186 3100 C ;----- ;----- AH4:
1186 3101 C ;----- ;----- AH4:
1186 3102 C ;----- ;----- AH4:
1186 3103 C ;----- ;----- AH4:
1186 3104 C ;----- ;----- AH4:
1186 3105 C ;----- ;----- AH4:
1186 3106 C ;----- ;----- AH4:
1186 3107 C ;----- ;----- AH4:
1186 3108 C ;----- ;----- AH4:
1186 3109 C ;----- ;----- AH4:
1186 3110 C ;----- ;----- AH4:
1186 3111 C ;----- ;----- AH4:
1186 3112 C ;----- ;----- AH4:
1186 3113 C ;----- ;----- AH4:
1186 3114 C ;----- ;----- AH4:
1186 3115 C ;----- ;----- AH4:
1186 3116 C ;----- ;----- AH4:
1186 3117 C ;----- ;----- AH4:
1186 3118 C ;----- ;----- AH4:
1186 3119 C ;----- ;----- AH4:
1186 3120 C ;----- ;----- AH4:
1186 3121 C ;----- ;----- AH4:
1186 3122 C ;----- ;----- AH4:
1186 3123 C ;----- ;----- AH4:
1186 3124 C ;----- ;----- AH4:
1186 3125 C ;----- ;----- AH4:
1186 3126 C ;----- ;----- AH4:
1186 3127 C ;----- ;----- AH4:
11C1 06 07 07 05 05 3128 C ;----- SUBTRACT_TABLE:
11C1 04 05 00 00 00 00 3129 C ;----- LABEL BYTE
11C1 04 06 00 04 04 04 3130 C DB 006H,006H,007H,007H,005H,005H ; 0-5
11C1 04 06 00 04 04 04 3131 C DB 004H,005H,000H,000H,000H,000H ; 6-8
11D3 04 06 00 04 04 04 3132 C DB 004H,006H,006H,004H,004H,004H ; 9-11
11D9 07 04 3133 C DB 004H,006H,006H,004H,007H,004H ; 12-17
11D9 07 04 3134 C DB 007H,004H ; 18-19
11DB 3135 C ;----- READ_LPEN PROC NEAR
11DB 3136 C ;----- ;----- WAIT FOR LIGHT PEN TO BE DEPRESSED
11DB 3137 C ;----- ;----- V1
11DB 3138 C ;----- ;----- ;----- WAIT FOR LIGHT PEN TO BE DEPRESSED
11DB 3139 C ;----- ;----- MOV DX,ADDR_6845 ; GET BASE ADDRESS OF 6845
11DB 3140 C ;----- ;----- ADD DX,6 ; POINT TO STATUS REGISTER
11DB 3141 C ;----- ;----- IN AL,6 ; GET STATUS REGISTER
11DB 3142 C ;----- ;----- TEST AL,4 ; TEST LIGHT PEN SWITCH
11DB 3143 C ;----- ;----- MOV AH,0 ; SET NO LIGHT PEN RETURN
11DB 3144 C ;----- ;----- JZ V9 ; CODE
11DB 3145 C ;----- ;----- JMP V6 ; NOT SET, RETURN
11DB 3146 C ;----- ;----- ;----- NOW TEST FOR LIGHT PEN TRIGGER
11DB 3147 C ;----- ;----- ;----- V9:
11DB 3148 C ;----- ;----- TEST AL,2 ; TEST LIGHT PEN TRIGGER
11DB 3149 C
11EC A8 02 3150 C

```

```

11EE 75 03          3151   C   JNZ    V7A          ; RETURN WITHOUT RESETTING
11FO E9 129B R      3152   C   JMP    V7          ; TRIGGER
11F3 B4 10          3153   C   ;----- TRIGGER HAS BEEN SET, READ THE VALUE IN
11F3 B4 10          3154   C   ;----- INPUT REGS POINTED TO BY AH, AND CONVERT TO ROW COLUMN IN DX
11F3 B4 10          3155   C   ;----- V7A:          MOV AH,16          ; LIGHT PEN REGISTERS
11F3 B4 10          3156   C   ;----- 3157   C   MOV DX,ADDR_6845 ; ADDRESS REGISTER
11F3 B4 10          3157   C   MOV AL,AH          ; REGISTERED TO READ
11F3 B4 10          3158   C   OUT DX,AL          ; SET UP PORT
11F3 B4 10          3159   C   INC DX             ; DATA REGISTER
11F3 B4 10          3160   C   ;----- 3161   C   PUSH AX           ; GET THE VALUE
11F3 B4 10          3162   C   MOV DX,AL,DX        ; SAVE IN CX
11F3 B4 10          3163   C   MOV CH,AL          ; ADDRESS REGISTER
11F3 B4 10          3164   C   DEC DX             ; SECOND DATA REGISTER
11F3 B4 10          3165   C   INC AH             ; ADDRESS REGISTER
11F3 B4 10          3166   C   INC DX             ; POINT TO DATA REGISTER
11F3 B4 10          3167   C   MOV AL,DX          ; GET THE 2ND DATA VALUE
11F3 B4 10          3168   C   MOV AH,CH          ; AX HAS INPUT VALUE
11F3 B4 10          3169   C   ;----- 3170   C   DEC DX             ; ADDRESS REGISTER
11F3 B4 10          3171   C   INC AH             ; SECOND DATA REGISTER
11F3 B4 10          3172   C   MOV AL,DX          ; POINT TO DATA REGISTER
11F3 B4 10          3173   C   OUT DX,AL          ; GET THE 2ND DATA VALUE
11F3 B4 10          3174   C   INC DX             ; AX HAS INPUT VALUE
11F3 B4 10          3175   C   MOV AL,DX          ; ADDRESS REGISTER
11F3 B4 10          3176   C   MOV AH,CH          ; SECOND DATA REGISTER
11F3 B4 10          3177   C   ;----- 3178   C   MOV AL,DX          ; AX HAS THE VALUE READ IN FROM THE 6845
11F3 B4 10          3179   C   ;----- 3180   C   MOV BL,CRT_MODE ; DETERMINE MODE OF OPERATION
120C 8A 1E 0449 R  3181   C   SUB BH,BH          ; MODE VALUE TO BX
1210 2A 0E 0449 R  3182   C   MOV BX,BX:V1[BX] ; AMOUNT TO SUBTRACT
1210 2A 0E 0449 R  3183   C   ADD BX,BX          ; TAKE IT AWAY
1210 2A 0E 0449 R  3184   C   MOV AX,BX          ; SCREEN ADDRESS
1210 2A 0E 0449 R  3185   C   CMP BX,CRT_START ; DIVIDE BY 2
1210 2A 0E 0449 R  3186   C   SHL BX,1           ; ADD 1 TO ZERO START
1210 2A 0E 0449 R  3187   C   SUB AX,BX          ; IF POSITIVE, GET MODE
1210 2A 0E 0449 R  3188   C   INS VS,BX          ; <0 PLAYS AS '0
1210 2A 0E 0449 R  3189   C   SUB AX,AX          ; >0 PLAYS AS '1
1210 2A 0E 0449 R  3190   C   ;----- 3191   C   V2:          MOV CL,3           ; DETERMINE_MODE
1225 B1 03          3192   C   CMP CRT_MODE,4 ; SET *8 SHIFT COUNT
1227 B0 3E 0449 R  04 3193   C   JNE V3             ; GRAPHICS OR ALPHA
1227 B0 3E 0449 R  04 3194   C   CMP CRT_MODE,7 ; ALPHA_PEN
1227 B0 3E 0449 R  04 3195   C   JE V4              ; ALPHA_PEN
1227 B0 3E 0449 R  04 3196   C   ;----- 3197   C   V2:          MOV CL,3           ; DETERMINE_MODE
1227 B0 3E 0449 R  04 3198   C   CMP CRT_MODE,6 ; SET *8 SHIFT COUNT
1233 7B 46          3199   C   JNE V3             ; GRAPHICS OR ALPHA
1233 7B 46          3200   C   CMP CRT_MODE,7 ; ALPHA_PEN
1233 7B 46          3201   C   JE V4              ; ALPHA_PEN
1233 7B 46          3202   C   ;----- 3203   C   V8X:          MOV DL,40          ; OLD GRAPHICS MODES
1235 B0 3E 0449 R  06 3204   C   DIV DL,40          ; DIVISOR FOR GRAPHICS
1235 B0 3E 0449 R  06 3205   C   ;----- 3206   C   3206   C   DIV DL,40          ; ROW(AH) OR COLUMN(AH)
1235 B0 3E 0449 R  06 3207   C   ;----- 3208   C   3208   C   DIV DL,40          ; ALPHABIT 0-99
1235 B0 3E 0449 R  06 3209   C   ;----- 3210   C   3210   C   DIV DL,40          ; AH RANGE 0-39
1235 B0 3E 0449 R  06 3211   C   ;----- 3212   C   3212   C   ;----- DETERMINE GRAPHIC ROW POSITION
1244 8A E8          3213   C   MOV OH,AL          ; SAVE ROW VALUE IN CH
1246 02 ED          3214   C   ADD CH,CH          ; #2 FOR EVEN/ODD FIELD
1246 8A DC          3215   C   MOV BL,AH          ; COLUMN VALUE TO BX
1246 2A FF          3216   C   SUB BH,BH          ; #8 FOR MEDIUM RES
124C B0 3E 0449 R  06 3217   C   CMP CRT_MODE,6 ; MEDIUM OR HIGH RES
124C B0 3E 0449 R  06 3218   C   JE V3              ; HIGH RES
124C B0 3E 0449 R  06 3219   C   MOV CL,4           ; SHIFT VALUE FOR HIGH RES
1253 B1 D4          3220   C   SAL AH,1           ; COLUMN VALUE *2 FOR HIGH RES
1255 D0 E4          3221   C   ;----- 3222   C   3222   C   NOT_HIGH_RES
1257 D3 E3          3223   C   ;----- 3224   C   3224   C   *16 FOR HIGH RES
1259 B0 DH          3225   C   ;----- DETERMINE ALPHA CHAR POSITION
1259 B0 DH          3226   C   MOV DL,AH          ; COLUMN VALUE FOR RETURN
1259 B0 F0          3227   C   MOV DH,AL          ; DIVIDE BY 4
1259 D0 EE          3228   C   SHR DH,1           ; FOR VALUE IN 0-24 RANGE
1259 D0 EE          3229   C   SHR DH,1           ; LIGHT_PEN_RETURN_SET
1261 E6 2C 90          3230   C   ;----- 3231   C   3231   C   V8:          MOV V5
1264 99          3232   C   ;----- 3233   C   3233   C   ;----- NEW GRAPHICS MODES
1265 F7 36 044A R  3234   C   CMP CRT_COLS ; PREPARE TO DIVIDE
1265 F7 36 044A R  3235   C   DIV DL,DH          ; AX = ROW, DX = COLUMN
1269 B8 DA          3236   C   ;----- 3237   C   3237   C   SAVE REMAINDER
1268 D3 E3          3238   C   MOV BX,DH          ; PEL COLUMN
1268 B8 C8          3239   C   SAL BX,CL          ; PEL ROW
1270 99          3240   C   MOV CX,AX          ; SHIFT ROW
1270 99          3241   C   CMP CX,DH          ; SHIFT ROW DIVIDE
1271 F7 36 0485 R  3242   C   DIV POINTS ; PREPARE TO DIVIDE
1275 5A          3243   C   POP DX             ; DIVIDE BY BYTES/CHAR
1276 B8 F0          3244   C   MOV DH,AL          ; RECOVER
1278 EB 15 90          3245   C   JMP V5              ; CHARACTER ROW
1278 EB 15 90          3246   C   ;----- 3247   C   ;----- ALPHA MODE ON LIGHT PEN
1278 EB 15 90          3248   C   ;----- 3249   C   ;----- V4:          ALPHA_PEN
1278 F6 36 044A R  3249   C   DIV BYTE_PTR_CRT_COLS ; ROW, COLUMN VALUE
1278 F6 36 044A R  3250   C   MOV DH,AL          ; ROWS TO DH
1278 F6 36 044A R  3251   C   ;----- 3252   C   3252   C   COLS TO DL
1281 B8 D4          3252   C   MOV DL,AH          ; COLUMN VALUE
1281 B8 D4          3253   C   MOV BL,BH          ; TO BX
1285 32 FF          3254   C   MOV BH,BH          ; GET BASE ADDRESS
1287 D3 F5          3255   C   SAL BX,CL          ; POINT TO RESET PARM
1289 F6 26 0485 R  3256   C   MUL BYTE_PTR_POINTS ; ADDRESS, NOT DATA,
1289 F6 26 0485 R  3257   C   MOV CX,AX          ; IS IMPORTANT
128F B4 01          3258   C   ;----- 3259   C   RECOVER VALUE
128F B4 01          3259   C   MOV AH,1              ; RETURN_NO_RESET
1291 52          3260   C   ;----- 3261   C   ;----- V5:          MOV DX,ADDR_6845
1291 52          3261   C   ;----- 3262   C   3262   C   ADD DX,7           ; POINT TO RESET PARM
1291 52          3263   C   OUT DX,AL          ; ADDRESS, NOT DATA,
1292 B8 16 0463 R  3264   C   ;----- 3265   C   3265   C   IS IMPORTANT
1292 B8 16 0463 R  3265   C   ;----- 3266   C   3266   C   RECOVER VALUE
1292 B8 16 0463 R  3267   C   ;----- 3267   C   3267   C   RETURN_NO_RESET
1298 5A          3268   C   ;----- 3268   C   3268   C   ;----- V7:          READ_LPEN
1298 5F          3269   C   ;----- 3269   C   3269   C   3269   C   ;----- V7:          READ_LPEN
129C 5E          3270   C   ;----- 3270   C   3270   C   3270   C   ;----- V7:          READ_LPEN
12A0 B3 C4 06          3271   C   ;----- 3271   C   3271   C   3271   C   ;----- V7:          READ_LPEN
12A1 07          3272   C   ;----- 3272   C   3272   C   3272   C   ;----- V7:          READ_LPEN
12A2 50          3273   C   ;----- 3273   C   3273   C   3273   C   ;----- V7:          READ_LPEN
12A2 50          3274   C   ;----- 3274   C   3274   C   3274   C   ;----- V7:          READ_LPEN
12A2 50          3275   C   ;----- 3275   C   3275   C   3275   C   ;----- V7:          READ_LPEN
12A2 50          3276   C   ;----- 3276   C   3276   C   3276   C   ;----- V7:          READ_LPEN

```

```

3277 C
3278 C
3279 C ; ACT_DISP_PAGE  SELECT ACTIVE DISPLAY PAGE
3280 C THIS ROUTINE SETS THE ACTIVE DISPLAY PAGE, ALLOWING
3281 C FOR MULTIPLE PAGES OF DISPLAYED VIDEO.
3282 C
3283 C INPUT  AL HAS THE NEW ACTIVE DISPLAY PAGE
3284 C
3285 C OUTPUT THE CRTC IS RESET TO DISPLAY THAT PAGE
3286 C
3287 C
1244 1244 A2 0462 R 3288 C AH5: MOV ACTIVE_PAGE,AL ; SAVE ACTIVE PAGE VALUE
1244 1244 B8 0E 044C R 3289 C MOV CX,CRT_LEN ; GET_SAVED_LENGTH_OF
3290 C
1244 1244 CBW ; REGEN BUFFER
1244 1244 50 ; CONVERT AL TO WORD
1244 1244 F7 E1 3292 C PUSH AX ; SAVE PAGE VALUE
3293 C MUL CX ; DIVIDE PAGE TIMES
3294 C
1244 1244 A3 044E R 3295 C MOV CRT_START,AX ; REGEN LENGTH
3296 C
1244 1244 BB C8 3297 C MOV CX,AX ; SAVE START ADDRESS FOR
1244 1244 B8 1E 0449 R 3298 C MOV BL,CRT_MODE ; LATER REQUIREMENTS
1244 1244 80 FB 07 3299 C CMP BL,7 ; START ADDRESS TO CX
1244 1244 77 02 3300 C JA ADP_1 ; DO NOT DIVIDE BY TWO
1244 1244 D1 F9 3301 C ADP_2: SAR CX,1 ; / 2 FOR CRTC HANDLING
1244 1244 3302 C ADP_1: MOV AH,C_STRT_HGH ; REG FOR START ADDRESS
1244 1244 BB 0C 3304 C CALL M16 ; INCLUDE VSCROLL.INC
1244 1244 E8 1135 R 3305 C POP BX ; SUBTTL VSCROLL.INC
1244 1244 50 ; PAGE
1244 1244 D1 E3 3306 C
1244 1244 BB 87 0450 R 3307 C SAD BX,1 ; RECOVER PAGE VALUE
1244 1244 E8 1175 R 3308 C MOV AX,[BX + OFFSET_CURSOR_POSN] ; #2 FOR WORD OFFSET
1244 1244 E9 219E R 3309 C CALL M18 ; GET CURSOR FOR THIS PAGE
3310 C JMP V_RET ; SET THE CURSOR POSITION
3311 C
3312 C SUBTTL
3313 C
3314 C INCLUDE VSCROLL.INC
3315 C SUBTTL VSCROLL.INC
3316 C PAGE
3317 C
1201 1201 50 3318 C FLTA PROC NEAR ; CHECK FOR SCROLL COUNT
3319 C PUSH AX
3320 C MOV AL, DH ; LOWER ROW
3321 C SUB AH, CH ; UPPER ROW
3322 C INC AH ; NUMBER TO SCROLL
3323 C CMP AH, AL ; SAME AS REQUESTED
3324 C POP AX
3325 C JNE LTA ; YES, SET TO 0 FOR BLANK
3326 C SUB AL, AL
3327 C LTA: RET
3328 C FLTA ENDP
3329 C
1200 1200 C CRANK PROC NEAR ; MOVE ROWS OF PELS UP
3330 C
3331 C CRANK ASSUME DS:ABSO ; MOVE DATA SEGMENT
3332 C PUSH BX ; SET DATA SEGMENT
3333 C ASSUME DS:ABSO ; SAVE DATA SEGMENT
3334 C PUSH DS ; SET DATA SEGMENT
3335 C CALL DDS ; MOVE THAT ROW
3336 C MOV BX,CRT_COLS ; RECOVER POINTERS
3337 C POP DS ; MOVE THAT ROW
3338 C CRANK_A: PUSH CX ; CLEAR HIGH BYTE
3339 C MOV CL, DL ; SAVE POINTERS
3340 C SUB CH, CH
3341 C PUSH SI
3342 C PUSH DI
3343 C REP MOVS ; MOVE THAT ROW
3344 C POP DI ; RECOVER POINTERS
3345 C POP SI
3346 C ADD DI, BX ; NEXT ROW
3347 C ADD DI, BX ; NEXT ROW
3348 C POP CX ; RECOVER ROW COUNT
3349 C LOOP CRANK_A ; DO MORE
3350 C POP BX ; RETURN TO CALLER
3351 C RET
3352 C CRANK ENDP ; RETURN TO CALLER
3353 C
3354 C CRANK_4 PROC NEAR ; MOVE ROWS OF PELS DOWN
3355 C PUSH BX
3356 C ASSUME DS:ABSO ; MOVE DATA SEGMENT
3357 C PUSH DS ; SET DATA SEGMENT
3358 C CALL DDS ; MOVE DATA SEGMENT
3359 C MOV BX,CRT_COLS ; SET DATA SEGMENT
3360 C POP DS
3361 C CRANK_B: PUSH CX ; SAVE MOVE COUNT
3362 C MOV CL, DL ; COLUMN COUNT
3363 C SUB CH, CH ; CLEAR HIGH BYTE
3364 C PUSH SI ; SAVE POINTERS
3365 C PUSH DI
3366 C REP MOVS ; MOVE THAT ROW
3367 C POP DI ; RECOVER POINTERS
3368 C POP SI
3369 C ADD SI, BX ; NEXT ROW
3370 C SUB SI, BX ; NEXT ROW
3371 C POP CX ; RECOVER ROW COUNT
3372 C ADD DI, BX ; DO MORE
3373 C POP CX ; RETURN TO CALLER
3374 C LOOP CRANK_B ; RETURN TO CALLER
3375 C POP BX ; FILL ROW AFTER SCROLL
3376 C RET
3377 C CRANK_ENDP ; FILL ROW AFTER SCROLL
3378 C
3379 C PART_1 PROC NEAR ; FILL ROW AFTER SCROLL
3380 C PUSH DX ; FILL ROW AFTER SCROLL
3381 C MOV DH, 3 ; FILL ROW AFTER SCROLL
3382 C MOV AX, SEQ_ADDR ; SEQUENCER
3383 C MOV AX, 0000FH ; MAP MASK
3384 C CALL OUT_DX ; ALL MAPS ON
3385 C POP DX ; ZERO
3386 C SUB AX, AX ; COLUMN COUNT
3387 C MOV DL, DL ; CLEAR ONE ROW OF PELS
3388 C SUB CH, CH ; RECOVER POINTER
3389 C PUSH DI ; GET COLOR VALUE
3390 C REP STOSB ; SEQUENCER
3391 C POP DI ; MAP MASK
3392 C MOV AL, DH ; SET THE COLOR
3393 C PUSH DX ; ALL BITS ON
3394 C MOV DH, 3 ; COLUMN COUNT
3395 C MOV DL, SEQ_ADDR ; RECOVER POINTER
3396 C MOV AH, 02H ; SET THE COLOR
3397 C CALL OUT_DX ; SAVE POINTER
3398 C POP DX ; TURN ON THOSE BITS IN
3399 C MOV AL, OFFH ; COLUMN COUNT
3400 C MOV DL, DL ; RECOVER POINTER
3401 C PUSH DI ; GET COLOR VALUE
3402 C REP STOSB ; SEQUENCER

```

```

3403 C          POP    DI          ; ENABLED PLANES
3404 C          RET    DS          ; RECOVER POINTER
3405 C          ENDP
3406 C          PART_1 PROC   NEAR
3407 C          ENDP
3408 C          PART_2 PROC   NEAR
3409 C          MOV    DH, 3        ; SEQUENCER
3410 C          MOV    DL, SEQ        ; MAP MASK, ALL MAPS
3411 C          MOV    AX, 020FH      ; ENABLE THE MAPS
3412 C          CALL   OUT_DX      ; RETURN TO CALLER
3413 C          RET
3414 C          ENDP
3415 C          BLNK_3 PROC   NEAR
3416 C          ENDP
3417 C          ASSUME DS:AB50      ; BLANK FOR SCROLL UP
3418 C          CALL   DS:AB50      ; SAVE DATA SEGMENT
3419 C          CALL   DDS
3420 C          MOV    DH, BH
3421 C          SUB   BH, BH
3422 C          PUSH  AX
3423 C          PUSH  DX
3424 C          MOV    AX, BX
3425 C          MUL   POINTS
3426 C          MOV    BX, AX
3427 C          POP   DX
3428 C          POP   AX
3429 C
3430 C          POP    DS          ; GET LOW MEMORY SEGMENT
3431 C          ASSUME DS:NOTHING ; ATTRIBUTE FOR BLANK LINE
3432 C          S13: CALL   PART_1      ; CLEAR HIGH BYTE
3433 C          ASSUME DS:AB50      ; SAVE SEGMENT
3434 C          CALL   DS:AB50      ; LOW MEMORY SEGMENT
3435 C          CALL   DDS
3436 C          ADD   DI,CRT_COLS
3437 C          DEC   BX
3438 C          JNZ   S13
3439 C          DEC   BX
3440 C          JNZ   S13
3441 C          CALL   PART_2      ; SAVE BECAUSE OF MULTIPLY
3442 C          RET
3443 C          BLNK_3 ENDP
3444 C
3445 C          BLNK_4 PROC   NEAR
3446 C          PUSH  DS          ; BLANK FOR SCROLL DOWN
3447 C          ASSUME DS:AB50      ; SAVE DATA SEGMENT
3448 C          CALL   DDS
3449 C          SUB   BH, BH
3450 C          PUSH  AX
3451 C          PUSH  DX
3452 C          MOV    AX, BX
3453 C          MUL   POINTS
3454 C          MOV    BX, AX
3455 C          POP   DX
3456 C          POP   AX
3457 C
3458 C          POP    DS          ; LOW MEMORY SEGMENT
3459 C          ASSUME DS:NOTHING ; ATTRIBUTE FOR BLANK LINE
3460 C          S13_4: CALL   PART_1      ; CLEAR HIGH BYTE
3461 C          ASSUME DS:AB50      ; SAVE SEGMENT
3462 C          CALL   DS:AB50      ; LOW MEMORY SEGMENT
3463 C          CALL   DDS
3464 C          SUB   DS,CRT_COLS
3465 C          POP   DS
3466 C          DEC   BX
3467 C          JNZ   S13_4
3468 C          DEC   BX
3469 C          JNZ   S13_4
3470 C          CALL   PART_2      ; SAVE BECAUSE OF MULTIPLY
3471 C          RET
3472 C          BLNK_4 ENDP
3473 C
3474 C          SCROLL_UP
3475 C          THIS ROUTINE MOVES A BLOCK OF CHARACTERS UP
3476 C          ON THE SCREEN
3477 C
3478 C          INPUT
3479 C          (AH) = CURRENT CH MODE
3480 C          (CX) = ROW/COLUMN TO SCROLL
3481 C          (DX) = ROW/COLUMN OF UPPER LEFT CORNER
3482 C          (BH) = ROW/COLUMN OF LOWER RIGHT CORNER
3483 C          (DS) = DATA SEGMENT
3484 C          (ES) = REGEN BUFFER SEGMENT
3485 C
3486 C          OUTPUT
3487 C          NONE -- THE REGEN BUFFER IS MODIFIED
3488 C
3489 C          ASSUME CS:CODE, DS:AB50, ES:NOTHING
3490 C          SCROLL_UP PROC   NEAR
3491 C          MOV    BL, AL      ; SAVE LINE COUNT IN BL
3492 C          CALL   MK_ES      ; TEST FOR GRAPHICS MODE
3493 C          CMP   AX, '4      ; HANDLE SEPARATELY
3494 C          JE    N1
3495 C          CMP   AH, 7      ; TEST FOR BW CARD
3496 C          JE    N1
3497 C          JMP   GRAPHICS_UP
3498 C
3499 C          N1: PUSH  BX      ; UP_CONTINUE
3500 C          MOV    AX, CX      ; SAVE FILL ATTR IN BH
3501 C          CALL   SCROLL_POSITION ; UPPER LEFT POSITION
3502 C          ADD   SI, BX      ; DO SETUP FOR SCROLL
3503 C          ADD   SI, AX      ; BLANK LINE
3504 C          MOV    AH, DH      ; FROM ADDRESS
3505 C          SUB   AH, BL      ; # ROWS IN BLOCK
3506 C
3507 C          N2: CALL   N10      ; # ROWS TO BE MOVED
3508 C          ADD   SI, BP      ; ROW_LOOP
3509 C          ADD   DI, BP      ; MOVE ONE ROW
3510 C          DEC   AH
3511 C          JNZ   N2
3512 C
3513 C          N3: POP   AX      ; NEXT LINE IN BLOCK
3514 C          MOV    AL, ' '
3515 C
3516 C          N4: CALL   N11      ; COUNT OF LINES TO MOVE
3517 C          ADD   DI, BP      ; ROW_LOOP
3518 C          DEC   BL
3519 C          JNZ   N4
3520 C
3521 C          N5: CALL   DDS      ; CLEAR ENTRY
3522 C          CMP   CRT_MODE, 7 ; RECOVER ATTRIBUTE IN AH
3523 C          JE    N6
3524 C          MOV    AL, CRT_MODE_SET ; FILL WITH BLANKS
3525 C          MOV    DX, 030BH    ; CLEAR THE ROW
3526 C          OUT   DX, AL      ; POINT TO NEXT LINE
3527 C          OUT   DX, AL      ; LINES TO SCROLL
3528 C          SCROLL_END
3529 C
3530 C          N6: JMP   V_RET      ; IS THIS THE B/W CARD
3531 C          SKIP THE MODE RESET
3532 C          GET THE MODE SET
3533 C          ALWAYS SET COLOR CARD
3534 C
3535 C          N6: VIDEO_RET_HERE

```

```

13EE 3529 C N7: ; BLANK_FIELD
13EE 8A DE 3530 C MOV BL, DH ; GET ROW COUNT
13F0 EB DC 3531 C JMP N3 ; GO CLEAR THAT AREA
13F2 3532 C SCROLL_UP ENDP

3533 C ;----- HANDLE COMMON SCROLL SET UP HERE
3534 C
3535 C SCROLL_POSITION PROC NEAR
3536 C TEST INFO,4
3537 C JZ N9
3538 C
3539 C ;----- 80X25 COLOR CARD SCROLL
3540 C
3541 C PUSH DX ; COLOR CARD HERE
3542 C MOV DH, 3 ; WAIT DISP_ENABLE
3543 C MOV DL, DDAH ; WAIT DISP_ENABLE
3544 C PUSH AX ; COLOR CARD HERE
3545 C
3546 C NB: IN AL, DX ; WAIT DISP_ENABLE
3547 C TEST AL, 8 ; WAIT FOR VERT RETRACE
3548 C JZ N9 ; WAIT DISP_ENABLE
3549 C PUSH AX ; COLOR CARD HERE
3550 C MOV AL, 25H ; DX=3D8
3551 C MOV DL, DDBH ; TURN OFF VIDEO
3552 C OUT DX, AL ; DURING VERTICAL RETRACE
3553 C POP AX
3554 C POP DX
3555 C
3556 C N9: CALL POSITION ; CONVERT TO REGEN POINTER
3557 C ADD AX, CRT_START ; OFFSET OF ACTIVE PAGE
3558 C PUSH DI ; TO ADDRESS FOR SCROLL
3559 C MOV SI, AX ; FROM ADDRESS FOR SCROLL
3560 C SUB DX, CX ; DX = #ROWS, #COLUMNS
3561 C INC DI ; INCR. #COLUMNS
3562 C XOR CH, CH ; INCR. #ROWS
3563 C MOV BP, CRT_COLS ; NUM OF COLUMNS IN DISPLAY
3564 C ADD BP, BP ; GET LINE COUNT
3565 C MOV AX, BL ; TIMES 2 FOR ATTR BYTE
3566 C ADD AX, AX ; GET LINE COUNT
3567 C MUL BYTE PTR CRT_COLS ; OFFSET TO FROM ADDRESS
3568 C ADD AX, AX ; #2 FOR ATTRIBUTE BYTE
3569 C PUSH ES ; ESTABLISH ADDRESSING
3570 C POP DS ; FOR BOTH POINTERS
3571 C ADD BP, BL, 0 ; 0 MEANS BLANK FIELD
3572 C RET ; RETURN WITH FLAGS SET
3573 C
3574 C SCROLL_POSITION ENDP
3575 C
3576 C ;----- MOVE_ROW
3577 C
3578 C N10: PROC NEAR ; GET # OF COLUMNS TO MOVE
3579 C MOV CL, DL ; SAVE START ADDRESS
3580 C PUSH SI ; MOVE THAT LINE ON SCREEN
3581 C REP MOVS ; RECOVER ADDRESSES
3582 C POP DI
3583 C POP SI
3584 C RET
3585 C ENDP
3586 C
3587 C ;----- CLEAR_ROW
3588 C
3589 C N11: PROC NEAR ; GET # COLUMNS TO CLEAR
3590 C MOV CL, DL ; STORE THE FILL CHARACTER
3591 C PUSH DI
3592 C REP STOSB
3593 C POP DI
3594 C RET
3595 C ENDP
3596 C
3597 C
3598 C ;----- SCROLL_DOWN
3599 C THIS ROUTINE MOVES THE CHARACTERS WITHIN A
3600 C DEFINED BLOCK DOWN ON THE SCREEN, FILLING THE
3601 C TOP LINES WITH A DEFINED CHARACTER
3602 C INPUT
3603 C (AH) = CURRENT CRT MODE
3604 C (AL) = NUMBER OF LINES TO SCROLL
3605 C (CX) = UPPER LEFT CORNER OF REGION
3606 C (DX) = LOWER RIGHT CORNER OF REGION
3607 C (ES) = DATA SEGMENT
3608 C (DS) = DATA SEGMENT
3609 C (ES) = REGEN SEGMENT
3610 C
3611 C OUTPUT
3612 C NONE -- SCREEN IS SCROLLED
3613 C SCROLL_DOWN PROC NEAR
3614 C STD ; SCROLL DOWN
3615 C MOV BL, AL ; LINE COUNT TO BL
3616 C CALL MK_ES ; SAVE ATTRIBUTE IN BH
3617 C PUSH BX ; LOWER RIGHT CORNER
3618 C MOV AX, DX ; GET REGEN LOCATION
3619 C CALL SCROLL_POSITION ; GET REGEN LOCATION
3620 C SUB SI, AX ; SI IS FROM ADDRESS
3621 C MOV AH, DH ; GET TOTAL # ROWS
3622 C SUB AH, BL ; COUNT TO MOVE IN SCROLL
3623 C
3624 C N13: CALL N10 ; MOVE ONE ROW
3625 C SUB SI, BP ; LINE COUNT TO BL
3626 C SUB DI, BP ; SAVE ATTRIBUTE IN BH
3627 C DEC AH ; LOWER RIGHT CORNER
3628 C JNZ N13 ; GET REGEN LOCATION
3629 C
3630 C N14: POP AX ; RECOVER ATTRIBUTE IN AH
3631 C MOV AL, 1 ; CLEAR ONE ROW
3632 C
3633 C N15: CALL N11 ; GO TO NEXT ROW
3634 C SUB DI, BP ; CLEAR ONE ROW
3635 C DEC BL ; GO TO NEXT ROW
3636 C JNZ N15 ; CLEAR ONE ROW
3637 C
3638 C N16: MOV BL, DH ; SCROLL_END
3639 C JMP N14 ; SCROLL_END
3640 C
3641 C SCROLL_DOWN ENDP
3642 C
3643 C
3644 C ;----- SCROLL_UP
3645 C THIS ROUTINE SCROLLS UP THE INFORMATION ON THE CRT
3646 C ENTRY
3647 C CH, CL = UPPER LEFT CORNER OF REGION TO SCROLL
3648 C DH, DL = LOWER RIGHT CORNER OF REGION TO SCROLL
3649 C BOTH OF THE ABOVE ARE IN CHARACTER POSITIONS
3650 C BH = FULL VALUE FOR BLANKED LINES
3651 C AL = # LINES TO SCROLL (AL=0 MEANS BLANK THE ENTIRE
3652 C FIELD)
3653 C DS = DATA SEGMENT
3654 C

```

```

3655 C ; EXIT      ES = REGEN SEGMENT
3656 C ; NOTHING, THE SCREEN IS SCROLLED
3657 C -----
3658 C ;----- GRAPHICS_UP PROC NEAR
3659 C     MOV     BL, AL      ; SAVE LINE COUNT IN BL
3660 C     MOV     AX, CX      ; GET UPPER LEFT POSITION
3661 C     MOV     AX, CX      ; INTO AX REG
3662 C
3663 C
3664 C ;----- USE CHARACTER SUBROUTINE FOR POSITIONING
3665 C ;----- ADDRESS RETURNED IS MULTIPLIED BY 2 FROM CORRECT VALUE
3666 C
3667 C     CALL    GRAPH_POS
3668 C     MOV     DI, AX      ; SAVE RESULT AS
3669 C
3670 C ;----- DETERMINE SIZE OF WINDOW
3671 C
3672 C     SUB    DX, CX
3673 C     ADD    DX, 101H      ; ADJUST VALUES
3674 C     SAL    DH, 1        ; MULTIPLY # ROWS BY 4
3675 C
3676 C     SAL    DH, 1        ; SINCE 8 VERT DOTS/CHAR
3677 C
3678 C ;----- DETERMINE CRT MODE
3679 C
3680 C     CMP    CRT_MODE, 6  ; TEST FOR MEDIUM RES
3681 C     JNC    R7          ; FIND_SOURCE
3682 C
3683 C ;----- MEDIUM RES UP
3684 C
3685 C     SAL    DL, 1        ; * 2,
3686 C     SAL    DL, 1        ; SINCE 2 BYTES/CHAR
3687 C
3688 C ;----- DETERMINE THE SOURCE ADDRESS IN THE BUFFER
3689 C
3690 C
3691 C R7:    PUSH   ES      ; FIND_SOURCE
3692 C     POP    DS      ; GET SEGMENTS BOTH
3693 C     SUB    CH, CH      ; POINTING TO REGEN
3694 C     SAL    BL, 1        ; 0 TO HIGH OF COUNT REG
3695 C     SAL    BL, 1        ; NUMBER OF LINES *4
3696 C
3697 C     JZ    R11          ; IF 0, BLANK ENTIRE FIELD
3698 C     MOV    AL, BL      ; NUMBER OF LINES IN AL
3699 C     MOV    AH, 80      ; 80 BYTES/ROW
3700 C     MUL   AH, AH      ; OFFSET TO SOURCE
3701 C     MOV    SI, DI      ; SET UP SOURCE
3702 C     ADD    SI, AX      ; ADD IN OFFSET TO IT
3703 C     MOV    AH, DH      ; NUMBER OF ROWS IN FIELD
3704 C     SUB    AH, BL      ; DETERMINE NUMBER TO MOVE
3705 C
3706 C ;----- LOOP THROUGH, MOVING ONE ROW AT A TIME, BOTH EVEN AND ODD FIELDS
3707 C
3708 C R8:    CALL   R17          ; ROW_LOOP
3709 C     SUB    SI, 2000H-80  ; MOVE ONE ROW
3710 C     SUB    DI, 2000H-80  ; MOVE TO NEXT ROW
3711 C     DEC    AH          ; NUMBER OF ROWS TO MOVE
3712 C     JNZ    R8          ; CONTINUE TILL ALL MOVED
3713 C
3714 C ;----- FILL IN THE VACATED LINE(S)
3715 C
3716 C R9:    MOV    AL, BH      ; CLEAR_ENTRY
3717 C
3718 C R10:   CALL   R18          ; ATTRIBUTE TO FILL WITH
3719 C     SUB    DI, 2000H-80  ; CLEAR THAT ROW
3720 C     REP    MOVSB        ; POINT TO NEXT LINE
3721 C     DEC    AH          ; NUMBER OF LINES TO FILL
3722 C     JNZ    R10          ; CLEAR_LOOP
3723 C
3724 C R11:   MOV    BL, DH      ; BLANK_FIELD
3725 C     ADD    SI, 2000H      ; SET COUNT TO
3726 C     ADD    DI, 2000H      ; EVERYTHING IN FIELD
3727 C     RET    R9          ; CLEAR THE FIELD
3728 C
3729 C GRAPHICS_UP ENDP
3730 C
3731 C ;----- ROUTINE TO MOVE ONE ROW OF INFORMATION
3732 C
3733 C R17:   PROC   NEAR
3734 C     MOV    CL, DL      ; NUM OF BYTES IN THE ROW
3735 C     PUSH   SI          ; SAVE POINTERS
3736 C     PUSH   DI          ; MOVE THE EVEN FIELD
3737 C     REP    MOVSB        ; POINT TO THE ODD FIELD
3738 C     POP    DI          ; SAVE THE POINTERS
3739 C     ADD    SI, 2000H      ; COUNT BACK
3740 C     ADD    DI, 2000H      ; MOVE THE ODD FIELD
3741 C     RET    R17          ; POINTERS BACK
3742 C     ADD    DI, 2000H      ; RETURN TO CALLER
3743 C
3744 C R18:   PROC   NEAR
3745 C     MOV    CL, DL      ; NUMBER OF BYTES IN FIELD
3746 C     PUSH   DI          ; SAVE POINTER
3747 C     REP    STOSB        ; STORE THE NEW VALUE
3748 C     ADD    DI, 2000H      ; POINTER BACK
3749 C     PUSH   DI          ; POINT TO ODD FIELD
3750 C     RET    R18          ; FILL THE ODD FIELD
3751 C
3752 C ;----- CLEAR A SINGLE ROW
3753 C
3754 C R18:   PROC   NEAR
3755 C     MOV    CL, DL      ; NUMBER OF BYTES IN FIELD
3756 C     REP    STOSB        ; SAVE POINTER
3757 C     ADD    DI, 2000H      ; STORE THE NEW VALUE
3758 C     PUSH   DI          ; POINTER BACK
3759 C     ADD    DI, 2000H      ; POINT TO ODD FIELD
3760 C     REP    STOSB        ; FILL THE ODD FIELD
3761 C     ADD    DI, 2000H      ; RETURN TO CALLER
3762 C
3763 C R18:   ENDP
3764 C
3765 C MEM_DET PROC NEAR
3766 C     ASSUME DS:ABSO
3767 C     PUSH  AX
3768 C     PUSH  DS
3769 C     PUSH  DI
3770 C     CALL  DS
3771 C     MOV   AH, INFO
3772 C     AND  AH, 060H
3773 C     POP  DS
3774 C     POP  AX
3775 C     XOR  AX, MIN
3776 C     STC
3777 C     RET
3778 C MIN:    CLC
3779 C
3780 C     RET

```

```

1508 3781 C MEM_DET ENDP
1508 3782 C ;----- SCROLL ACTIVE PAGE UP
1508 3783 C SC_2:
1508 3784 C JMP SCROLL_UP
1508 3785 C
1508 3786 C AH6:
1508 3787 C ASSUME DS:ABSO
1508 3788 C CALL FILEH
1508 3789 C MOV AH,CRT_MODE
1508 3790 C CMP AH,07H ; GET CURRENT MODE
1508 3791 C JBE SC_2 ; ANY OF THE OLD MODES
1508 3792 C CMP AH,0FH ; NEW GRAPHICS MODES
1508 3793 C JAE GRAPHICS_UP_2 ; NOT A RECOGNIZED MODE
1508 3794 C
1508 3795 C JMP V_RET
1508 3796 C
1508 3797 C GR_ST_1 PROC NEAR
1508 3798 C ASSUME DS:ABSO ; REGEN BUFFER
1508 3799 C MOV DX,0A000H ; GRAPHICS WRITE MODE
1508 3800 C MOV BF,0501H
1508 3801 C CMP AH,0FH
1508 3802 C JBE CALL_MEM_DET
1508 3803 C INT 21H
1508 3804 C JNC V_RET
1508 3805 C MOV BF,0501H ; GRAPHICS WRITE MODE
1508 3806 C
1508 3807 C VV1: RET
1508 3808 C GR_ST_1 ENDP
1508 3809 C
1536 3810 C GRAPHICS_UP_2 PROC NEAR
1536 3811 C ASSUME DS:ABSO ; SET SEGMENT, WRITE MODE
1536 3812 C PUSH DX ; SET REGEN
1536 3813 C CALL GR_ST_1
1536 3814 C SRLLOAD ES,DX
1536 3815 C+ MOV ES,DX
1536 3816 C POP DX ; NUMBER OF LINES
1536 3817 C MOV BL,AL ; UPPER LEFT CORNER
1536 3818 C PUSH BX
1536 3819 C MOV AX,CX
1536 3820 C PUSH BX ; ACTIVE PAGE FOR SCROLL
1536 3821 C CALL GRX_PSN ; ADDRESS IN REGEN
1536 3822 C POP BX
1536 3823 C MOV DI,AX ; SET POINTER
1536 3824 C SUB DX,CX ; DETERMINE WINDOW
1536 3825 C ADD DX,0101H ; ADJUST
1536 3826 C SUB AX,DX ; ZERO HIGH BYTE
1536 3827 C MOV AL,DL ; LINE COUNT
1536 3828 C PUSH DX ; BYTES PER CHARACTER
1536 3829 C MUL POINTS ; COLUMNS
1536 3830 C MUL CRT_COLS ; SET UP SOURCE INDEX
1536 3831 C MOV SI,B ; SET UP
1536 3832 C ADD SI,AX ; ADJUST
1536 3833 C ASSUME DS:NOTHING
1536 3834 C PUSH ES ; LINE COUNT
1536 3835 C POP DS
1536 3836 C POP DX
1536 3837 C OR BL,BL ; LOW MEMORY SEGMENT
1536 3838 C JZ AR9
1536 3839 C MOV CL,DH
1536 3840 C SUB CL,BL
1536 3841 C SUB CL,CH
1536 3842 C
1536 3843 C ASSUME DS:ABSO ; SET THE COUNT
1536 3844 C PUSH DS
1536 3845 C CALL DDS
1536 3846 C PUSH AX
1536 3847 C PUSH DX ; BYTES PER CHAR
1536 3848 C MOV AX,CX ; SET THE COUNT
1536 3849 C HUL POINTS
1536 3850 C MOV CX,AX
1536 3851 C POP DX
1536 3852 C POP AX
1536 3853 C ASSUME DS:NOTHING
1536 3854 C POP DS ; SCROLL THE SCREEN
1536 3855 C
1536 3856 C PUSH DX
1536 3857 C MOV AX,BP
1536 3858 C MOV DH,0FH
1536 3859 C MOV DL,GRAPH_ADDR ; GRAPHICS
1536 3860 C CALL OUT_DX ; SEQUENCER
1536 3861 C MOV DL,SEQ_ADDR ; ENABLE ALL MAPS
1536 3862 C MOV AX,020FH
1536 3863 C CALL OUT_DX
1536 3864 C POP DX
1536 3865 C CALL CRANK
1536 3866 C
1536 3867 C PUSH DX ; SCROLL ACTIVE DISPLAY PAGE DOWN
1536 3868 C DEC BP
1536 3869 C MOV AX,BP
1536 3870 C MOV DH,3
1536 3871 C MOV DL,GRAPH_ADDR
1536 3872 C CALL OUT_DX ; BLANK ENTIRE WINDOW
1536 3873 C POP DX
1536 3874 C
1536 3875 C AR10: CALL BLNK_3
1536 3876 C JMP V_RET
1536 3877 C
1536 3878 C AR9: MOV BL,DH ; BLANK ENTIRE WINDOW
1536 3879 C JMP AR10
1536 3880 C GRAPHICS_UP_2 ENDP
1536 3881 C ;----- SCROLL ACTIVE DISPLAY PAGE DOWN
1536 3882 C
1536 3883 C
1536 3884 C SC_3: JMP SCROLL_DOWN
1536 3885 C
1536 3886 C AH7: ASSUME DS:ABSO
1536 3887 C CALL FILEH ; OLD COLOR ALPHA
1536 3888 C MOV AH,CRT_MODE ; MONOCHROME ALPHA
1536 3889 C CMP AH,03H ; NEW GRAPHICS MODES
1536 3890 C JAE GRAPHICS_DN_2 ; OLD GRAPHICS MODES
1536 3891 C CMP AH,0FH
1536 3892 C JBE SC_3
1536 3893 C CMP AH,07H
1536 3894 C JE SC_3
1536 3895 C
1536 3896 C CMP AH,ODH ; NEW GRAPHICS MODES
1536 3897 C JAE GRAPHICS_DN_2 ; OLD GRAPHICS MODES
1536 3898 C CMP AH,0FH
1536 3899 C JA M_0 ; NEW GRAPHICS MODES
1536 3900 C MOV AH,07H ; OLD GRAPHICS MODES
1536 3901 C INT 42H
1536 3902 C
1536 3903 C M_0: JMP V_RET ; DIRECTION TO DECREMENT
1536 3904 C
1536 3905 C GRAPHICS_DN_2 PROC NEAR
1536 3906 C STD

```

```

1503 8A D8 3907 C MOV BL,AL ; LINE COUNT
1505 52 3908 C PUSH ; SAVE LOWER RIGHT
1508 E8 1522 R 3909 C CALL GR_ST_1 ; SET REGEN SEGMENT
1509 8E C2 3910 C SRLLOAD ES
150B 5A C2 3911 C MOV ES,DX ; MOV CHAR ROW UP BY ONE
150C 8B C2 3912 C POP DX
150E FE C4 3913 C MOV AX,DX
150F 53 3914 C INC AH
1510 3F 04 62 R 3915 C PUSH BX
1555 E8 16C6 R 3916 C MOV BX,ACTIVE_PAGE
1556 5B 3917 C CALL GRX_PSN ; ADDRESS IN REGEN
1559 E8 26 04 8A R 3918 C POP BX
1559 E8 26 04 8A R 3919 C SUB AX,CRT_COLS ; ONE SCAN OVERSHOOT
155A 5B 3920 C MOV DI,AX
155F 2B D1 3921 C SUB DX,AX ; CALCULATE WINDOW
15F1 81 C2 0101 3922 C ADD DX,0101H ; ADJUST COUNT
15F2 2A E4 3923 C SUH AH,AH
15F3 8A C3 3924 C MOV AL,BL
15F4 5B 3925 C PUSH DX ; BYTES PER CHAR
15F4 27 26 04 85 R 3926 C MUL POINTS
15F5 F7 26 04 84 R 3927 C MUL CRT_COLS ; BYTES PER ROW
1604 2B F0 3928 C MOV SI,DI
1605 5B 3929 C SUB SI,AX ; SET DS TO
1606 0F 3930 C ASSUME DS:NOTHING ; THE REGEN SEGMENT
1607 5A 3931 C PUSH DS
1608 5A 3932 C CALL DDS ; SCROLL COUNT
1609 8A DB 3933 C PUSH AX ; BL,BL
160B 74 40 3934 C OR BL,BL ; BLANK ENTIRE WINDOW
160D 8A CE 3935 C JZ DXR9
160F 2A CB 3936 C MOV CL,DH
1611 2A ED 3937 C SUB CL,BL ; BYTES PER CHAR
1612 5A 3938 C SUB CH,CH
1613 1E 3939 C ASSUME DS:ABSO ; GRAPHICS
1614 0F 0CFE R 3940 C PUSH DS
1615 50 3941 C CALL DDS
1616 52 3942 C PUSH AX
1618 52 3943 C PUSH DX
1619 8B C1 3944 C PUSH DX
161A F7 26 04 85 R 3945 C MOV AX,CX ; SEQUENCER
1620 5A C6 3946 C MUL POINTS ; ENABLE ALL MAPS
1621 5A 3947 C MOV CX,AX
1622 58 3948 C POP DX
1623 1F 3949 C ASSUME DS:NOTHING ; SCROLL THE SCREEN
1624 52 3950 C POP DS
1625 8B C5 3951 C PUSH DX
1626 52 3952 C MOV AX,BP
1627 8B C5 3953 C MOV MOV BH,3
1628 8B 0D15 R 3954 C MOV DL,GRAPH_ADDR ; GRAPHICS
1629 8B C4 3955 C CALL OUT_DX
1630 8B 020F 3956 C MOV DL,SEQ_ADDR ; SCROLL COUNT
1631 8B 0D15 R 3957 C MOV AX,020FH ; BLANK ENTIRE WINDOW
1632 5A 3958 C CALL OUT_DX
1633 8B C4 3959 C POP DX
1634 5A 3960 C CALL CRANK_4
1635 E8 12FE R 3961 C POP DX
1646 52 3962 C CALL BLNK_4 ; BLANK ENTIRE WINDOW
1647 8B C5 3963 C CLD
1648 8B 03 3964 C JMP V_RET ; PAGE LOOP
1649 E8 0D15 R 3965 C MOV BL,DH ; LENGTH OF BUFFER
1650 5A 3966 C CALL DXR10 ; NUMBER OF TIMES
1651 8A CF 3967 C SUBTTL
1652 32 ED 3968 C INCLUDE VGRW.INC
1653 8B F1 3969 C SUBTTL VGRW.INC
1654 8B 03 3970 C PAGE
1655 8B C4 3971 C ASSUME DS:ABSO ; DISPLAY PAGE TO CX
1656 8B 04 50 R 3972 C FIND_POSITION PROC NEAR ; MOVE TO SI FOR INDEX
1657 8B 04 50 R 3973 C MOV CL,BH ; * 2 FOR WORD OFFSET
1658 8B 04 50 R 3974 C XOR CH,CH ; READ COUNT OF THAT PAGE
1659 8B 8B 04 50 R 3975 C MOV SI,1 ; SET START ADDRESS TO 0
1660 8B 04 50 R 3976 C XOR BX,BX ; NO PAGE
1661 8B 04 50 R 3977 C JCXZ P5 ; PAGE_LOOP
1662 03 0E 04 4C R 3978 C ADD BX,CRT_LEN ; LENGTH OF BUFFER
1663 E8 1146 R 3979 C LOOP P4 ; NUMBER OF TIMES
1664 8D 03 DB 3980 C CALL POSITION ; NO PAGE
1665 E8 0003 C ADD BX,AX ; DETERMINE LOC IN REGEN
1666 C0 0001 C RET ; ADD TO START OF REGEN
1667 4003 C FIND_POSITION ENDP ; PAGE LOOP
1668 4004 C
1669 4005 C EXPAND_MED_COLOR ; THIS ROUTINE EXPANDS THE LOW 2 BITS IN BL TO
1670 4006 C THIS ROUTINE TAKES THE BYTES IN AL AND DOUBLES ; FILL THE ENTIRE BX REGISTER
1671 4007 C ENTRY ; BX = COLOR TO BE USED ( LOW 2 BITS )
1672 4008 C EXIT ; BX = COLOR TO BE USED ( 8 REPLICATIONS OF THE
1673 4009 C 2 COLOR BITS ) ; COLOR BITS )
1674 4010 C
1675 4011 C
1676 4012 C
1677 4013 C
1678 4014 C
1679 4015 C S19 PROC NEAR ; ISOLATE THE COLOR BITS
1680 4016 C AND BL,3 ; AND COLOR VERSION
1681 4017 C MOV AL,BL ; COPY TO AL
1682 4018 C PUSH CX ; SAVE REGISTER
1683 4019 C MOV CX,3 ; NUMBER OF TIMES
1684 4020 C S20: SAL AL,1 ; LEFT SHIFT BY 2
1685 4021 C SAL AL,1 ; AND COLOR VERSION
1686 4022 C OR BL,AL ; INTO BL
1687 4023 C
1688 4024 C LOOP S20 ; FILL ALL OF BL
1689 4025 C MOV BH,BL ; FILL UPPER PORTION
1690 4026 C POP CX ; REGISTER BACK
1691 4027 C RET ; ALL DONE
1692 4028 C S19 ENDP ; EXPAND_BYT
1693 4029 C
1694 4030 C
1695 4031 C THIS ROUTINE TAKES THE BYTES IN AL AND DOUBLES ; ALL OF THE BITS, TURNING THE 8 BITS INTO
1696 4032 C

```

```

4033 C : 16 BITS. THE RESULT IS LEFT IN AX : 1
4034
4035 C S21 PROC NEAR
4036 C PUSH DX ; SAVE REGISTERS
4037 C PUSH CX
4038 C PUSH BX
4039 C SUB DX, DX ; RESULT REGISTER
4040 C MOV CX, 1 ; MASK REGISTER
4041 C S22:
4042 C MOV BX, AX ; BASE INTO TEMP
4043 C AND BX, CX ; USE MASK TO EXTRACT BIT
4044 C OR DX, BX ; PUT INTO RESULT REGISTER
4045 C SHL CX, 1 ; SHIFT BASE AND MASK BY 1
4046 C MOV BX, AX ; BASE TO TEMP
4047 C AND BX, CX ; EXTRACT THE SAME BIT
4048 C OR DX, BX ; PUT INTO RESULT
4049 C SHL CX, 1 ; SHIFT ONLY MASK NOW
4050 C MOV BX, AX ; MOVE TO BASE
4051 C MOV DX, BX ; USE MASK BIT COMING OUT
4052 C JNC S22 ; TO TERMINATE
4053 C MOV AX, DX ; RESULT TO PARM REGISTER
4054 C POP BX
4055 C POP CX ; RECOVER REGISTERS
4056 C POP DX
4057 C RET
4058 C S21 ENDP ; ALL DONE
4059 C
4060 C S26 PROC NEAR
4061 C MOV AX, CURSOR_POSN ; GET CURRENT CURSOR
4062 C GRAPH_POSN LABEL NEAR
4063 C PUSH BX
4064 C MOV BH, BH
4065 C MOV BX, AX ; SAVE REGISTER
4066 C MOV AL, AH ; COPY OF CURSOR
4067 C MUL BYTE PTR CRT_COLS ; GET ROWS TO AL
4068 C SHL AX, 1 ; MULTIPLY BY BYTES/COLUMN
4069 C SUB BH, BH ; *4 SINCE 4 ROWS/BYTE
4070 C ADD AX, BX ; ISOLATE COLUMN VALUE
4071 C POP BX ; DETERMINE OFFSET
4072 C RET ; RECOVER POINTER
4073 C S26 ENDP ; ALL DONE
4074 C
4075 C
4076 C
4077 C
4078 C
4079 C
4080 C
4081 C
4082 C
4083 C GR_CUR: ; ENTRY BH = DISPLAY PAGE
4084 C ASSUME DS:ABSO ; EXIT AX = CURSOR POSITION FOR REQUESTED PAGE
4085 C PUSH BX ; SAVE REGISTER
4086 C MOV BH, BH ; GET TO LOW BYTE
4087 C SUB BH, BH ; ZERO HIGH BYTE
4088 C SAL BX, 1 ; *2 FOR WORD COUNT
4089 C MOV AX, [BX + OFFSET CURSOR_POSN] ; CURSOR, REQUESTED PAGE
4090 C POP BX ; RECOVER REGISTER
4091 C
4092 C
4093 C
4094 C
4095 C
4096 C
4097 C
4098 C
4099 C GRX_PSN PROC NEAR
4100 C 53 ; SAVE
4101 C 51 ; SAVE
4102 C 52 ; SAVE
4103 C 2A ED ; ZERO
4104 C 40 CF ; PAGE NUMBER
4105 C 89 08 ; ROW, COLUMN
4106 C 8A C4 ; ROW
4107 C F6 26 044A R ; ROW * COLUMNS/ROW
4108 C F7 26 0485 R ; BYTES PER ROW
4109 C 2A 00 ; ZERO IN COL VALUE
4110 C 03 C3 ; ADD IN COLUMN
4111 C 8B 04 ; PAGE LENGTH
4112 C E1 04 ; NO PAGE OFFSET
4113 C 1E ; REGEN
4114 C 03 C3 ; ADD IN THE PAGE LENGTH
4115 C E2 FC ; DO FOR NUMBER OF PAGES
4116 C
4117 C
4118 C
4119 C
4120 C
4121 C
4122 C
4123 C
4124 C
4125 C
4126 C
4127 C
4128 C
4129 C
4130 C
4131 C
4132 C
4133 C
4134 C
4135 C
4136 C
4137 C
4138 C
4139 C
4140 C
4141 C
4142 C
4143 C
4144 C
4145 C
4146 C
4147 C
4148 C
4149 C
4150 C
4151 C
4152 C
4153 C
4154 C
4155 C
4156 C
4157 C
4158 C
MK_ES: ; READ_AC_CURRENT
MOV SI, 0B800H ; THIS ROUTINE READS THE ATTRIBUTE AND CHARACTER
MOV DI, EQUIP_FLAG ; AT THE CURRENT CURSOR POSITION AND RETURNS THEM
AND DI, 030H ; TO THE CALLER
DI, 030H
JMP DI, 030H
JNE P6_A
MOV SI, 0B800H
P6_A: MOV ES, SI
RET
GRX_PSN ENDP
MK_ES:
READ_AC_CURRENT ASSUME CS:CODE, DS:ABSO, ES:NOTHING
READ_AC_CURRENT PROC NEAR
4159 C INPUT ; ADDRESSING IN SI
4160 C CALL FIND_POSITION
4161 C MOV SI, BX ; ADDRESSING IN SI
4162 C CALL FIND_POSITION ; GET BASE ADDRESS
4163 C MOV DX, ADDR_6845 ; POINT AT STATUS PORT
4164 C ADD DX, 6
4165 C TEST INFO, 4

```



```

17A3 B9 0008          4285  C      MOV    CX,8          ; NUMBER OF BYTES TO MATCH
17A6 F3 / A6          4286  C      REPE   CHPSB        ; COMPARE THE 8 BYTES
17A8 5F               4287  C      POP    DI          ; RECOVER THE POINTERS
17A9 5E               4288  C      POP    SI          ; IF ZERO FLAG SET,
17AA 74 1D             4289  C      JZ    S18P        ; THEN MATCH OCCURRED
17AC FE C0             4291  C      INC    AL          ; NO MATCH, MOVE TO NEXT
17AE 83 C7 08          4292  C      ADD    DI,8        ; NEXT CODE POINT
17B1 4A               4293  C      DEC    DX          ; LOOP CONTROL
17B2 75 ED             4294  C      JNZ   S17P        ; DO ALL OF THEM
17B3 4C               4295  C
17B4 3C 00             4296  C      ;----- CHAR NOT MATCHED, MIGHT BE IN USER SUPPLIED SECOND HALF
17B5 4C               4297  C      ;----- CHAR NOT MATCHED, MIGHT BE IN USER SUPPLIED SECOND HALF
17B6 74 11             4298  C      CMP    AL,0        ; AL < 0 IF ONLY 1ST
17B7 4C               4299  C      ;----- HALF SCANNED
17B8 74 10             4300  C      JE    S18P        ; IF 0, THEN ALL HAS
17B9 4C               4301  C      ;----- BEEN SCANNED
17B8 E8 00FF R          4302  C      ASSUME DS:AB50
17B9 C4 3E 007C R          4303  C      CALL   DS,EXT_PTR
17BF 8C C0             4304  C      LES    DS,D
17C1 0B C7             4305  C      MOV    AX,ES
17C3 74 04             4306  C      OR    AX,DI
17C5 74 08             4307  C      JZ    S18P        ; GET POINTER
17C6 74 00             4308  C      MOV    AL,128      ; SEE IF THE PTR EXISTS
17C7 EB D3             4309  C      JMP   S16P        ; IF ALL 0, DOESN'T EXIST
17C8 4C               4310  C      ;----- NO SENSE LOOKING
17C9 83 C4 08             4311  C      ;----- ORIGIN FOR SECOND HALF
17C9 E9 219E R          4312  C      ;----- GO BACK AND TRY FOR IT
17C9 4C               4313  C
17C9 4C               4314  C      ADD    SP,8        ;----- CHARACTER IS FOUND ( AL=0 IF NOT FOUND )
17C9 4C               4315  C      ;----- READJUST THE STACK,
17C9 4C               4316  C      JMP   V_RET        ;----- THROW AWAY SAVE
17C9 4C               4317  C      ;----- ALL DONE
17C9 4C               4318  C
17C9 4C               4319  C
17C9 4C               4320  C      ;----- READ CHARACTER/ATTRIBUTE AT CURRENT CURSOR POSITION
17C9 4C               4321  C
17C9 4C               4322  C      ;----- READ_AC_CURRENT
17C9 4C               4323  C      AH8:   JMP   READ_AC_CURRENT
17C9 4C               4324  C
17D2 4C               4325  C      AH8:   ASSUME DS:AB50
17D2 8A 26 0449 R          4326  C      MOV    AH,CRT_MODE
17D6 80 FC 07             4327  C      CMP    AH,07H
17D9 74 F4             4328  C      JE    AH8S        ; GET THE CURRENT MODE
17DE 74 FC 03             4329  C      CMP    AH,03H
17DE 74 FC 0E             4330  C      JE    AH8S
17DE 80 FC 06             4331  C      CMP    AH,06H
17E3 77 03             4332  C      JA    Z_1
17E5 E9 1745 R             4333  C      CMP    AH,09H
17E5 4C               4334  C      JMP   GRAPHICS_READ
17E5 4C               4335  C      Z_1:   CMP    AH,0FH
17E8 80 FC 0F             4336  C      JB    GRX_RD2
17E8 72 52             4337  C      CALL   MEM_DET
17ED E8 14F7 R             4338  C      JC    GRX_RD2
17F0 72 4D             4339  C      CMP    AH,0DH
17F0 72 4A             4340  C      SHL   AL,4
17F4 80 FC 0D             4341  C      CMP    AH,0DH
17F7 73 46             4342  C      JAE   GRX_RD2
17F9 80 00             4343  C      MOV    AL,0
17FB E9 219E R          4344  C      JMP   V_RET
17FE 4C               4345  C
17FE 4C               4346  C      GRX_RD1 PROC NEAR
17FE 4C               4347  C      ASSUME DS:AB50
17FE 4C               4348  C      SRLOAD ES,0A00H      ; REGEN SEGMENT
1801 8E C2             4349  C      C+    MOV    ES,0A00H
1803 E8 16BA R             4350  C      C+    MOV    ES,0A00H
1806 BB F0             4351  C      CALL   GR_CUR
1808 BB 1E 0485 R          4352  C      MOV    SI,AX
180C 2B E3             4353  C      MOV    BX,POINTS
180E BB EC             4354  C      SUB   SP,BX
180E 4C               4355  C      MOV    BP,SP
1810 53               4356  C      ;----- GET VALUES FROM REGEN BUFFER AND CONVERT TO CODE POINT
1811 24 01             4357  C      ;----- GET VALUES FROM REGEN BUFFER AND CONVERT TO CODE POINT
1811 80 00             4358  C      ;----- GET VALUES FROM REGEN BUFFER AND CONVERT TO CODE POINT
1815 80 05             4359  C      ;----- GET VALUES FROM REGEN BUFFER AND CONVERT TO CODE POINT
1817 02 E0             4360  C      PUSH  BX          ; SAVE BYTES PER CHARACTER
1818 80 00             4361  C      AND   AL,1          ; ODD OR EVEN BYTE
1819 80 00             4362  C      MOV    CL,AL
1819 80 00             4363  C      SHL   AL,2          ; USE FOR SHIFT
1819 80 00             4364  C      MOV    AH,0C00H      ; COLOR COMPARE VALUE (C0-C2)
1819 80 00             4365  C      MOV    AH,G_COLOR
1819 80 00             4366  C      MOV    DH,3          ; (C1-C3), IF ODD BYTE
1819 80 00             4367  C      CALL   DL_GRAPH_ADDR
1819 80 00             4368  C      CALL   OUT_DX        ; COLOR COMPARE REGISTER
1822 80 0518             4369  C      MOV    AX,518H
1825 E8 0D15 R             4370  C      CALL   OUT_DX        ; SET GRAPHICS CHIP
1825 E8 0D15 R             4371  C      MOV    AX,510H      ; READ MODE
1828 26: 8A 04             4372  C      CALL   OUT_DX        ; SET GRAPHICS CHIP
1828 F6 D0             4373  C      MOV    AL,ES:[SI]      ; GET FIRST BYTE
182D 88 46 00             4374  C      NOT   AL
1831 85 00             4375  C      MOV    SS:[BP],AL
1831 03 36 044A R          4376  C      IND   BP
1835 4B               4377  C      DEC   BX
1836 75 F0             4378  C      JNZ   S12_1        ; NEXT LOCATION
1836 5B               4379  C      POP   BX
1836 4B 0510             4380  C      MOV    AX,510H      ; POINT INTO REGEN
183C EB 32 90             4381  C      MOV    AX,510H      ; LOOP CONTROL
183F 4C               4382  C      JMP   GRX_RD2        ; DO IT SOME MORE
183F 4C               4383  C
183F 4C               4384  C      GRX_RD2 PROC NEAR
183F 4C               4385  C      ASSUME DS:AB50
183F 4C               4386  C      SRLOAD ES,0A00H      ; REGEN SEGMENT
183F 4C               4387  C      C+    MOV    ES,0A00H
1842 8E C2             4388  C      C+    MOV    ES,0A00H
1843 E8 16BA R             4389  C      CALL   GR_CUR
1847 BB F0             4390  C      MOV    SI,AX
1849 BB 1E 0485 R          4391  C      MOV    BX,POINTS
184D 2B E3             4392  C      SUB   SP,BX
184F BB EC             4393  C      MOV    BP,SP
1850 53               4394  C      ;----- GET VALUES FROM REGEN BUFFER AND CONVERT TO CODE POINT
1850 4C               4395  C
1850 4C               4396  C      ;----- GET VALUES FROM REGEN BUFFER AND CONVERT TO CODE POINT
1851 B6 03             4397  C      ;----- GET VALUES FROM REGEN BUFFER AND CONVERT TO CODE POINT
1853 B2 CE             4398  C      MOV    DH,3          ; GRAPHICS CHIP
1855 BB 0508             4399  C      MOV    DL,GRAPH_ADDR
1858 E8 0D15 R             4400  C      MOV    AX,508H
1858 E8 0D15 R             4401  C      CALL   OUT_DX        ; COLOR COMPARE
185C 53               4402  C      MOV    AL,ES:[SI]      ; SET THE REGISTER
185C 4C               4403  C      NOT   AL
185C 4C               4404  C      MOV    SS:[BP],AL
185F F6 D0             4405  C      IND   BP
1861 88 46 00             4406  C      ADD   S1,CRT_COLS
1861 4C               4407  C      MOV    AX,508H
1865 03 36 044A R          4408  C      ADD   S1,CRT_COLS
1869 4B               4409  C      DEC   BX
186A 75 F0             4410  C      JNZ   S12          ; SAVE BYTES PER CHARACTER
186A 4C               4411  C
186A 4C               4412  C      ;----- GET COLOR COMPARED BYTE
186A 4C               4413  C      ;----- ADJUST
186A 4C               4414  C      ;----- SAVE IN STORAGE AREA
186A 4C               4415  C      ;----- NEXT LOCATION
186A 4C               4416  C      ;----- POINT INTO REGEN
186A 4C               4417  C      ;----- LOOP CONTROL
186A 4C               4418  C      ;----- DO IT SOME MORE

```

```

186C 5B ; RECOVER BYTES PER CHAR
186D B8 0500 ; UNDO READ MODE
1870
1870 ;----- SAVE AREA HAS CHARACTER IN IT, MATCH IT
1870 E8 0015 R 4411 C POP BX
1873 C4 3E 010C R 4412 C MOV AX,500H
1877 2B EB 4413 C GRX_RD2 ENDP
1879 BB F5 4414 C
1878 FC 4415 C GRX_RECV:
1879 00 00 4416 C ;----- SAVE AREA HAS CHARACTER IN IT, MATCH IT
1877 16 4417 C
187F 1F 4418 C
1880 BA 0100 4419 C CALL OUT_DK
1883 56 4420 C LES DI,GRX_SET
1884 57 4421 C SUB BP,BX
1885 BB CB 4422 C
1887 F3/ A6 4423 C MOV SI,BP
1888 5E 4424 C CLD
1888 74 07 4425 C MOV AL,0
1889 03 E3 4426 C PUSH SS
1890 F9 219E R 4427 C POP DS
1891 03 FB 4428 C MOV DX,256D
1892 75 EF 4429 C S17_5:
1894 04 03 4430 C PUSH SI
1895 04 03 4431 C PUSH DI
1896 E9 219E R 4432 C MOV CX,BX
1897 04 03 4433 C REPE CMPSB
1898 5E 4434 C POP DS
1899 04 03 4435 C POP SI
1899 04 04 4436 C JZ S18_5
1899 E8 00CE R 4437 C
189C BA 26 0449 R 4438 C INC AL
189D 03 FB 4439 C ADD DI,BX
1891 4A 4440 C DEC DX
1892 75 EF 4441 C JNZ S17_5
1894 04 03 4442 C
1895 04 03 4443 C ADD SP,BX
1896 E9 219E R 4444 C JMP V_RET
1899 04 04 4445 C
1899 04 04 4446 C ;----- WRITE CHARACTER/ATTRIBUTE AT CURRENT CURSOR POSITION
1899 04 04 4447 C
1899 04 04 4448 C
1899 04 04 4449 C ;----- WRITE_AC_CURRENT
1899 04 04 4450 C THIS ROUTINE WRITES THE ATTRIBUTE
1899 04 04 4451 C AND CHARACTER AT THE CURRENT CURSOR
1899 04 04 4452 C POSITION
1899 04 04 4453 C INPUT:
1899 04 04 4454 C (AH) = CURRENT CRT MODE
1899 04 04 4455 C (BH) = DISPLAY PAGE
1899 04 04 4456 C (CX) = COUNT OF CHARACTERS TO WRITE
1899 04 04 4457 C (AL) = CHAR TO WRITE
1899 04 04 4458 C (BL) = ATTRIBUTE OF CHAR TO WRITE
1899 04 04 4459 C (DS) = DATA SEGMENT
1899 04 04 4460 C (ES) = REGEN SEGMENT
1899 04 04 4461 C OUTPUT:
1899 04 04 4462 C NONE
1899 04 04 4463 C
1899 04 04 4464 C AH9:
1899 04 04 4465 C ASSUME DS:ABSO
1899 04 04 4466 C CALL DDS
1899 04 04 4467 C MOV AH,CRT_MODE
18A0 80 FC 04 4468 C
18A3 72 08 4469 C CMP AH,4 ; IS THIS GRAPHICS
18A5 80 FC 07 4470 C JC P6 ; IS THIS BW CARD
18A8 74 03 4471 C CMP AH,7
18AA EB 74 90 4472 C JE P6
18A9 04 03 4473 C JMP GRAPHICS_WRITE
18A9 04 03 4474 C P6:
18A9 04 03 4475 C CALL MK_ES
18B0 8A E3 4476 C MOV AH,BL ; GET ATTRIBUTE TO AH
18B2 50 4477 C PUSH AX ; SAVE ON STACK
18B3 51 4478 C PUSH CX ; SAVE WRITE COUNT
18B4 04 03 1651 R 4479 C CALL FIND POSITION
18B7 BB FB 4480 C MOV DI,BX ; ADDRESS TO DI REGISTER
18B9 59 4481 C POP CX ; WRITE COUNT
18B8 5B 0463 R 4482 C POP BX ; CHARACTER IN BX REG
18B8 88 16 0463 R 4483 C MOV DX,ADDR_6845 ; GET BASE ADDRESS
18B8 83 C2 06 4484 C ADD DX,6 ; POINT AT STATUS PORT
18B8 83 C2 06 4485 C
18B8 83 C2 06 4486 C ;----- WAIT FOR HORIZONTAL RETRACE
18C2 F6 06 0487 R 04 4487 C
18C7 74 0B 4488 C P7:
18C9 4489 C TEST INFO,4
18C9 4490 C JZ P9A
18C9 4491 C
18C9 EC 4492 C P8:
18CA A8 01 4493 C IN AL,DX ; GET STATUS
18CC 75 FB 4494 C TEST AL,1 ; IS IT LOW
18CE FA 4495 C JNZ P8 ; WAIT UNTIL IT IS
18CF EC 4496 C CLI ; NO MORE INTERRUPTS
18D0 A8 01 4497 C
18D2 74 FB 4498 C IN AL,DX ; GET STATUS
18D4 04 03 4499 C TEST AL,1 ; IS IT HIGH
18D5 8B C3 4500 C JZ P9 ; WAIT UNTIL IT IS
18D6 AB 4501 C
18D7 FB 4502 C MOV AX,BX ; RECOVER THE CHAR/ATTR
18D8 E2 E8 4503 C STOSW ; PUT THE CHAR/ATTR
18D9 E9 219E R 4504 C LOOP P7 ; INTERRUPTS BACK ON
18D9 E9 219E R 4505 C JMP V_RET ; AS MANY TIMES
18D9 E9 219E R 4506 C
18D9 E9 219E R 4507 C
18D9 E9 219E R 4508 C
18D9 E9 219E R 4509 C
18D9 E9 219E R 4510 C
18D9 E9 219E R 4511 C
18D9 E9 219E R 4512 C ;----- WRITE_C_CURRENT
18D9 E9 219E R 4513 C THIS ROUTINE WRITES THE CHARACTER AT
18D9 E9 219E R 4514 C THE CURRENT CURSOR POSITION, ATTRIBUTE
18D9 E9 219E R 4515 C UNCHANGED
18D9 E9 219E R 4516 C INPUT:
18D9 E9 219E R 4517 C (AH) = CURRENT CRT MODE
18D9 E9 219E R 4518 C (BH) = DISPLAY PAGE
18D9 E9 219E R 4519 C (CX) = COUNT OF CHARACTERS TO WRITE
18D9 E9 219E R 4520 C (AL) = CHAR TO WRITE
18D9 E9 219E R 4521 C (DS) = DATA SEGMENT
18D9 E9 219E R 4522 C (ES) = REGEN SEGMENT
18D9 E9 219E R 4523 C OUTPUT:
18D9 E9 219E R 4524 C NONE
18D9 E9 219E R 4525 C
18D9 E9 219E R 4526 C AHA:
18D9 E9 219E R 4527 C ASSUME DS:ABSO
18D9 E9 219E R 4528 C CALL DDS
18D9 E9 219E R 4529 C MOV AH,CRT_MODE
18E4 80 FC 04 4530 C
18E7 72 08 4531 C CMP AH,4 ; IS THIS GRAPHICS
18E9 80 FC 07 4532 C JC P10 ; IS THIS BW CARD
18E9 74 03 4533 C CMP AH,7
18EE EB 30 90 4534 C
18F1 EB 16EB R 4535 C
18F1 EB 16EB R 4536 C P10:
18F1 EB 16EB R 4537 C CALL MK_ES

```



```

1A9C 8A 04        4789  C      MOV    AL, DS:[SI]
1A9E 26: 8A 25    4790  C      MOV    AH, ES:[DI]
1A91 20: 88 05    4791  C      MOV    ES:[DI], AL
1A54 46          4792  C      INC    SI
1A55 03 F9        4793  C      ADD    DI, CX
1A57 4B          4794  C      DEC    BX
1A58 75 F2        4795  C      JNZ    SI<K
1A59 76          4796  C      ADD    DI, BX
1A5A 59          4797  C      POP    CX
1A5B 5B          4798  C      POP    BX
1A5C 28 F5        4799  C      SUB    SI, BP
1A5E 4F          4800  C      PSH    DI
1A5F 47          4801  C      INC    DI
1A60 E2 A6        4802  C      LOOP   S20A
1A62 B2 CE        4803  C      ADD    DI, BX
1A64 B8 0300      4804  C      MOV    DL, GRAPH_ADDR
1A67 E8 0D15 R    4805  C      MOV    AX, 0000H
1A6A B2 C4        4806  C      CALL   OUT_DX
1A6C B8 020F      4807  C      MOVL  DL, SEQ_ADDR
1A6D E8 0D15 R    4808  C      MOV    AX, 0020H
1A6E B8 0D15 R    4809  C      CALL   OUT_DX
1A72 E9 219E R    4810  C      JMP    V_RET
1A75              4811  C      GRX_WRT ENDP
1A75              4812  C      SUBTLL
1A75              4813  C      SUBTLL
1A75              4814
1A75              4815
1A75              4816
1A75              4817
1A75 80 3E 0463 R B4 4818
1A7A 74 09        4819
1A7C F6 06 0487 R 02 4820
1A6A B2 C4        4821
1A81 74 05        4822
1A82 C0 42        4823
1A85              4824
1A85 E9 219E R    4825
1A88 28 C0        4826
1A88 B8 E8        4827
1A8C C4 3E 04A8 R 4828
1A90 83 C7 04    4829
1A91 20: 83 24 3D 4830
1A96 B8 C0        4831
1A98 08 C7        4832
1A9A 74 01        4833
1A9C 45          4834
1A9D E8 10C0 R    4835
1AA0 0A FF        4836
1AA2 75 65        4837
1A75              4838
1A75              4839
1A75              4840
1A75              4841
1A75              4842
1A75              4843
1A75              4844
1A75              4845
1A75              4846
1A75 8A FB        4847
1A66 A0 0466 R    4848
1A79 20: 80 E3 1F 4849
1AA0 50 E3 1F    4850
1AAE 0A C3        4851
1AB0 A2 0466 R    4852
1AB3 8A DF        4853
1AB8 80 08        4854
1AB8 B0 E7        4855
1ABA 8A E8        4856
1ABC 80 E5 EF    4857
1ABC 08 ED        4858
1AC1 80 00 0F    4859
1AC4 8A FB        4860
1AC6 D3 E3        4861
1AC8 80 E3 10    4862
1ACB 80 00 07    4863
1ACD 0A DF        4864
1ACD 80 E5        4865
1AD0 A0 0449 R    4866
1AD3 3C 03        4867
1AD5 76 0E        4868
1A75              4869
1A75              4870
1A75              4871
1AD7 B4 00        4872
1AD9 8A C3        4873
1ADB E8 109F R    4874
1AEC 08 ED        4875
1AE0 74 03        4876
1AE2 26: 88 1D    4877
1AE2 4878
1AE2 4879
1AE2 4880
1AE5 80 3E 0449 R 03 4881
1AE5 77 05        4882
1ACD E8 0E9A R    4883
1AEF 72 07        4884
1AF1              4885
1AF1 B4 11        4886
1AF3 8A C3        4887
1AF4 80 00 0F    4888
1AF4 E8 109F R    4889
1AF8 08 ED        4890
1AF8 78 04        4891
1AF8 26: 88 5D 10 4892
1AF8 4893
1B00 8A DD        4894
1B02 80 E3 20    4895
1B05 B1 05        4896
1B07 D2 EB        4897
1A55              4898
1A55              4899
1A55              4900
1A55              4901
1A55              4902
1A55              4903
1A55              4904
1A55              4905
1A55              4906
1A55              4907
1A55              4908
1A55              4909
1A55              4910
1A55              4911
1A55              4912
1A55              4913
1A55              4914
1A55              4915
1A55              4916
1A55              4917
1A55              4918
1A55              4919
1A55              4920
1A55              4921
1A55              4922
1A55              4923
1A55              4924
1A55              4925
1A55              4926
1A55              4927
1A55              4928
1A55              4929
1A55              4930
1A55              4931
1A55              4932
1A55              4933
1A55              4934
1A55              4935
1A55              4936
1A55              4937
1A55              4938
1A55              4939
1A55              4940
1A55              4941
1A55              4942
1A55              4943
1A55              4944
1A55              4945
1A55              4946
1A55              4947
1A55              4948
1A55              4949
1A55              4950
1A55              4951
1A55              4952
1A55              4953
1A55              4954
1A55              4955
1A55              4956
1A55              4957
1A55              4958
1A55              4959
1A55              4960
1A55              4961
1A55              4962
1A55              4963
1A55              4964
1A55              4965
1A55              4966
1A55              4967
1A55              4968
1A55              4969
1A55              4970
1A55              4971
1A55              4972
1A55              4973
1A55              4974
1A55              4975
1A55              4976
1A55              4977
1A55              4978
1A55              4979
1A55              4980
1A55              4981
1A55              4982
1A55              4983
1A55              4984
1A55              4985
1A55              4986
1A55              4987
1A55              4988
1A55              4989
1A55              4990
1A55              4991
1A55              4992
1A55              4993
1A55              4994
1A55              4995
1A55              4996
1A55              4997
1A55              4998
1A55              4999
1A55              5000
1A55              5001
1A55              5002
1A55              5003
1A55              5004
1A55              5005
1A55              5006
1A55              5007
1A55              5008
1A55              5009
1A55              5010
1A55              5011
1A55              5012
1A55              5013
1A55              5014
1A55              5015
1A55              5016
1A55              5017
1A55              5018
1A55              5019
1A55              5020
1A55              5021
1A55              5022
1A55              5023
1A55              5024
1A55              5025
1A55              5026
1A55              5027
1A55              5028
1A55              5029
1A55              5030
1A55              5031
1A55              5032
1A55              5033
1A55              5034
1A55              5035
1A55              5036
1A55              5037
1A55              5038
1A55              5039
1A55              5040
1A55              5041
1A55              5042
1A55              5043
1A55              5044
1A55              5045
1A55              5046
1A55              5047
1A55              5048
1A55              5049
1A55              5050
1A55              5051
1A55              5052
1A55              5053
1A55              5054
1A55              5055
1A55              5056
1A55              5057
1A55              5058
1A55              5059
1A55              5060
1A55              5061
1A55              5062
1A55              5063
1A55              5064
1A55              5065
1A55              5066
1A55              5067
1A55              5068
1A55              5069
1A55              5070
1A55              5071
1A55              5072
1A55              5073
1A55              5074
1A55              5075
1A55              5076
1A55              5077
1A55              5078
1A55              5079
1A55              5080
1A55              5081
1A55              5082
1A55              5083
1A55              5084
1A55              5085
1A55              5086
1A55              5087
1A55              5088
1A55              5089
1A55              5090
1A55              5091
1A55              5092
1A55              5093
1A55              5094
1A55              5095
1A55              5096
1A55              5097
1A55              5098
1A55              5099
1A55              5100
1A55              5101
1A55              5102
1A55              5103
1A55              5104
1A55              5105
1A55              5106
1A55              5107
1A55              5108
1A55              5109
1A55              5110
1A55              5111
1A55              5112
1A55              5113
1A55              5114
1A55              5115
1A55              5116
1A55              5117
1A55              5118
1A55              5119
1A55              5120
1A55              5121
1A55              5122
1A55              5123
1A55              5124
1A55              5125
1A55              5126
1A55              5127
1A55              5128
1A55              5129
1A55              5130
1A55              5131
1A55              5132
1A55              5133
1A55              5134
1A55              5135
1A55              5136
1A55              5137
1A55              5138
1A55              5139
1A55              5140
1A55              5141
1A55              5142
1A55              5143
1A55              5144
1A55              5145
1A55              5146
1A55              5147
1A55              5148
1A55              5149
1A55              5150
1A55              5151
1A55              5152
1A55              5153
1A55              5154
1A55              5155
1A55              5156
1A55              5157
1A55              5158
1A55              5159
1A55              5160
1A55              5161
1A55              5162
1A55              5163
1A55              5164
1A55              5165
1A55              5166
1A55              5167
1A55              5168
1A55              5169
1A55              5170
1A55              5171
1A55              5172
1A55              5173
1A55              5174
1A55              5175
1A55              5176
1A55              5177
1A55              5178
1A55              5179
1A55              5180
1A55              5181
1A55              5182
1A55              5183
1A55              5184
1A55              5185
1A55              5186
1A55              5187
1A55              5188
1A55              5189
1A55              5190
1A55              5191
1A55              5192
1A55              5193
1A55              5194
1A55              5195
1A55              5196
1A55              5197
1A55              5198
1A55              5199
1A55              5200
1A55              5201
1A55              5202
1A55              5203
1A55              5204
1A55              5205
1A55              5206
1A55              5207
1A55              5208
1A55              5209
1A55              5210
1A55              5211
1A55              5212
1A55              5213
1A55              5214
1A55              5215
1A55              5216
1A55              5217
1A55              5218
1A55              5219
1A55              5220
1A55              5221
1A55              5222
1A55              5223
1A55              5224
1A55              5225
1A55              5226
1A55              5227
1A55              5228
1A55              5229
1A55              5230
1A55              5231
1A55              5232
1A55              5233
1A55              5234
1A55              5235
1A55              5236
1A55              5237
1A55              5238
1A55              5239
1A55              5240
1A55              5241
1A55              5242
1A55              5243
1A55              5244
1A55              5245
1A55              5246
1A55              5247
1A55              5248
1A55              5249
1A55              5250
1A55              5251
1A55              5252
1A55              5253
1A55              5254
1A55              5255
1A55              5256
1A55              5257
1A55              5258
1A55              5259
1A55              5260
1A55              5261
1A55              5262
1A55              5263
1A55              5264
1A55              5265
1A55              5266
1A55              5267
1A55              5268
1A55              5269
1A55              5270
1A55              5271
1A55              5272
1A55              5273
1A55              5274
1A55              5275
1A55              5276
1A55              5277
1A55              5278
1A55              5279
1A55              5280
1A55              5281
1A55              5282
1A55              5283
1A55              5284
1A55              5285
1A55              5286
1A55              5287
1A55              5288
1A55              5289
1A55              5290
1A55              5291
1A55              5292
1A55              5293
1A55              5294
1A55              5295
1A55              5296
1A55              5297
1A55              5298
1A55              5299
1A55              5300
1A55              5301
1A55              5302
1A55              5303
1A55              5304
1A55              5305
1A55              5306
1A55              5307
1A55              5308
1A55              5309
1A55              5310
1A55              5311
1A55              5312
1A55              5313
1A55              5314
1A55              5315
1A55              5316
1A55              5317
1A55              5318
1A55              5319
1A55              5320
1A55              5321
1A55              5322
1A55              5323
1A55              5324
1A55              5325
1A55              5326
1A55              5327
1A55              5328
1A55              5329
1A55              5330
1A55              5331
1A55              5332
1A55              5333
1A55              5334
1A55              5335
1A55              5336
1A55              5337
1A55              5338
1A55              5339
1A55              5340
1A55              5341
1A55              5342
1A55              5343
1A55              5344
1A55              5345
1A55              5346
1A55              5347
1A55              5348
1A55              5349
1A55              5350
1A55              5351
1A55              5352
1A55              5353
1A55              5354
1A55              5355
1A55              5356
1A55              5357
1A55              5358
1A55              5359
1A55              5360
1A55              5361
1A55              5362
1A55              5363
1A55              5364
1A55              5365
1A55              5366
1A55              5367
1A55              5368
1A55              5369
1A55              5370
1A55              5371
1A55              5372
1A55              5373
1A55              5374
1A55              5375
1A55              5376
1A55              5377
1A55              5378
1A55              5379
1A55              5380
1A55              5381
1A55              5382
1A55              5383
1A55              5384
1A55              5385
1A55              5386
1A55              5387
1A55              5388
1A55              5389
1A55              5390
1A55              5391
1A55              5392
1A55              5393
1A55              5394
1A55              5395
1A55              5396
1A55              5397
1A55              5398
1A55              5399
1A55              5400
1A55              5401
1A55              5402
1A55              5403
1A55              5404
1A55              5405
1A55              5406
1A55              5407
1A55              5408
1A55              5409
1A55              5410
1A55              5411
1A55              5412
1A55              5413
1A55              5414
1A55              5415
1A55              5416
1A55              5417
1A55              5418
1A55              5419
1A55              5420
1A55              5421
1A55              5422
1A55              5423
1A55              5424
1A55              5425
1A55              5426
1A55              5427
1A55              5428
1A55              5429
1A55              5430
1A55              5431
1A55              5432
1A55              5433
1A55              5434
1A55              5435
1A55              5436
1A55              5437
1A55              5438
1A55              5439
1A55              5440
1A55              5441
1A55              5442
1A55              5443
1A55              5444
1A55              5445
1A55              5446
1A55              5447
1A55              5448
1A55              5449
1A55              5450
1A55              5451
1A55              5452
1A55              5453
1A55              5454
1A55              5455
1A55              5456
1A55              5457
1A55              5458
1A55              5459
1A55              5460
1A55              5461
1A55              5462
1A55              5463
1A55              5464
1A55              5465
1A55              5466
1A55              5467
1A55              5468
1A55              5469
1A55              5470
1A55              5471
1A55              5472
1A55              5473
1A55              5474
1A55              5475
1A55              5476
1A55              5477
1A55              5478
1A55              5479
1A55              5480
1A55              5481
1A55              5482
1A55              5483
1A55              5484
1A55              5485
1A55              5486
1A55              5487
1A55              5488
1A55              5489
1A55              5490
1A55              5491
1A55              5492
1A55              5493
1A55              5494
1A55              5495
1A55              5496
1A55              5497
1A55              5498
1A55              5499
1A55              5500
1A55              5501
1A55              5502
1A55              5503
1A55              5504
1A55              5505
1A55              5506
1A55              5507
1A55              5508
1A55              5509
1A55              5510
1A55              5511
1A55              5512
1A55              5513
1A55              5514
1A55              5515
1A55              5516
1A55              5517
1A55              5518
1A55              5519
1A55              5520
1A55              5521
1A55              5522
1A55              5523
1A55              5524
1A55              5525
1A55              5526
1A55              5527
1A55              5528
1A55              5529
1A55              5530
1A55              5531
1A55              5532
1A55              5533
1A55              5534
1A55              5535
1A55              5536
1A55              5537
1A55              5538
1A55              5539
1A55              5540
1A55              5541
1A55              5542
1A55              5543
1A55              5544
1A55              5545
1A55              5546
1A55              5547
1A55              5548
1A55              5549
1A55              5550
1A55              5551
1A55              5552
1A55              5553
1A55              5554
1A55              5555
1A55              5556
1A55              5557
1A55              5558
1A55              5559
1A55              5560
1A55              5561
1A55              5562
1A55              5563
1A55              5564
1A55              5565
1A55              5566
1A55              5567
1A55              5568
1A55              5569
1A55              5570
1A55              5571
1A55              5572
1A55              5573
1A55              5574
1A55              5575
1A55              5576
1A55              5577
1A55              5578
1A55              5579
1A55              5580
1A55              5581
1A55              5582
1A55              5583
1A55              5584
1A55              5585
1A55              5586
1A55              5587
1A55              5588
1A55              5589
1A55              5590
1A55              5591
1A55              5592
1A55              5593
1A55              5594
1A55              5595
1A55              5596
1A55              5597
1A55              5598
1A55              5599
1A55              5600
1A55              5601
1A55              5602
1A55              5603
1A55              5604
1A55              5605
1A55              5606
1A55              5607
1A55              5608
1A55              5609
1A55              5610
1A55              5611
1A55              5612
1A55              5613
1A55              5614
1A55              5615
1A55              5616
1A55              5617
1A55              5618
1A55              5619
1A55              5620
1A55              5621
1A55              5622
1A55              5623
1A55              5624
1A55              5625
1A55              5626
1A55              5627
1A55              5628
1A55              5629
1A55              5630
1A55              5631
1A55              5632
1A55              5633
1A55              5634
1A55              5635
1A55              5636
1A55              5637
1A55              5638
1A55              5639
1A55              5640
1A55              5641
1A55              5642
1A55              5643
1A55              5644
1A55              5645
1A55              5646
1A55              5647
1A55              5648
1A55              5649
1A55              5650
1A55              5651
1A55              5652
1A55              5653
1A55              5654
1A55              5655
1A55              5656
1A55              5657
1A55              5658
1A55              5659
1A55              5660
1A55              5661
1A55              5662
1A55              5663
1A55              5664
1A55              5665
1A55              5666
1A55              5667
1A55              5668
1A55              5669
1A55              5670
1A55              5671
1A55              5672
1A55              5673
1A55              5674
1A55              5675
1A55              5676
1A55              5677
1A55              5678
1A55              5679
1A55              5680
1A55              5681
1A55              5682
1A55              5683
1A55              5684
1A55              5685
1A55              5686
1A55              5687
1A55              5688
1A55              5689
1A55              5690
1A55              5691
1A55              5692
1A55              5693
1A55              5694
1A55              5695
1A55              5696
1A55              5697
1A55              5698
1A55              5699
1A55              5700
1A55              5701
1A55              5702
1A55              5703
1A55              5704
1A55              5705
1A55              5706
1A55              5707
1A55              5708
1A55              5709
1A55              5710
1A55              5711
1A55              5712
1A55              5713
1A55              5714
1A55              5715
1A55              5716
1A55              5717
1A55              5718
1A55              5719
1A55              5720
1A55              5721
1A55              5722
1A55              5723
1A55              5724
1A55              5725
1A55              5726
1A55              5727
1A55              5728
1A55              5729
1A55              5730
1A55              5731
1A55              5732
1A55              5733
1A55              5734
1A55              5735
1A55              5736
1A55              5737
1A55              5738
1A55              5739
1A55              5740
1A55              5741
1A55              5742
1A55              5743
1A55              5744
1A55              5745
1A55              5746
1A55              5747
1A55              5748
1A55              5749
1A55              5750
1A55              5751
1A55              5752
1A55              5753
1A55              5754
1A55              5755
1A55              5756
1A55              5757
1A55              5758
1A55              5759
1A55              5760
1A55              5761
1A55              5762
1A55              5763
1A55              5764
1A55              5765
1A55              5766
1A55              5767
1A55              5768
1A55              5769
1A55              5770
1A55              5771
1A55              5772
1A55              5773
1A55              5774
1A55              5775
1A55              5776
1A55              5777
1A55              5778
1A55              5779
1A55              5780
1A55              5781
1A55              5782
1A55              5783
1A55              5784
1A55              5785
1A55              5786
1A55              5787
1A55              5788
1A55              5789
1A55              5790
1A55              5791
1A55              5792
1A55              5793
1A55              5794
1A55              5795
1A55              5796
1A55              5797
1A55              5798
1A55              5799
1A55              5800
1A55              5801
1A55              5802
1A55              5803
1A55              5804
1A55              5805
1A55              5806
1A55              5807
1A55              5808
1A55              5809
1A55              5810
1A55              5811
1A55              5812
1A55              5813
1A55              5814
1A55              5815
1A55              5816
1A55              5817
1A55              5818
1A55              5819
1A55              5820
1A55              5821
1A55              5822
1A55              5823
1A55              5824
1A55              5825
1A55              5826
1A55              5827
1A55              5828
1A55              5829
1A55              5830
1A55              5831
1A55              5832
1A55              5833
1A55              5834
1A55              5835
1A55              5836
1A55              5837
1A55              5838
1A55              5839
1A55              5840
1A55              5841
1A55              5842
1A55              5843
1A55              5844
1A55              5845
1A55              5846
1A55              5847
1A55              5848
1A55              5849
1A55
```



```

5041 C : CL = # OF ADDRESS BITS IN COLUMN VALUE ( 3/2 FOR H/M )
5042 C : BL = MASK TO SELECT BITS FROM POINTED BYTE (0FH/0CH FOR H/M) :
5043 C : BH = NUMBER OF VALID BITS IN POINTED BYTE ( 1/2 FOR H/M ) :
5044 C :-----.

1BA0 BB 02C0 5045 C MOV BX, 20H
1BA3 B9 0302 5046 C MOV CX, 302H ; SET PARM FOR MED RES
1BA6 80 3E 0449 R 06 5047 C CMP CRT_MODE, 6
1BAB T2 06 5048 C JC R5 ; HANDLE IF MED ARES
1BA0 BB 0180 5049 C MOV BX, 180H
1BB0 B9 0703 5050 C MOV CX, 703H ; SET PARM FOR HIGH RES
5051 C
5052 C
5053 C ;----- DETERMINE BIT OFFSET IN BYTE FROM COLUMN MASK
5054 C
5055 R5: 5056 C AND CH, DL ; ADDRESS OF PEL WITHIN BYTE TO CH
5057 C
5058 C ;----- DETERMINE PEL BYTE OFFSET FOR THIS LOCATION IN COLUMN
5059 C
5060 C SHR DX, CL ; SHIFT BY CORRECT AMOUNT
5061 C ADD SI, DX ; INCREMENT THE POINTER
5062 C MOV DH, BH ; GET THE # OF BITS IN RESULT TO DH
5063 C
5064 C ;----- MULTIPLY BH (VALID BITS IN BYTE) BY CH (BIT OFFSET)
5065 C SUB CL, CL ; ZERO INTO STORAGE LOCATION
5066 R6: 5067 C ROR AL, 1 ; LEFT JUSTIFY THE VALUE
5068 C ADD CL, CH ; ADD AL TO AL (FOR WRITE)
5069 C DEC BH ; LOOP CONTROL
5070 C JNZ R6 ; ON EXIT, CL HAS SHIFT COUNT
5071 C
5072 C
5073 C
5074 C MOV AH, BL ; TO RESTORE BITS
5075 C SHR AH, CL ; MOVE THE MASK TO CORRECT LOCATION
5076 C POP BX ; RECOVER REG
5077 C RET ; RETURN WITH EVERYTHING SET UP
5078 C
5079 C
5080 C ;----- READ DOT -- WRITE DOT
5081 C THESE ROUTINES WILL WRITE A DOT, OR READ THE DOT AT
5082 C THE INDICATED LOCATION
5083 C
5084 C ENTRY -- :-----.
5085 C DX = ROW (0-199) (THE ACTUAL VALUE DEPENDS ON THE MODE)
5086 C CX = COLUMN (0-639) (THE VALUES ARE NOT RANGE CHECKED)
5087 C AL = DATA SEGMENT AL WILL WRITE 12 OF 16 BITS DEPENDING ON MODE,
5088 C BIT 7 OF AL=1 INDICATES XOR THE VALUE INTO THE LOCATION
5089 C DS = DATA SEGMENT
5090 C ES = REGEN SEGMENT
5091 C
5092 C
5093 C EXIT :-----.
5094 C AL = DOT VALUE READ, RIGHT JUSTIFIED, READ ONLY
5095 C
5096 C
5097 C
5098 C ;----- WRITE DOT
5099 C
5100 C AHC: ASSUME DS:ABSO
5101 C CMP CRT_MODE, 7
5102 C JA WRITE_DOT_2
5103 C
5104 C
5105 C ;----- WRITE_DOT PROC NEAR
5106 C ASSUME DS:ABSO, ES:NOTHING
5107 C PUSH DS
5108 C SRULOAD ES, 0B800H
5109 C C* MOV DX, 0B800H
5110 C C* MOV ES, DX
5111 C POP DX
5112 C PUSH AX ; SAVE DOT VALUE
5113 C PUSH AX ; TWICE
5114 C CALL R3 ; DETERMINE BYTE POSITION OF THE DOT
5115 C SHR AL, CL ; SET UP THE BITS FOR OUTPUT
5116 C AND AL, AH ; STRIP OFF THE OTHER BITS
5117 C MOV CL, ES:[SI] ; GET THE CURRENT BYTE
5118 C POP BX ; RECOVER XOR FLAG
5119 C TEST BL, 80H ; IS IT ON
5120 C NOT AH ; YES, XOR THE DOT
5121 C AND CL, AH ; SET THE MASK TO REMOVE THE
5122 C OR AL, CL ; INDICATED BITS
5123 C
5124 C R1: MOV ES:[SI], AL ; OR IN THE NEW VALUE OF THOSE BITS
5125 C POP AX ; FINISH DOT
5126 C JMP V_RET ; RESTORE THE BYTE IN MEMORY
5127 C
5128 C R2: XOR AL, CL ; XOR DOT
5129 C C* JMP R1 ; EXCLUSIVE OR THE DOTS
5130 C WRITE_DOT ENDP ; FINISH UP THE WRITING
5131 C
5132 C ;----- WRITE_DOT_2 PROC NEAR
5133 C CMP CRT_MODE, 0FH
5134 C JB NO_ADJ2 ; BASE CARD
5135 C CALL MEM_DET ; NO ADJ2
5136 C JC NO_ADJ2 ; NO ADJ2
5137 C AND AL, 0000010B ; 85H, XOR C2 C0 MASK
5138 C MOV AH, AL
5139 C SHL AH, 1 ; EXPAND C0 TO C1, C2 TO C3
5140 C OR AL, AH ; BUILD ?(80H) + (0,3,C,F)
5141 C
5142 C NO_ADJ2: PUSH AX ; REGEN SEGMENT
5143 C MOV AX, DX ; ROW VALUE
5144 C CALL DOT_SUP_1 ; BX=OFFSET, AL=BIT MASK
5145 C C* MOV DH, 40000H ; GRAPHICS CHIP
5146 C C* MOV DH, GRAPH_ADDR ; BIT MASK REGISTER
5147 C C* MOV AH, G_BIT_MASK ; SET BIT MASK
5148 C CALL OUT_DX ; RECOVER COLOR
5149 C PUSH DS ; SAVE COLOR
5150 C SRULOAD DS, 0A000H ; SEE IF XOR
5151 C C* MOV DX, 0A000H ; XOR FUNCTION
5152 C C* MOV ES, DX ; SET THE REGISTER
5153 C C* POP AX ; SKIP THE BLANK
5154 C C* MOV CH, AL ; BLANK THE DOT
5155 C C* TEST CH, 0B0H ; DECODE RASTER
5156 C C* JZ WD_A ; MAP MASK
5157 C C* MOV AH, DATA_ROT ; ENABLE ALL MAPS
5158 C C* MOV AL, 0FH ; SET THE REGISTER
5159 C C* MOV AH, 01H
5160 C C* CALL OUT_DX
5161 C C* JMP WD_B
5162 C
5163 C WD_A: MOV DL, SEQ_ADDR
5164 C MOV AH, S_MAP
5165 C MOV AL, 0FFH
5166 C CALL OUT_DX

```

```

1C41 26: 8A 07      5167  C      MOV    AL, ES:[ BX ]
1C44 26: 8A 07      5168  C      SUB    AL, AL
1C46 26: 88 07      5169  C      MOV    ES:[ BX ], AL
1C49
1C49 B2 C4          5170  C      HD_B:
1C48 B4 02          5171  C      MOV    DL, SEQ_ADDR
1C4D B4 C0          5172  C      MOV    AH, S_MAP
1C4F B4 0F          5173  C      MOV    AL, CH
1C51 E8 0015 R      5174  C      AND    AL, DH
1C54 26: 8A 07      5175  C      CALL   OUT_DX
1C56 B0 FF          5176  C      MOV    AL, ES:[ BX ]
1C57 B0 FF          5177  C      MOV    AL, OFFH
1C59 26: 88 07      5178  C      MOV    ES:[ BX ], AL
1C59
1C59 5180 C      ;----- NORMALIZE THE ENVIRONMENT
1C5C E8 0015 R      5182  C      CALL   OUT_DX
1C5F B2 CE          5183  C      MOV    DL, GRAPH_ADDR
1C61 B4 03          5184  C      MOV    AH, G_DATA_ROT
1C63 2A C0          5185  C      SUB    AL, AL
1C65 E8 0015 R      5186  C      CALL   OUT_DX
1C66 B4 03          5187  C      MOV    AH, G_LNT_MASK
1C66 B0 FF          5188  C      MOV    AL, OFFH
1C66 E8 0015 R      5189  C      CALL   OUT_DX
1C67 E9 219E R      5190  C      JMP   V_RET
1C72
1C72 5192 C      WRITE_DOT_2
1C72 5193 C      RD_S  PROC  NEAR
1C72 5194  C      ASSUME DS:ABSO
1C73 5195  C      PUSH  AX
1C73 5196  C      PUSH  DX
1C74 5197  C      SRL/LOAD ES, D4000H
1C74 BA A000          5198  C      MOV    DX, D4000H
1C77 B6 C2          5199  C      MOV    ES, DX
1C79 5A 00          5200  C      POP   DX
1C7A 52 00          5201  C      POP   AX
1C7B B8 C2          5202  C      MOV    AX, DX
1C7D E8 1B60 R      5203  C      CALL   DOT_SUP_1
1C80 B5 07          5204  C      MOV    CH, 7
1C82 52 00          5205  C      SUB   CH, CL
1C84 2B D2          5206  C      SUB   DX, DX
1C86 B0 00          5207  C      MOV    AL, 0
1C88 C3 00          5208  C      RET
1C89 5209 C      RD_S  ENDP
1C89 5210
1C89 5211 C      RD_IS  PROC  NEAR
1C89 5212  C      ASSUME DS:ABSO
1C88 5213  C      MOV    CL, CH
1C89 5214  C      MOV    AH, 4
1C89 5215  C      PUSH  DX
1C8E B6 03          5216  C      MOV    DH, 3
1C90 B2 CE          5217  C      MOV    DL, GRAPH_ADDR
1C92 E8 0015 R      5218  C      CALL   OUT_DX
1C93 52 00          5219  C      POP   DX
1C96 26: 8A 27      5219  C      MOV    AH, ES:[ BX ]
1C99 D2 EC          5220  C      SHR   AH, CL
1C9B 80 E4 01      5221  C      AND   AH, 1
1C9E C3 00          5222  C      RET
1C9F 5223 C      RD_IS  ENDP
1C9F 5224 C      ;----- READ DOT
1C9F 5225 C      AH:D:
1C9F 5226  C      ASSUME DS:ABSO, CRT_MODE, 7
1C9F 5227  C      JA   R_1
1C9F 5228  C      READ_DOT PROC  NEAR
1C9F 5229  C      ASSUME DS:ABSO
1C9F 5230  C      CMP   CRT_MODE, 7
1C9F 5231  C      JA   R_1
1C9F 5232  C      READ_DOT DS:ABSO, ES:NOTHING
1C9F 5233  C      ASSUME DS:ABSO, ES:NOTHING
1C9F 5234  C      PUSH  DX
1C9F 5235  C      SRL/LOAD ES, DB800H
1C9F 5236  C      MOV    DX, DB800H
1C9F 5237  C      MOV    EDX, DX
1C9F 5238  C      POP   DX
1C9F 5239  C      CALL   R3
1C9F 5240  C      MOV    AL, ES:[ SI ]
1C9F 5241  C      AND   AL, DH
1C9F 5242  C      SHL   AL, CL
1C9F 5243  C      MOV    CL, DH
1C9F 5244  C      ROL   AL, CL
1C9F 5245  C      RET
1C9F 5246  C      READ_DOT ENDP
1C9F 5247
1C9F 5248  C      R_1:
1C9F 5249  C      CMP   CRT_MODE, OFH
1C9F 5250  C      JB   READ_DOT_2
1C9F 5251  C      CALL   MEM_DET
1C9F 5252  C      JC   READ_DOT_2
1C9F 5253  C      READ_DOT_1 PROC  NEAR
1C9F 5254  C      ASSUME DS:ABSO, ES:NOTHING ; 2 MAPS
1C9F 5255  C      CALL   RD_S
1C9F 5256  C      CALL   RD_IS
1C9F 5257  C      OR    DL, AH
1C9F 5258  C      SHL   AH, 1
1C9F 5259  C      OR    DL, AH
1C9F 5260  C      MOV    AL, 2
1C9F 5261  C      CALL   RD_IS
1C9F 5262  C      SHL   AH, 1
1C9F 5263  C      SHL   AH, 1
1C9F 5264  C      OR    DL, AH
1C9F 5265  C      SHL   AH, 1
1C9F 5266  C      OR    DL, AH
1C9F 5267  C      SHL   AH, 1
1C9F 5268  C      OR    DL, AH
1C9F 5269  C      MOV    AL, DL
1C9F 5270  C      JMP   V_RET
1C9F 5271  C      READ_DOT_1 ENDP
1C9F 5272
1C9F 5273  C      READ_DOT_2 PROC  NEAR ; 4 MAPS
1C9F 5274  C      ASSUME DS:ABSO, ES:NOTHING
1C9F 5275  C      RD_2A: CALL   RD_S
1C9F 5276  C      CALL   RD_IS
1C9F 5277  C      MOV    CL, AL
1C9F 5278  C      SHL   AH, CL
1C9F 5279  C      OR    DL, AH
1C9F 5280  C      INC   AL
1C9F 5281  C      CMP   AL, 3
1C9F 5282  C      JBE   RD_2A
1C9F 5283  C      MOV    AL, DL
1C9F 5284  C      JMP   V_RET
1C9F 5285  C      READ_DOT_2 ENDP
1C9F 5286
1C9F 5287
1C9F 5288  C      ;----- WRITE_TTY  WRITE TELETYPE TO ACTIVE PAGE
1C9F 5289  C      ;----- THIS INTERFACE PROVIDES A TELETYPE LIKE INTERFACE TO THE VIDEO
1C9F 5290  C      ;----- CARD. THE INPUT CHARACTERS ARE WRITTEN TO THE CURRENT CURSOR
1C9F 5291  C      ;----- POSITION, AND THE CURSOR IS MOVED TO THE NEXT POSITION. IF THE
1C9F 5292  C      ;----- CURSOR LEAVES THE LAST COLUMN OF THE FIELD, THE COLUMN IS SET

```



```

1D96 5F
1D97 5E
1D98 59
1D99 59
1D9A 5A
1D9B 1F
1D9C 07
1D9D 5D
1D9E CF

5419 C POP DI
5420 C POP SI
5421 C POP CX
5422 C POP DX
5423 C POP DS
5424 C POP ES
5425 C POP BP
5426 C POP BP
5427 C IRET
5428 C
5429 C
5430 C SUBTLL
5431
5432 PAL_SET PROC NEAR
5433 PUSH AX
5434 CALL WHAT_BASE
5435 CLI
5436 VR: IN AL,DX
5437 TEST AL,0DH
5438 JZ VR
5439 POP AX
5440 MOV DL,ATTR_WRITE
5441 MOV AH,AL
5442 XCHG AH,AH
5443 OUT DX,AL
5444 XCHG AL,AH
5445 OUT DX,AL
5446 MOV AL,020H
5447 OUT DX,AL
5448 STI
5449 RET
5450 PAL_SET ENDP
5451
5452 PAL_ON PROC NEAR
5453 CALL PAL_INIT
5454 MOV DL,ATTR_WRITE
5455 MOV AL,020H
5456 OUT DX,AL
5457 RET
5458 PAL_ON ENDP
5459
5460 PAL_INIT PROC NEAR
5461 CALL WHAT_BASE
5462 IN AL,DX
5463 RET
5464 PAL_INIT ENDP
5465
5466 ;----- SET PALETTE REGISTERS
5467
5468 AH10:
5469 ASSUME DS:AB80
5470 TEST INFO,2
5471 JNZ BM_OK
5472
5473 ;----- HERE THE EGA IS IN A COLOR MODE
5474
5475 CMP BYTE PTR ADDR_6845,0B4H
5476 JE BM_OUT
5477
5478 MOV AH,AL
5479 OR AH,AH
5480 JNZ BM_1
5481
5482 ;----- SET INDIVIDUAL REGISTER
5483
5484 SUB BP,BP
5485 LES D1,SAVE_PTR
5486 ADD D1,BH
5487 LES D1,DWORD PTR ES:[D1]
5488 MOV AX,ES
5489 OR AX,D1
5490 JZ TLO_1
5491 INC BP
5492 TLO_1:
5493
5494 CALL PAL_INIT
5495 MOV AH,BL
5496 MOV AL,BH
5497 CALL PAL_SET
5498 CALL PAL_ON
5499 OR BP,BP
5500 JZ BM_OUT
5501 MOV AL,BH
5502 SUB BH,BH
5503 ADD D1,BX
5504 MOV ES:[D1],AL
5505
5506 BM_OUT: JMP V_RET
5507
5508 BM_1:
5509 DEC AH
5510 JNZ BM_2
5511
5512 SUB BP,BP
5513 LES D1,SAVE_PTR
5514 ADD D1,BH
5515 LES D1,DWORD PTR ES:[D1]
5516 MOV AX,ES
5517 OR AX,D1
5518 JZ TLO_2
5519 INC BP
5520 TLO_2:
5521
5522 ;----- SET OVERSCAN REGISTER
5523
5524 CALL PAL_INIT
5525 MOV AH,011H
5526 MOV AL,BH
5527 CALL PAL_SET
5528 CALL PAL_ON
5529
5530 OR BP,BP
5531 JZ BM_OUT
5532 ADD D1,011H
5533 MOV ES:[D1],BH
5534
5535 JMP V_RET
5536
5537 BM_2:
5538 DEC AH
5539 JNZ BM_3
5540
5541 ;----- SET 16 PALETTE REGISTERS AND OVERSCAN REGISTER
5542
5543 PUSH DS
5544 RUSH FS
5545
5546 ; DISCARD BX
5547 ; VERTICAL RETRACE

```

```

1E40 C4 3E 04A8 R 5545
1E44 B3 C7 04 5546 LES D1_SAVE_PTR
1E47 26: C4 3D 5547 ADD D1,4
1E4A 8C C0 5548 LES D1_DWORD PTR ES:[D1] ; ES:DI PTR TO PAL SAVE AREA
1E4C 0B C7 5549 MOV AX,ES
1E4E 74 09 5550 OR AX,D1
1E4F 5551 JZ TL0_3
1E50 1F 5552
1E51 1E 5553 POP DS ; PARAMETER ES
1E52 8B F2 5554 PUSH DS ; PARAMETER OFFSET
1E54 B9 0011 5555 MOV SI,DX
1E57 F3/A4 5556 MOV CX,17D
1E58 5557 REP MOVS B
1E59 07 5558 TL0_3:
1E5A 1F 5559 POP ES
1E5B 8B DA 5560 POP DS
1E5D E8 1000 R 5561
1E60 2A E4 5562
1E62 5563 MOV BX,DX
1E63 E8 1000 R 5564 CALL PAL_INIT
1E64 2A E4 5565 SUB AH,AH
1E66 FE C4 5566 BM_2A: MOV AL,ES:[BX]
1E67 26: B8 07 5567 ADD AL,1
1E68 E8 109F R 5568 INC AH
1E69 FE C4 5569 CMP AH,010H
1E6A 43 5570 INC BX
1E6B 80 FC 10 5571 CALL BM_2A
1E6C 2F F2 5572 INC AH
1E70 F4 0H 5573 MOV AL,ES:[BX]
1E72 26: B8 07 5574 CALL PAL_SET
1E75 E8 109F R 5575 CALL PAL_ON
1E78 E8 10B7 R 5576 JMP V_RET
1E7B E9 219E R 5577
1E7E 5578 BM_3:
1E7E FE CC 5579 DEC AH
1E80 75 29 5580 JNZ BM_4
1E81 5581
1E82 5582
1E83 5583 ;----- TOGGLE INTENSIFY/BLINKING BIT
1E84 5584
1E85 5585 PUSH BX
1E86 B8 005A R 5586 CALL MAKE_BASE
1E87 B3 C3 33 5587 ADD AL,010H + LN_4
1E89 26: B8 07 5588 MOV AL,ES:[BX]
1E8C 5B 5589 POP BX
1E8D 0A DB 5590 OR BL,BL
1E8F 75 0A 5592 JNZ BM_6
1E8F 5593
1E90 5594
1E91 80 26 0465 R DF 5595
1E96 24 F7 5596 AND CRT_MODE_SET,11011111B
1E98 EB OC 90 5597 AND AL,0F7H
1E99 5598 JMP BM_7
1E9B FE CB 5599 BM_6: DEC BL
1E9D 75 07 5600 JNZ BM_7
1E9F 80 0E 0465 R 20 5602 ;----- ENABLE BLINK
1EAB OC 0B 5603 OR CRT_MODE_SET,020H
1EAC 5604 BM_7: MOV AH,P_MODE
1EAD B4 10 5605 CALL PAL_SET
1EAF E8 109F R 5606 BM_8: JMP V_RET
1EAB E9 219E R 5611
1EAC 5612
1EAD 5613
1EAF 5614 INCLUDE VCHGEN. INC
1EAB 5615 SUBTTL VCHGEN. INC
1EAC 5616 PAGE
1EAD 5617 ;----- ENTRY POINT
1EAF 5618 AL = 0 USER SPECIFIED FONT
1EAB 5619 1 8 X 14 FONT
1EAC 5620 2 8 X 8 DOUBLE DOT
1EAD 5621 BL = BLOCK TO LOAD
1EAE 5622
1EAF 5623 CH_GEN: PUSH AX ; SAVE THE INVOLVED REGS
1EAB 5624 PUSH BP
1EAC 5625 PUSH BX
1EAD 5626 PUSH CX
1EAE 5627 PUSH DX
1EAF 5628 PUSH ES
1EAB 5629
1EAC 5630
1EAD 5631 ASSUME DS:ABSO
1EAE 5632 CALL DDS
1EAF 5633 MOV AL,CRT_MODE ; SET DATA SEGMENT
1EAB 5634 PUSH AX ; GET THE CURRENT MODE
1EAC 5635 PUSH AL ; SAVE IT
1EAD 5636 JC H14 ; THIS IS MONOCHROME
1EAE 5637 MOV CRT_MODE,0BH ; MONOCHROME VALUES
1EAF 5638 JMP SHORT H15 ; COLOR VALUES
1EAB 5639
1EAC 5640 H14: MOV CRT_MODE,0CH ; SKIP
1EAD 5641
1EAE 5642 CALL SET_REGS ; MONOCHROME VALUES
1EAF 5643 CALL DDS ; RESET THE DATA SEGMENT
1EAB 5644 POP AX ; RECOVER OLD MODE VALUE
1EAC 5645 MOV CRT_MODE,AL ; RETURN TO LOW MEMORY
1EAD 5646
1EAE 5647 POP ES ; RESTORE REGS THAT WERE
1EAF 5648 POP DX ; USED BY THE MODE SET
1EAB 5649 POP CX ; ROUTINES
1EAC 5650 POP BX
1EAD 5651 POP BP
1EAE 5652 POP AX
1EAF 5653
1EAB 5654 OR AL,AL ; SET FLAGS
1EAC 5655 JZ DO_MAP2 ; USER SPECIFIED FONT
1EAD 5656 PUSH CS ; SET SEGMENT TO
1EAE 5657 POP ES ; THIS MODE
1EAF 5658 SUB DX,DX ; ZEROES THE START OFFSET
1EAB 5659 MOV CX,0256D ; CHAR COUNT (FULL SET)
1EAC 5660 DEC AL ; WHICH PARAMETER
1EAD 5661 JNZ H7 ; MUST BE ON
1EAE 5662 MOV BH,010H ; DIBBLE THE CHARACTER
1EAF 5663 MOV BP,OFFSET CGMN ; 8 X 14 TABLE OFFSET
1EAB 5664 JMP SHORT DO_MAP2 ; STORE IT
1EAC 5665 H7: MOV BH,8 ; 8 X 8 FONT
1EAD 5666 MOV BP,OFFSET CGDDOT ; ROM 8 X 8 DOUBLE DOT
1EAE 5667
1EAF 5668
1EAB 5669 C ; ALPHA CHARACTER GENERATOR LOAD
1EAC 5670 C

```



```

FICA BB 0003 5797 C MOV AX,3 ; AH_S_RESET, AL=3
FED8 EB 0015 R 5798 CALL OUT_DX
1FDD 00 5799 C H2: JMP V_RET ; RETURN TO CALLER
1FD3 00 5800 C AH11_ALPHAI:
1FD3 3C 20 5801 C ASSUME DS:ABSO
1FD5 73 26 5802 C CMP AL,020H
5803 C JAE AH11_GRAPHICS
5804 C
5805 C
5806 C
5807 C
5808 C ;----- ALPHA MODE ACTIVITY HERE
5809 C SUB AL,010H ; ADJUST TO 0 - N
5810 C CMP AL,02H ; RANGE CHECK
5811 C JAE AH11_ALPHAI ; INVALID CALL
5812 C PUSH AX ; SAVE
5813 C PUSH BX
5814 C CALL CH_GEN ; CALLING PARAMETER
5815 C CALL SET_REGS ; USER MODE
5816 C
5817 C POP AX ; RESTORE
5818 C MOV AH,AL ; CALLING PARAMETER
5819 C OR AH,AH ; USER MODE
5820 C MOV AH,BH ; SAVE
5821 C JZ H13 ; DO NOT SET BYTES/CHAR
5822 C MOV AL,8 ; 8 X 8 FONT
5823 C CMP AH,1 ; IS THE CALL FOR MONOC
5824 C JNE H13 ; NO, LEAVE IT AT 8
5825 C MOV AL,14D ; MONOC SET
5826 C
5827 C SUB AH,AH ; CLEAR UPPER BYTE
5828 C JMP BRK_1 ; CONTINUE
5829 C
5830 C ;----- GRAPHICS MODE ACTIVITY HERE
5831 C
5832 C AH11_GRAPHICS:
5833 C ASSUME DS:ABSO ; DS:ABSO
5834 C CMP AL,030H ; AL,030H
5835 C JAE AH11_INFORM ; AH11_INFORM
5836 C SUB AL,020H ; SUB AL,020H
5837 C JNZ F10 ; F10
5838 C
5839 C ;----- COMPATIBILITY, UPPER HALF GRAPHICS CHARACTER SET
5840 C
5841 C ASSUME DS:ABSO
5842 C SRLOAD DS,0 ; DS,0
5843 C SUB DX,DX
5844 C MOV DS,DX
5845 C CLI
5846 C MOV WORD PTR EXT_PTR, BP ; WORD PTR EXT_PTR + 2 , ES
5847 C MOV WORD PTR EXT_PTR + 2 , ES
5848 C STI
5849 C
5850 C F11: JMP V_RET ; RANGE CHECK
5851 C
5852 C F10: ASSUME DS:ABSO
5853 C PUSH DX
5854 C SRLOAD DS,0
5855 C SUB DX,DX
5856 C MOV DS,DX
5857 C POP DX
5858 C CMP AL,03H ; JA F11
5859 C JZ F19 ; F19
5860 C CLI ; AL
5861 C MOV AL,03H ; AL,03H
5862 C PUSH CS ; DS,0
5863 C POP ES ; DS,0
5864 C MOV AL,03H ; AL,03H
5865 C JNZ F13 ; F13
5866 C MOV CX,14D ; CX,14D
5867 C MOV BP,OFFSET CGMMN ; BP,OFFSET CGMMN
5868 C JMP SHORT F19 ; ROM 8 X 14 CHARACTER SET
5869 C
5870 C F13: MOV CX,8 ; ROM 8 X 8 DOUBLE DOT
5871 C MOV BP,OFFSET CGDDOT ; ROM 8 X 8 DOUBLE DOT
5872 C
5873 C F19: CLI
5874 C MOV WORD PTR GRX_SET, BP ; WORD PTR GRX_SET + 2 , ES
5875 C MOV WORD PTR GRX_SET + 2 , ES
5876 C STI
5877 C ASSUME DS:ABSO
5878 C CALL DDS
5879 C MOV POINTS,CX ; POINTS,CX
5880 C MOV AL,BL ; AL,BL
5881 C MOV BX,OFFSET RT ; BX,OFFSET RT
5882 C MOV AL,DL ; AL,DL
5883 C JNZ DR_3 ; DR_3
5884 C MOV AL,DL ; AL,DL
5885 C JMP DR_1 ; DR_1
5886 C
5887 C DR_3: CMP AL,3 ; CMP AL,3
5888 C JBE DR_2 ; JBE DR_2
5889 C MOV AL,2 ; MOV AL,2
5890 C
5891 C DR_2: XLAT CS:RT ; XLAT CS:RT
5892 C
5893 C DR_1: DEC AL ; DEC AL
5894 C MOV ROWS,AL ; MOV ROWS,AL
5895 C JMP V_RET ; V_RET
5896 C
5897 C RT LABEL BYTE ; RT LABEL BYTE
5898 C DB 00D,14D,25D,43D ; DB 00D,14D,25D,43D
5899 C
5900 C
5901 C ;----- INFORMATION RETURN DONE HERE
5902 C
5903 C AH11_INFORM:
5904 C ASSUME DS:ABSO ; DS:ABSO
5905 C CMP AL,030H ; AL,030H
5906 C JE F6 ; F6
5907 C
5908 C F5: JMP V_RET ; RETURN TO CALLER
5909 C
5910 C F6: MOV CX,POINTS ; CX,POINTS
5911 C MOV DL,ROWS ; DL,ROWS
5912 C CMP BH,7 ; CMP BH,7
5913 C JZ F7 ; F7
5914 C CMP BH,1 ; CMP BH,1
5915 C JA F7 ; JA F7
5916 C
5917 C ASSUME DS:ABSO ; ASSUME DS:ABSO
5918 C PUSH DX ; PUSH DX
5919 C SRLOAD DS,0 ; SRLOAD DS,0
5920 C SUB DX,DX ; SUB DX,DX
5921 C MOV DS,DX ; MOV DS,DX
5922 C
5923 C ;----- INFORMATION RETURN DONE HERE
5924 C
5925 C AH11_INFORM:
5926 C ASSUME DS:ABSO ; DS:ABSO
5927 C CMP AL,030H ; AL,030H
5928 C JE F6 ; F6
5929 C
5930 C F5: JMP V_RET ; RETURN TO CALLER
5931 C
5932 C F6: MOV CX,POINTS ; CX,POINTS
5933 C MOV DL,ROWS ; DL,ROWS
5934 C CMP BH,7 ; CMP BH,7
5935 C JZ F7 ; F7
5936 C CMP BH,1 ; CMP BH,1
5937 C JA F7 ; JA F7
5938 C
5939 C ;----- INFORMATION RETURN DONE HERE
5940 C
5941 C AH11_INFORM:
5942 C ASSUME DS:ABSO ; DS:ABSO
5943 C CMP AL,030H ; AL,030H
5944 C JE F6 ; F6
5945 C
5946 C F5: JMP V_RET ; RETURN TO CALLER
5947 C
5948 C F6: MOV CX,POINTS ; CX,POINTS
5949 C MOV DL,ROWS ; DL,ROWS
5950 C CMP BH,7 ; CMP BH,7
5951 C JZ F7 ; F7
5952 C CMP BH,1 ; CMP BH,1
5953 C JA F7 ; JA F7
5954 C
5955 C ;----- INFORMATION RETURN DONE HERE
5956 C
5957 C AH11_INFORM:
5958 C ASSUME DS:ABSO ; DS:ABSO
5959 C CMP AL,030H ; AL,030H
5960 C JE F6 ; F6
5961 C
5962 C F5: JMP V_RET ; RETURN TO CALLER
5963 C
5964 C F6: MOV CX,POINTS ; CX,POINTS
5965 C MOV DL,ROWS ; DL,ROWS
5966 C CMP BH,7 ; CMP BH,7
5967 C JZ F7 ; F7
5968 C CMP BH,1 ; CMP BH,1
5969 C JA F7 ; JA F7
5970 C
5971 C ;----- INFORMATION RETURN DONE HERE
5972 C
5973 C AH11_INFORM:
5974 C ASSUME DS:ABSO ; DS:ABSO
5975 C CMP AL,030H ; AL,030H
5976 C JE F6 ; F6
5977 C
5978 C F5: JMP V_RET ; RETURN TO CALLER
5979 C
5980 C F6: MOV CX,POINTS ; CX,POINTS
5981 C MOV DL,ROWS ; DL,ROWS
5982 C CMP BH,7 ; CMP BH,7
5983 C JZ F7 ; F7
5984 C CMP BH,1 ; CMP BH,1
5985 C JA F7 ; JA F7
5986 C
5987 C ;----- INFORMATION RETURN DONE HERE
5988 C
5989 C AH11_INFORM:
5990 C ASSUME DS:ABSO ; DS:ABSO
5991 C CMP AL,030H ; AL,030H
5992 C JE F6 ; F6
5993 C
5994 C F5: JMP V_RET ; RETURN TO CALLER
5995 C
5996 C F6: MOV CX,POINTS ; CX,POINTS
5997 C MOV DL,ROWS ; DL,ROWS
5998 C CMP BH,7 ; CMP BH,7
5999 C JZ F7 ; F7
6000 C CMP BH,1 ; CMP BH,1
6001 C JA F7 ; JA F7
6002 C
6003 C ;----- INFORMATION RETURN DONE HERE
6004 C
6005 C AH11_INFORM:
6006 C ASSUME DS:ABSO ; DS:ABSO
6007 C CMP AL,030H ; AL,030H
6008 C JE F6 ; F6
6009 C
6010 C F5: JMP V_RET ; RETURN TO CALLER
6011 C
6012 C F6: MOV CX,POINTS ; CX,POINTS
6013 C MOV DL,ROWS ; DL,ROWS
6014 C CMP BH,7 ; CMP BH,7
6015 C JZ F7 ; F7
6016 C CMP BH,1 ; CMP BH,1
6017 C JA F7 ; JA F7
6018 C
6019 C ;----- INFORMATION RETURN DONE HERE
6020 C
6021 C AH11_INFORM:
6022 C ASSUME DS:ABSO ; DS:ABSO
6023 C CMP AL,030H ; AL,030H
6024 C JE F6 ; F6
6025 C
6026 C F5: JMP V_RET ; RETURN TO CALLER
6027 C
6028 C F6: MOV CX,POINTS ; CX,POINTS
6029 C MOV DL,ROWS ; DL,ROWS
6030 C CMP BH,7 ; CMP BH,7
6031 C JZ F7 ; F7
6032 C CMP BH,1 ; CMP BH,1
6033 C JA F7 ; JA F7
6034 C
6035 C ;----- INFORMATION RETURN DONE HERE
6036 C
6037 C AH11_INFORM:
6038 C ASSUME DS:ABSO ; DS:ABSO
6039 C CMP AL,030H ; AL,030H
6040 C JE F6 ; F6
6041 C
6042 C F5: JMP V_RET ; RETURN TO CALLER
6043 C
6044 C F6: MOV CX,POINTS ; CX,POINTS
6045 C MOV DL,ROWS ; DL,ROWS
6046 C CMP BH,7 ; CMP BH,7
6047 C JZ F7 ; F7
6048 C CMP BH,1 ; CMP BH,1
6049 C JA F7 ; JA F7
6050 C
6051 C ;----- INFORMATION RETURN DONE HERE
6052 C
6053 C AH11_INFORM:
6054 C ASSUME DS:ABSO ; DS:ABSO
6055 C CMP AL,030H ; AL,030H
6056 C JE F6 ; F6
6057 C
6058 C F5: JMP V_RET ; RETURN TO CALLER
6059 C
6060 C F6: MOV CX,POINTS ; CX,POINTS
6061 C MOV DL,ROWS ; DL,ROWS
6062 C CMP BH,7 ; CMP BH,7
6063 C JZ F7 ; F7
6064 C CMP BH,1 ; CMP BH,1
6065 C JA F7 ; JA F7
6066 C
6067 C ;----- INFORMATION RETURN DONE HERE
6068 C
6069 C AH11_INFORM:
6070 C ASSUME DS:ABSO ; DS:ABSO
6071 C CMP AL,030H ; AL,030H
6072 C JE F6 ; F6
6073 C
6074 C F5: JMP V_RET ; RETURN TO CALLER
6075 C
6076 C F6: MOV CX,POINTS ; CX,POINTS
6077 C MOV DL,ROWS ; DL,ROWS
6078 C CMP BH,7 ; CMP BH,7
6079 C JZ F7 ; F7
6080 C CMP BH,1 ; CMP BH,1
6081 C JA F7 ; JA F7
6082 C
6083 C ;----- INFORMATION RETURN DONE HERE
6084 C
6085 C AH11_INFORM:
6086 C ASSUME DS:ABSO ; DS:ABSO
6087 C CMP AL,030H ; AL,030H
6088 C JE F6 ; F6
6089 C
6090 C F5: JMP V_RET ; RETURN TO CALLER
6091 C
6092 C F6: MOV CX,POINTS ; CX,POINTS
6093 C MOV DL,ROWS ; DL,ROWS
6094 C CMP BH,7 ; CMP BH,7
6095 C JZ F7 ; F7
6096 C CMP BH,1 ; CMP BH,1
6097 C JA F7 ; JA F7
6098 C
6099 C ;----- INFORMATION RETURN DONE HERE
6100 C
6101 C AH11_INFORM:
6102 C ASSUME DS:ABSO ; DS:ABSO
6103 C CMP AL,030H ; AL,030H
6104 C JE F6 ; F6
6105 C
6106 C F5: JMP V_RET ; RETURN TO CALLER
6107 C
6108 C F6: MOV CX,POINTS ; CX,POINTS
6109 C MOV DL,ROWS ; DL,ROWS
6110 C CMP BH,7 ; CMP BH,7
6111 C JZ F7 ; F7
6112 C CMP BH,1 ; CMP BH,1
6113 C JA F7 ; JA F7
6114 C
6115 C ;----- INFORMATION RETURN DONE HERE
6116 C
6117 C AH11_INFORM:
6118 C ASSUME DS:ABSO ; DS:ABSO
6119 C CMP AL,030H ; AL,030H
6120 C JE F6 ; F6
6121 C
6122 C F5: JMP V_RET ; RETURN TO CALLER
6123 C
6124 C F6: MOV CX,POINTS ; CX,POINTS
6125 C MOV DL,ROWS ; DL,ROWS
6126 C CMP BH,7 ; CMP BH,7
6127 C JZ F7 ; F7
6128 C CMP BH,1 ; CMP BH,1
6129 C JA F7 ; JA F7
6130 C
6131 C ;----- INFORMATION RETURN DONE HERE
6132 C
6133 C AH11_INFORM:
6134 C ASSUME DS:ABSO ; DS:ABSO
6135 C CMP AL,030H ; AL,030H
6136 C JE F6 ; F6
6137 C
6138 C F5: JMP V_RET ; RETURN TO CALLER
6139 C
6140 C F6: MOV CX,POINTS ; CX,POINTS
6141 C MOV DL,ROWS ; DL,ROWS
6142 C CMP BH,7 ; CMP BH,7
6143 C JZ F7 ; F7
6144 C CMP BH,1 ; CMP BH,1
6145 C JA F7 ; JA F7
6146 C
6147 C ;----- INFORMATION RETURN DONE HERE
6148 C
6149 C AH11_INFORM:
6150 C ASSUME DS:ABSO ; DS:ABSO
6151 C CMP AL,030H ; AL,030H
6152 C JE F6 ; F6
6153 C
6154 C F5: JMP V_RET ; RETURN TO CALLER
6155 C
6156 C F6: MOV CX,POINTS ; CX,POINTS
6157 C MOV DL,ROWS ; DL,ROWS
6158 C CMP BH,7 ; CMP BH,7
6159 C JZ F7 ; F7
6160 C CMP BH,1 ; CMP BH,1
6161 C JA F7 ; JA F7
6162 C
6163 C ;----- INFORMATION RETURN DONE HERE
6164 C
6165 C AH11_INFORM:
6166 C ASSUME DS:ABSO ; DS:ABSO
6167 C CMP AL,030H ; AL,030H
6168 C JE F6 ; F6
6169 C
6170 C F5: JMP V_RET ; RETURN TO CALLER
6171 C
6172 C F6: MOV CX,POINTS ; CX,POINTS
6173 C MOV DL,ROWS ; DL,ROWS
6174 C CMP BH,7 ; CMP BH,7
6175 C JZ F7 ; F7
6176 C CMP BH,1 ; CMP BH,1
6177 C JA F7 ; JA F7
6178 C
6179 C ;----- INFORMATION RETURN DONE HERE
6180 C
6181 C AH11_INFORM:
6182 C ASSUME DS:ABSO ; DS:ABSO
6183 C CMP AL,030H ; AL,030H
6184 C JE F6 ; F6
6185 C
6186 C F5: JMP V_RET ; RETURN TO CALLER
6187 C
6188 C F6: MOV CX,POINTS ; CX,POINTS
6189 C MOV DL,ROWS ; DL,ROWS
6190 C CMP BH,7 ; CMP BH,7
6191 C JZ F7 ; F7
6192 C CMP BH,1 ; CMP BH,1
6193 C JA F7 ; JA F7
6194 C
6195 C ;----- INFORMATION RETURN DONE HERE
6196 C
6197 C AH11_INFORM:
6198 C ASSUME DS:ABSO ; DS:ABSO
6199 C CMP AL,030H ; AL,030H
6200 C JE F6 ; F6
6201 C
6202 C F5: JMP V_RET ; RETURN TO CALLER
6203 C
6204 C F6: MOV CX,POINTS ; CX,POINTS
6205 C MOV DL,ROWS ; DL,ROWS
6206 C CMP BH,7 ; CMP BH,7
6207 C JZ F7 ; F7
6208 C CMP BH,1 ; CMP BH,1
6209 C JA F7 ; JA F7
6210 C
6211 C ;----- INFORMATION RETURN DONE HERE
6212 C
6213 C AH11_INFORM:
6214 C ASSUME DS:ABSO ; DS:ABSO
6215 C CMP AL,030H ; AL,030H
6216 C JE F6 ; F6
6217 C
6218 C F5: JMP V_RET ; RETURN TO CALLER
6219 C
6220 C F6: MOV CX,POINTS ; CX,POINTS
6221 C MOV DL,ROWS ; DL,ROWS
6222 C CMP BH,7 ; CMP BH,7
6223 C JZ F7 ; F7
6224 C CMP BH,1 ; CMP BH,1
6225 C JA F7 ; JA F7
6226 C
6227 C ;----- INFORMATION RETURN DONE HERE
6228 C
6229 C AH11_INFORM:
6230 C ASSUME DS:ABSO ; DS:ABSO
6231 C CMP AL,030H ; AL,030H
6232 C JE F6 ; F6
6233 C
6234 C F5: JMP V_RET ; RETURN TO CALLER
6235 C
6236 C F6: MOV CX,POINTS ; CX,POINTS
6237 C MOV DL,ROWS ; DL,ROWS
6238 C CMP BH,7 ; CMP BH,7
6239 C JZ F7 ; F7
6240 C CMP BH,1 ; CMP BH,1
6241 C JA F7 ; JA F7
6242 C
6243 C ;----- INFORMATION RETURN DONE HERE
6244 C
6245 C AH11_INFORM:
6246 C ASSUME DS:ABSO ; DS:ABSO
6247 C CMP AL,030H ; AL,030H
6248 C JE F6 ; F6
6249 C
6250 C F5: JMP V_RET ; RETURN TO CALLER
6251 C
6252 C F6: MOV CX,POINTS ; CX,POINTS
6253 C MOV DL,ROWS ; DL,ROWS
6254 C CMP BH,7 ; CMP BH,7
6255 C JZ F7 ; F7
6256 C CMP BH,1 ; CMP BH,1
6257 C JA F7 ; JA F7
6258 C
6259 C ;----- INFORMATION RETURN DONE HERE
6260 C
6261 C AH11_INFORM:
6262 C ASSUME DS:ABSO ; DS:ABSO
6263 C CMP AL,030H ; AL,030H
6264 C JE F6 ; F6
6265 C
6266 C F5: JMP V_RET ; RETURN TO CALLER
6267 C
6268 C F6: MOV CX,POINTS ; CX,POINTS
6269 C MOV DL,ROWS ; DL,ROWS
6270 C CMP BH,7 ; CMP BH,7
6271 C JZ F7 ; F7
6272 C CMP BH,1 ; CMP BH,1
6273 C JA F7 ; JA F7
6274 C
6275 C ;----- INFORMATION RETURN DONE HERE
6276 C
6277 C AH11_INFORM:
6278 C ASSUME DS:ABSO ; DS:ABSO
6279 C CMP AL,030H ; AL,030H
6280 C JE F6 ; F6
6281 C
6282 C F5: JMP V_RET ; RETURN TO CALLER
6283 C
6284 C F6: MOV CX,POINTS ; CX,POINTS
6285 C MOV DL,ROWS ; DL,ROWS
6286 C CMP BH,7 ; CMP BH,7
6287 C JZ F7 ; F7
6288 C CMP BH,1 ; CMP BH,1
6289 C JA F7 ; JA F7
6290 C
6291 C ;----- INFORMATION RETURN DONE HERE
6292 C
6293 C AH11_INFORM:
6294 C ASSUME DS:ABSO ; DS:ABSO
6295 C CMP AL,030H ; AL,030H
6296 C JE F6 ; F6
6297 C
6298 C F5: JMP V_RET ; RETURN TO CALLER
6299 C
6300 C F6: MOV CX,POINTS ; CX,POINTS
6301 C MOV DL,ROWS ; DL,ROWS
6302 C CMP BH,7 ; CMP BH,7
6303 C JZ F7 ; F7
6304 C CMP BH,1 ; CMP BH,1
6305 C JA F7 ; JA F7
6306 C
6307 C ;----- INFORMATION RETURN DONE HERE
6308 C
6309 C AH11_INFORM:
6310 C ASSUME DS:ABSO ; DS:ABSO
6311 C CMP AL,030H ; AL,030H
6312 C JE F6 ; F6
6313 C
6314 C F5: JMP V_RET ; RETURN TO CALLER
6315 C
6316 C F6: MOV CX,POINTS ; CX,POINTS
6317 C MOV DL,ROWS ; DL,ROWS
6318 C CMP BH,7 ; CMP BH,7
6319 C JZ F7 ; F7
6320 C CMP BH,1 ; CMP BH,1
6321 C JA F7 ; JA F7
6322 C
6323 C ;----- INFORMATION RETURN DONE HERE
6324 C
6325 C AH11_INFORM:
6326 C ASSUME DS:ABSO ; DS:ABSO
6327 C CMP AL,030H ; AL,030H
6328 C JE F6 ; F6
6329 C
6330 C F5: JMP V_RET ; RETURN TO CALLER
6331 C
6332 C F6: MOV CX,POINTS ; CX,POINTS
6333 C MOV DL,ROWS ; DL,ROWS
6334 C CMP BH,7 ; CMP BH,7
6335 C JZ F7 ; F7
6336 C CMP BH,1 ; CMP BH,1
6337 C JA F7 ; JA F7
6338 C
6339 C ;----- INFORMATION RETURN DONE HERE
6340 C
6341 C AH11_INFORM:
6342 C ASSUME DS:ABSO ; DS:ABSO
6343 C CMP AL,030H ; AL,030H
6344 C JE F6 ; F6
6345 C
6346 C F5: JMP V_RET ; RETURN TO CALLER
6347 C
6348 C F6: MOV CX,POINTS ; CX,POINTS
6349 C MOV DL,ROWS ; DL,ROWS
6350 C CMP BH,7 ; CMP BH,7
6351 C JZ F7 ; F7
6352 C CMP BH,1 ; CMP BH,1
6353 C JA F7 ; JA F7
6354 C
6355 C ;----- INFORMATION RETURN DONE HERE
6356 C
6357 C AH11_INFORM:
6358 C ASSUME DS:ABSO ; DS:ABSO
6359 C CMP AL,030H ; AL,030H
6360 C JE F6 ; F6
6361 C
6362 C F5: JMP V_RET ; RETURN TO CALLER
6363 C
6364 C F6: MOV CX,POINTS ; CX,POINTS
6365 C MOV DL,ROWS ; DL,ROWS
6366 C CMP BH,7 ; CMP BH,7
6367 C JZ F7 ; F7
6368 C CMP BH,1 ; CMP BH,1
6369 C JA F7 ; JA F7
6370 C
6371 C ;----- INFORMATION RETURN DONE HERE
6372 C
6373 C AH11_INFORM:
6374 C ASSUME DS:ABSO ; DS:ABSO
6375 C CMP AL,030H ; AL,030H
6376 C JE F6 ; F6
6377 C
6378 C F5: JMP V_RET ; RETURN TO CALLER
6379 C
6380 C F6: MOV CX,POINTS ; CX,POINTS
6381 C MOV DL,ROWS ; DL,ROWS
6382 C CMP BH,7 ; CMP BH,7
6383 C JZ F7 ; F7
6384 C CMP BH,1 ; CMP BH,1
6385 C JA F7 ; JA F7
6386 C
6387 C ;----- INFORMATION RETURN DONE HERE
6388 C
6389 C AH11_INFORM:
6390 C ASSUME DS:ABSO ; DS:ABSO
6391 C CMP AL,030H ; AL,030H
6392 C JE F6 ; F6
6393 C
6394 C F5: JMP V_RET ; RETURN TO CALLER
6395 C
6396 C F6: MOV CX,POINTS ; CX,POINTS
6397 C MOV DL,ROWS ; DL,ROWS
6398 C CMP BH,7 ; CMP BH,7
6399 C JZ F7 ; F7
6400 C CMP BH,1 ; CMP BH,1
6401 C JA F7 ; JA F7
6402 C
6403 C ;----- INFORMATION RETURN DONE HERE
6404 C
6405 C AH11_INFORM:
6406 C ASSUME DS:ABSO ; DS:ABSO
6407 C CMP AL,030H ; AL,030H
6408 C JE F6 ; F6
6409 C
6410 C F5: JMP V_RET ; RETURN TO CALLER
6411 C
6412 C F6: MOV CX,POINTS ; CX,POINTS
6413 C MOV DL,ROWS ; DL,ROWS
6414 C CMP BH,7 ; CMP BH,7
6415 C JZ F7 ; F7
6416 C CMP BH,1 ; CMP BH,1
6417 C JA F7 ; JA F7
6418 C
6419 C ;----- INFORMATION RETURN DONE HERE
6420 C
6421 C AH11_INFORM:
6422 C ASSUME DS:ABSO ; DS:ABSO
6423 C CMP AL,030H ; AL,030H
6424 C JE F6 ; F6
6425 C
6426 C F5: JMP V_RET ; RETURN TO CALLER
6427 C
6428 C F6: MOV CX,POINTS ; CX,POINTS
6429 C MOV DL,ROWS ; DL,ROWS
6430 C CMP BH,7 ; CMP BH,7
6431 C JZ F7 ; F7
6432 C CMP BH,1 ; CMP BH,1
6433 C JA F7 ; JA F7
6434 C
6435 C ;----- INFORMATION RETURN DONE HERE
6436 C
6437 C AH11_INFORM:
6438 C ASSUME DS:ABSO ; DS:ABSO
6439 C CMP AL,030H ; AL,030H
6440 C JE F6 ; F6
6441 C
6442 C F5: JMP V_RET ; RETURN TO CALLER
6443 C
6444 C F6: MOV CX,POINTS ; CX,POINTS
6445 C MOV DL,ROWS ; DL,ROWS
6446 C CMP BH,7 ; CMP BH,7
6447 C JZ F7 ; F7
6448 C CMP BH,1 ; CMP BH,1
6449 C JA F7 ; JA F7
6450 C
6451 C ;----- INFORMATION RETURN DONE HERE
6452 C
6453 C AH11_INFORM:
6454 C ASSUME DS:ABSO ; DS:ABSO
6455 C CMP AL,030H ; AL,030H
6456 C JE F6 ; F6
6457 C
6458 C F5: JMP V_RET ; RETURN TO CALLER
6459 C
6460 C F6: MOV CX,POINTS ; CX,POINTS
6461 C MOV DL,ROWS ; DL,ROWS
6462 C CMP BH,7 ; CMP BH,7
6463 C JZ F7 ; F7
6464 C CMP BH,1 ; CMP BH,1
6465 C JA F7 ; JA F7
6466 C
6467 C ;----- INFORMATION RETURN DONE HERE
6468 C
6469 C AH11_INFORM:
6470 C ASSUME DS:ABSO ; DS:ABSO
6471 C CMP AL,030H ; AL,030H
6472 C JE F6 ; F6
6473 C
6474 C F5: JMP V_RET ; RETURN TO CALLER
6475 C
6476 C F6: MOV CX,POINTS ; CX,POINTS
6477 C MOV DL,ROWS ; DL,ROWS
6478 C CMP BH,7 ; CMP BH,7
6479 C JZ F7 ; F7
6480 C CMP BH,1 ; CMP BH,1
6481 C JA F7 ; JA F7
6482 C
6483 C ;----- INFORMATION RETURN DONE HERE
6484 C
6485 C AH11_INFORM:
6486 C ASSUME DS:ABSO ; DS:ABSO
6487 C CMP AL,030H ; AL,030H
6488 C JE F6 ; F6
6489 C
6490 C F5: JMP V_RET ; RETURN TO CALLER
6491 C
6492 C F6: MOV CX,POINTS ; CX,POINTS
6493 C MOV DL,ROWS ; DL,ROWS
6494 C CMP BH,7 ; CMP BH,7
6495 C JZ F7 ; F7
6496 C CMP BH,1 ; CMP BH,1
6497 C JA F7 ; JA F7
6498 C
6499 C ;----- INFORMATION RETURN DONE HERE
6500 C
6501 C AH11_INFORM:
6502 C ASSUME DS:ABSO ; DS:ABSO
6503 C CMP AL,030H ; AL,030H
6504 C JE F6 ; F6
6505 C
6506 C F5: JMP V_RET ; RETURN TO CALLER
6507 C
6508 C F6: MOV CX,POINTS ; CX,POINTS
6509 C MOV DL,ROWS ; DL,ROWS
6510 C CMP BH,7 ; CMP BH,7
6511 C JZ F7 ; F7
6512 C CMP BH,1 ; CMP BH,1
6513 C JA F7 ; JA F7
6514 C
6515 C ;----- INFORMATION RETURN DONE HERE
6516 C
6517 C AH11_INFORM:
6518 C ASSUME DS:ABSO ; DS:ABSO
6519 C CMP AL,030H ; AL,030H
6520 C JE F6 ; F6
6521 C
6522 C F5: JMP V_RET ; RETURN TO CALLER
6523 C
6524 C F6: MOV CX,POINTS ; CX,POINTS
6525 C MOV DL,ROWS ; DL,ROWS
6526 C CMP BH,7 ; CMP BH,7
6527 C JZ F7 ; F7
6528 C CMP BH,1 ; CMP BH,1
6529 C JA F7 ; JA F7
6530 C
6531 C ;----- INFORMATION RETURN DONE HERE
6532 C
6533 C AH11_INFORM:
6534 C ASSUME DS:ABSO ; DS:ABSO
6535 C CMP AL,030H ; AL,030H
6536 C JE F6 ; F6
6537 C
6538 C F5: JMP V_RET ; RETURN TO CALLER
6539 C
6540 C F6: MOV CX,POINTS ; CX,POINTS
6541 C MOV DL,ROWS ; DL,ROWS
6542 C CMP BH,7 ; CMP BH,7
6543 C JZ F7 ; F7
6544 C CMP BH,1 ; CMP BH,1
6545 C JA F7 ; JA F7
6546 C
6547 C ;----- INFORMATION RETURN DONE HERE
6548 C
6549 C AH11_INFORM:
6550 C ASSUME DS:ABSO ; DS:ABSO
6551 C CMP AL,030H ; AL,030H
6552 C JE F6 ; F6
6553 C
6554 C F5: JMP V_RET ; RETURN TO CALLER
6555 C
6556 C F6: MOV CX,POINTS ; CX,POINTS
6557 C MOV DL,ROWS ; DL,ROWS
6558 C CMP BH,7 ; CMP BH,7
6559 C JZ F7 ; F7
6560 C CMP BH,1 ; CMP BH,1
6561 C JA F7 ; JA F7
6562 C
6563 C ;----- INFORMATION RETURN DONE HERE
6564 C
6565 C AH11_INFORM:
6566 C ASSUME DS:ABSO ; DS:ABSO
6567 C CMP AL,030H ; AL,030H
6568 C JE F6 ; F6
6569 C
6570 C F5: JMP V_RET ; RETURN TO CALLER
6571 C
6572 C F6: MOV CX,POINTS ; CX,POINTS
6573 C MOV DL,ROWS ; DL,ROWS
6574 C CMP BH,7 ; CMP BH,7
6575 C JZ F7 ; F7
6576 C CMP BH,1 ; CMP BH,1
6577 C JA F7 ; JA F7
6578 C
6579 C ;----- INFORMATION RETURN DONE HERE
6580 C
6581 C AH11_INFORM:
6582 C ASSUME DS:ABSO ; DS:ABSO
6583 C CMP AL,030H ; AL,030H
6584 C JE F6 ; F6
6585 C
6586 C F5: JMP V_RET ; RETURN TO CALLER
6587 C
6588 C F6: MOV CX,POINTS ; CX,POINTS
6589 C MOV DL,ROWS ; DL,ROWS
6590 C CMP BH,7 ; CMP BH,7
6591 C JZ F7 ; F7
6592 C CMP BH,1 ; CMP BH,1
6593 C JA F7 ; JA F7
6594 C
6595 C ;----- INFORMATION RETURN DONE HERE
6596 C
6597 C AH11_INFORM:
6598 C ASSUME DS:ABSO ; DS:ABSO
6599 C CMP AL,030H ; AL,030H
6600 C JE F6 ; F6
6601 C
6602 C F5: JMP V_RET ; RETURN TO CALLER
6603 C
6604 C F6: MOV CX,POINTS ; CX,POINTS
6605 C MOV DL,ROWS ; DL,ROWS
6606 C CMP BH,7 ; CMP BH,7
6607 C JZ F7 ; F7
6608 C CMP BH,1 ; CMP BH,1
6609 C JA F7 ; JA F7
6610 C
6611 C ;----- INFORMATION RETURN DONE HERE
6612 C
6613 C AH11_INFORM:
6614 C ASSUME DS:ABSO ; DS:ABSO
6615 C CMP AL,030H ; AL,030H
6616 C JE F6 ; F6
6617 C
6618 C F5: JMP V_RET ; RETURN TO CALLER
6619 C
6620 C F6: MOV CX,POINTS ; CX,POINTS
6621 C MOV DL,ROWS ; DL,ROWS
6622 C CMP BH,7 ; CMP BH,7
6623 C JZ F7 ; F7
6624 C CMP BH,1 ; CMP BH,1
6625 C JA F7 ; JA F7
6626 C
6627 C ;----- INFORMATION RETURN DONE HERE
6628 C
6629 C AH11_INFORM:
6630 C ASSUME DS:ABSO ; DS:ABSO
6631 C CMP AL,030H ; AL,030H
6632 C JE F6 ; F6
6633 C
6634 C F5: JMP V_RET ; RETURN TO CALLER
6635 C
6636 C F6: MOV CX,POINTS ; CX,POINTS
6637 C MOV DL,ROWS ; DL,ROWS
6638 C CMP BH,7 ; CMP BH,7
6639 C JZ F7 ; F7
6640 C CMP BH,1 ; CMP BH,1
6641 C JA F7 ; JA F7
6642 C
6643 C ;----- INFORMATION RETURN DONE HERE
6644 C
6645 C AH11_INFORM:
6646 C ASSUME DS:ABSO ; DS:ABSO
6647 C CMP AL,030H ; AL,030H
6648 C JE F6 ; F6
6649 C
6650 C F5: JMP V_RET ; RETURN TO CALLER
6651 C
6652 C F6: MOV CX,POINTS ; CX,POINTS
6653 C MOV DL,ROWS ; DL,ROWS
6654 C CMP BH,7 ; CMP BH,7
6655 C JZ F7 ; F7
6656 C CMP BH,1 ; CMP BH,1
6657 C JA F7 ; JA F7
6658 C
6659 C ;----- INFORMATION RETURN DONE HERE
6660 C
6661 C AH11_INFORM:
6662 C ASSUME DS:ABSO ; DS:ABSO
6663 C CMP AL,030H ; AL,030H
6664 C JE F6 ; F6
6665 C
6666 C F5: JMP V_RET ; RETURN TO CALLER
6667 C
6668 C F6: MOV CX,POINTS ; CX,POINTS
6669 C MOV DL,ROWS ; DL,ROWS
6670 C CMP BH,7 ; CMP BH,7
6671 C JZ F7 ; F7
6672 C CMP BH,1 ; CMP BH,1
6673 C JA F7 ; JA F7
6674 C
6675 C ;----- INFORMATION RETURN DONE HERE
6676 C
6677 C AH11_INFORM:
6678 C ASSUME DS:ABSO ; DS:ABSO
6679 C CMP AL,030H ; AL,030H
6680 C JE F6 ; F6
6681 C
6682 C F5: JMP V_RET ; RETURN TO CALLER
6683 C
6684 C F6: MOV CX,POINTS ; CX,POINTS
6685 C MOV DL,ROWS ; DL,ROWS
6686 C CMP BH,7 ; CMP BH,7
6687 C JZ F7 ; F7
6688 C CMP BH,1 ; CMP BH,1
6689 C JA F7 ; JA F7
6690 C
6691 C ;----- INFORMATION RETURN DONE HERE
6692 C
6693 C AH11_INFORM:
6694 C ASSUME DS:ABSO ; DS:ABSO
6695 C CMP AL,030H ; AL,030H
6696 C JE F6 ; F6
6697 C
6698 C F5: JMP V_RET ; RETURN TO CALLER
6699 C
6700 C F6: MOV CX,POINTS ; CX,POINTS
6701 C MOV DL,ROWS ; DL,ROWS
6702 C CMP BH,7 ; CMP BH,7
6703 C JZ F7 ; F7
6704 C CMP BH,1 ; CMP BH,1
6705 C JA F7 ; JA F7
6706 C
6707 C ;----- INFORMATION RETURN DONE HERE
6708 C
6709 C AH11_INFORM:
6710 C ASSUME DS:ABSO ; DS:ABSO
6711 C CMP AL,030H ; AL,030H
6712 C JE F6 ; F6
6713 C
6714 C F5: JMP V_RET ; RETURN TO CALLER
6715 C
6716 C F6: MOV CX,POINTS ; CX,POINTS
6717 C MOV DL,ROWS ; DL,ROWS
6718 C CMP BH,7 ; CMP BH,7
6719 C JZ F7 ; F7
6720 C CMP BH,1 ; CMP BH,1
6721 C JA F7 ; JA F7
6722 C
6723 C ;----- INFORMATION RETURN DONE HERE
6724 C
6725 C AH11_INFORM:
6726 C ASSUME DS:ABSO ; DS:ABSO
6727 C CMP AL,030H ; AL,030H
6728 C JE F6 ; F6
6729 C
6730 C F5: JMP V_RET ; RETURN TO CALLER
6731 C
6732 C F6: MOV CX,POINTS ; CX,POINTS
6733 C MOV DL,ROWS ; DL,ROWS
6734 C CMP BH,7 ; CMP BH,7
6735 C JZ F7 ; F7
6736 C CMP BH,1 ; CMP BH,1
6737 C JA F7 ; JA F7
6738 C
6739 C ;----- INFORMATION RETURN DONE HERE
6740 C
6741 C AH11_INFORM:
6742 C ASSUME DS:ABSO ; DS:ABSO
6743 C CMP AL,030H ; AL,030H
6744 C JE F6 ; F6
6745 C
6746 C F5: JMP V_RET ; RETURN TO CALLER
6747 C
6748 C F6: MOV CX,POINTS ; CX,POINTS
6749 C MOV DL,ROWS ; DL,ROWS
6750 C CMP BH,7 ; CMP BH,7
6751 C JZ F7 ; F7
6752 C CMP BH,1 ; CMP BH,1
6753 C JA F7 ; JA F7
6754 C
6755 C ;----- INFORMATION RETURN DONE HERE
6756 C
6757 C AH11_INFORM:
6758 C ASSUME DS:ABSO ; DS:ABSO
6759 C CMP AL,030H ; AL,030H
6760 C JE F6 ; F6
6761 C
6762 C F5: JMP V_RET ; RETURN TO CALLER
6763 C
6764 C F6: MOV CX,POINTS ; CX,POINTS
6765 C MOV DL,ROWS ; DL,ROWS
6766 C CMP BH,7 ; CMP BH,7
6767 C JZ F7 ; F7
6768 C CMP BH,1 ; CMP BH,1
6769 C JA F7 ; JA F7
6770 C
6771 C ;----- INFORMATION RETURN DONE HERE
6772 C
6773 C AH11_INFORM:
6774 C ASSUME DS:AB
```

```

208A 0A FF      5923  C   OR   BH,BH
208C 75 07      5924  C   JNZ  F9
208E C4 2E 007C R 5925  C   LES  BP,EXT_PTR
2092 EB 1A 90    5926  C   JMP  INFORM_OUT
2094 EB 1A 90    5927  C   F9:  LESS BP,GRX_SET
2095 C4 2E 010C R 5928  C   JMP  INFORM_OUT
2099 EB 13 90    5929  C
209A EB 13 90    5930  C
209B EB 13 90    5931  C
209C EB 13 90    5932  C
209D EB 13 90    5933  C   F7:  ASSUME DS:ABSO
209E 80 EF 02    5934  C   SUB  BH,2
209F 80 DF 02    5935  C   MOV  BH,BH
20A0 2A FF      5936  C   SUB  BH,BH
20A1 2A FF      5937  C   SAL  BX,1
20A3 D1 E3      5938  C   ADD  BX,OFFSET TBL_5
20A5 81 C3 20B7 R 5939  C   MOV  BP,CS:[BX]
20A9 2E 8B 2F    5940  C   PUSH CS
20A9 0E          5941  C   PUSH ES
20A9 07          5942  C   POP  ES
20A9 07          5943  C
20A9 07          5944  C   INFORM_OUT:
20A9 07          5945  C   POP  DI
20A9 07          5946  C   POP  SI
20B0 5B          5947  C   POP  BX
20B1 5B          5948  C   POP  AX
20B2 5B          5949  C   POP  AX
20B3 5B          5950  C   POP  DS
20B4 5B          5951  C   POP  AX
20B5 5B          5952  C   POP  AX
20B6 CF          5953  C   IRET
20B6 CF          5954  C
20B7 0000 E      5955  C   ;----- TABLE OF CHARACTER GENERATOR OFFSETS
20B7 0000 E      5956  C   ;----- TBL_5 LABEL WORD
20B8 0000 E      5957  C   DW  OFFSET COMM
20B9 0000 E      5958  C   DW  OFFSET CGDDOT
20B8 0000 E      5959  C   DW  OFFSET INT_1F_1
20BD 0000 E      5960  C   DW  OFFSET CGMR_FDG
20BD 0000 E      5961  C
20B8 0000 E      5962  C   SUBTLL
20B8 0000 E      5963  C
20B8 0000 E      5964  C
20B8 0000 E      5965  C   ;----- ALTERNATE SELECT
20B8 0000 E      5966  C
20B8 0000 E      5967  C   AH12:
20B8 0000 E      5968  C   ASSUME DS:ABSO
20B8 0000 E      5969  C   CMP  BL,010H
20B8 0000 E      5970  C   JB   ACT_1
20B8 0000 E      5971  C   JE   ACT_3
20B8 0000 E      5972  C   CMP  BL,020H
20B8 0000 E      5973  C   JE   ACT_2
20B8 0000 E      5974  C   JMP  V_RET
20B8 0000 E      5975  C   ACT_2:
20B8 0000 E      5976  C   SRLOAD DS,0
20B8 0000 E      5977  C   SUB  DX,DX
20B8 0000 E      5978  C   MOV  DS,DX
20B8 0000 E      5979  C
20B8 0000 E      5980  C   MOV  WORD PTR INT5_PTR,OFFSET PRINT_SCREEN
20B8 0000 E      5981  C   WORD PTR INT5_PTR+2,CS
20B8 0000 E      5982  C
20B8 0000 E      5983  C
20B8 0000 E      5984  C   ACT_3:
20B8 0000 E      5985  C   MOV  BH,INFO
20B8 0000 E      5986  C   AND  BH,1
20B8 0000 E      5987  C   SHR  BH,1
20B8 0000 E      5988  C
20B8 0000 E      5989  C   MOV  AL,INFO
20B8 0000 E      5990  C   AND  AL,01100000B
20B8 0000 E      5991  C   MOV  CL,2
20B8 0000 E      5992  C   SHR  CL,1
20B8 0000 E      5993  C   MOV  AL,CL
20B8 0000 E      5994  C   MOV  BL,AL
20B8 0000 E      5995  C   MOV  CL,INFO_3
20B8 0000 E      5996  C   MOV  CH,CL
20B8 0000 E      5997  C   AND  CL,0FH
20B8 0000 E      5998  C   SHR  CH,1
2100 DO ED      5999  C
2102 DO ED      6000  C   SHR  CH,1
2104 DO ED      6001  C   SHR  CH,1
2106 80 E5 0F    6002  C   AND  CH,0FH
2106 80 E5 0F    6003  C
2106 80 E5 0F    6004  C   POP  DI
2106 80 E5 0F    6005  C   POP  SI
2106 80 E5 0F    6006  C   POP  DX
2106 80 E5 0F    6007  C   POP  DS
2106 80 E5 0F    6008  C   POP  DX
2106 80 E5 0F    6009  C   POP  DS
2106 80 E5 0F    6010  C   POP  ES
2106 80 E5 0F    6011  C   POP  BP
2106 80 E5 0F    6012  C   IRET
2106 80 E5 0F    6013  C
2112 E9 219E R  6014  C   AH12_X:
2112 E9 219E R  6015  C   JMP  V_RET
2115 00 00 00    6016  C   ACT_1:
2115 00 00 00    6017  C   STR_OUTZ:
2115 00 00 00    6018  C   JMP  V_RET
2115 00 00 00    6019  C
2115 00 00 00    6020  C
2115 00 00 00    6021  C
2118 3C 04      6022  C   AH13:
2118 73 F9      6023  C   CMP  AL,04
211C E3 F7      6024  C   JAE  STR_OUTZ
211C E3 F7      6025  C   JCXZ STR_OUTZ
211F 8A DF      6026  C   PUSH BX
2121 2A FF      6027  C   SUB  BH,BH
2123 D1 E3      6028  C   SAL  BX,1
2123 D1 E3      6029  C   MOV  SI,[BX + OFFSET CURSOR_POSN]
2129 5B          6030  C   POP  SI
2129 5B          6031  C   PUSH SI
2129 5B          6032  C
212B 50          6033  C   PUSH AX
212C 88 0200    6034  C   MOV  AX,0200H
212F CD 10      6035  C   INT  10H
2131 5B          6036  C   POP  AX
2132 00          6037  C
2133 51          6038  C   STR_1:
2133 53          6039  C   PUSH CX
2134 50          6040  C   PUSH AX
2135 86 E0      6041  C   XCHG AH,AL
2135 86 E0      6042  C   MOV  AL,ES:[BP]
213B A5          6043  C   INC  BP
213C 3C 00      6044  C   CMP  AL,0DH
213E 74 3D      6045  C   JE   STR_CR_LF
2140 3C 0A      6046  C   CMP  AL,0AH
2140 70 39      6047  C   JE   STR_CR_LF
2144 3C 08      6048  C   CMP  AL,08H

```

```

2146 74 35          6099    JE     STR_CR_LF
214A 3C 07          6050    CMP    AL,07H
214C 74 31          6051    JE     STR_CR_LF
214C B9 0001        6052    MOV    CX,T
214C B8 FC 02        6053    CMP    AH,2
2152 72 02          6054    JB    DO_STR
2154 26: 8A 5E 00    6055    MOV    BL,ES:[BP]
2158 45             6056    INC    INC BP
2159             6057    DO_STR: ; BELL
2159 B4 09          6058    MOV    AH,09H
2159 CD 10          6059    INT    10H
215D FE C9          6060    INC    DL
215F 3A 16 044A R   6061    CMP    DL_BYT PTR CRT_COLS
2160 72 11          6062    JB    STR_2
2165 00 00 0484 R   6063    CMP    DH
2169 75 07          6064    JNE    STR_3
216B B8 0E0A        6065    MOV    AX,0E0AH
216E CD 10          6066    INT    10H
2171 FE CE          6067    DEC    DH
2172             6068    STR_3: ; WRITE THE CHAR/ATTR
2174 2A D2          6069    INC    DH
2174             6070    SUB    DL,DL
2176 B8 0200        6071    STR_2: ; NEXT CURSOR POSITION
2179 CD 10          6072    MOV    AX,0200H
217B EB 0E          6073    INT    10H
217D             6074    JMP    SHORT STR_4
217D B8 0E          6075    STR_CR_LF: ; COLUMN OVERFLOW
217F CD 10          6076    MOV    AH,0EH
2181 8A DF          6077    INT    10H
2183 2A FF          6078    MOV    BL,BH
2185 D1 E3          6079    SUB    BH,BH
2189 8B 97 0450 R   6080    SAL    BX,1
218B             6081    MOV    DX,[BX + OFFSET_CURSOR_POSN] ; *2 FOR WORD OFFSET
218B             6082    STR_4: ; GET CURSOR POSITION
218B 58             6083    POP    AX
218C 5B             6084    POP    BX
218D 59             6085    POP    CX
218E E2 A2          6086    LOOP   STR_1
218F             6087    STR_OUT: ; RECOVER CURSOR POSITION
2190 5A             6088    POP    DX
2191 3C 01          6089    POP    DX
2193 74 09          6090    CMP    AL,1
2195 3C 03          6091    JE     STR_OUT
2197 74 05          6092    CMP    AL,3
2199 B8 0200        6093    JE     STR_OUT
219C CD 10          6094    MOV    AX,0200H
219E             6095    INT    10H
219E             6096    STR_OUT: ; SET CURSOR POSITION
219F             6097    ; ALLOW FALL THROUGH
2199             6098
2199             6099
219E V_RET PROC NEAR
219E             6100    POP    DI
219F 5F             6101    POP    SI
21A0 5E             6102    POP    BX
21A1 5B             6103    POP    CX
21A1 59             6104    POP    DX
21A2 5A             6105    POP    DS
21A3 1F             6106    POP    ES
21A4 07             6107    POP    BP
21A5 30             6108    POP    BP
21A6 CF             6109    RET
21A7             6110    V_RET ENDP
21A7             6111
21A7 COMBO_VIDEO ENDP
21A7             6112
21A7             6113
21A7             6114    INCLUDE VPRSC.INC
21A7             6115    SUBTL VPRSC.INC
21A7             6116    PAGE
21A7             6117
21A7             6118    C----- INTERRUPT 5
21A7             6119    THIS LOGIC WILL BE INVOKED BY INTERRUPT 05H TO PRINT THE
21A7             6120    SCREEN. THE CURSOR POSITION AT THE TIME THIS ROUTINE IS INVOKED
21A7             6121    WILL BE SAVED AND RESTORED UPON RETURN. THIS ROUTINE IS
21A7             6122    INTENDED TO RUN WITH INTERRUPTS ENABLED. IF A SUBSEQUENT
21A7             6123    'PRINT SCREEN' KEY IS DEPRESSED DURING THE TIME THIS ROUTINE
21A7             6124    IS PRINTING, IT WILL BE IGNORED.
21A7             6125    ADDRESS 50:0 CONTAINS THE STATUS OF THE PRINT SCREEN:
21A7             6126
21A7             6127
21A7             6128
21A7             6129
21A7             6130
21A7             6131
21A7             6132
21A7             6133    ASSUME CS:CODE,DS:AB50
21A7 PRINT_SCREEN PROC FAR
21A7             6134    STI
21A8 1E             6135    PUSH DS ; MUST RUN WITH INTS ENABLED
21A8 1E             6136    PUSH AX ; MUST USE 50:0 FOR DATA
21A9 50             6137    PUSH BX ; AREA STORAGE
21A9 53             6138    PUSH CX ; USE THIS LATER FOR CURSOR LIMITS
21A9 21             6139    PUSH DX ; WILL HOLD CURRENT CURSOR POS
21AC 52             6140    CALL DDS
21AD E8 0CFE R     6141    CMP STATUS_BYTETIME,1 ; SEE IF PRINT ALREADY IN PROGRESS
21B0 80 3E 0500 R 01 6142    JZ .L1 ; IF PRINT IN PROGRESS
21B0 80 3E 0500 R 01 6143    MOV STATUS_BYTETIME,1 ; INDICATE PRINT NOW IN PROGRESS
21B7 C6 06 0500 R 01 6144    MOV AH,15 ; WILL REQUEST THE CURRENT MODE
21B8 B4 0F          6145    INT 10H ; [AL]=MODE (NOT USED)
21B8 CD 10          6146    ; [AH]=NUMBER COLUMNS/LINE
21B8             6147    ; [BH]=VISUAL PAGE
21B8             6148
21B8             6149
21B8             6150
21B8             6151
21B8             6152
21B8             6153
21C0 8A CC          6154    MOV CL,AH ; WILL MAKE USE OF [CX] REG TO
21C2 8A 2E 0484 R   6155    MOV CH,ROWS ; CONTROL ROW & COLUMNS
21C6 C5 0E          6156    INC CH
21C8 E8 2220 R     6157    CALL CRLF ; CAR RETURN LINE FEED ROUTINE
21C8 51             6158    PUSH CX ; SAVE SCREEN BOUNDS
21C8 B4 03          6159    MOV AH,3 ; WILL NOW READ THE CURSOR,
21CE CD 10          6160    INT 10H ; AND PRESERVE THE POSITION
21D0 29             6161    POP CX ; RECALCULATE SCREEN BOUNDS
21D1 52             6162    PUSH DX ; RECALL [BH]=VISUAL PAGE
21D2 33 D2          6163    XOR DX,DX ; SET CURSOR POSITION TO [0,0]
21D2             6164
21D2             6165
21D2             6166
21D2             6167
21D2             6168
21D2             6169
21D2             6170
21D2             6171
21D2             6172
21D2             6173
21D2             6174
21D2             6174 C----- PR110: ; TO INDICATE CURSOR SET REQUEST
21D2             6175    MOV AH,2 ; NEW CURSOR POS ESTABLISHED
21D2             6176    INT 10H ; TO INDICATE READ CHARACTER
21D2             6177    MOV AH,8 ; CHARACTER NOW IN [AL]
21D2             6178    INT 10H ; SEE IF VALID CHAR
21D2             6179
21D2             6180
21D2             6181
21D2             6182
21D2             6183
21D2             6184
21D2             6185
21D2             6186
21D2             6187
21D2             6188
21D2             6189
21D2             6190
21D2             6191
21D2             6192
21D2             6193
21D2             6194
21D2             6195
21D2             6196
21D2             6197
21D2             6198
21D2             6199
21D2             6200
21D2             6201
21D2             6202
21D2             6203
21D2             6204
21D2             6205
21D2             6206
21D2             6207
21D2             6208
21D2             6209
21D2             6210
21D2             6211
21D2             6212
21D2             6213
21D2             6214
21D2             6215
21D2             6216
21D2             6217
21D2             6218
21D2             6219
21D2             6220
21D2             6221
21D2             6222
21D2             6223
21D2             6224
21D2             6225
21D2             6226
21D2             6227
21D2             6228
21D2             6229
21D2             6230
21D2             6231
21D2             6232
21D2             6233
21D2             6234
21D2             6235
21D2             6236
21D2             6237
21D2             6238
21D2             6239
21D2             6240
21D2             6241
21D2             6242
21D2             6243
21D2             6244
21D2             6245
21D2             6246
21D2             6247
21D2             6248
21D2             6249
21D2             6250
21D2             6251
21D2             6252
21D2             6253
21D2             6254
21D2             6255
21D2             6256
21D2             6257
21D2             6258
21D2             6259
21D2             6260
21D2             6261
21D2             6262
21D2             6263
21D2             6264
21D2             6265
21D2             6266
21D2             6267
21D2             6268
21D2             6269
21D2             6270
21D2             6271
21D2             6272
21D2             6273
21D2             6274
21D2             6275
21D2             6276
21D2             6277
21D2             6278
21D2             6279
21D2             6280
21D2             6281
21D2             6282
21D2             6283
21D2             6284
21D2             6285
21D2             6286
21D2             6287
21D2             6288
21D2             6289
21D2             6290
21D2             6291
21D2             6292
21D2             6293
21D2             6294
21D2             6295
21D2             6296
21D2             6297
21D2             6298
21D2             6299
21D2             6300
21D2             6301
21D2             6302
21D2             6303
21D2             6304
21D2             6305
21D2             6306
21D2             6307
21D2             6308
21D2             6309
21D2             6310
21D2             6311
21D2             6312
21D2             6313
21D2             6314
21D2             6315
21D2             6316
21D2             6317
21D2             6318
21D2             6319
21D2             6320
21D2             6321
21D2             6322
21D2             6323
21D2             6324
21D2             6325
21D2             6326
21D2             6327
21D2             6328
21D2             6329
21D2             6330
21D2             6331
21D2             6332
21D2             6333
21D2             6334
21D2             6335
21D2             6336
21D2             6337
21D2             6338
21D2             6339
21D2             6340
21D2             6341
21D2             6342
21D2             6343
21D2             6344
21D2             6345
21D2             6346
21D2             6347
21D2             6348
21D2             6349
21D2             6350
21D2             6351
21D2             6352
21D2             6353
21D2             6354
21D2             6355
21D2             6356
21D2             6357
21D2             6358
21D2             6359
21D2             6360
21D2             6361
21D2             6362
21D2             6363
21D2             6364
21D2             6365
21D2             6366
21D2             6367
21D2             6368
21D2             6369
21D2             6370
21D2             6371
21D2             6372
21D2             6373
21D2             6374
21D2             6375
21D2             6376
21D2             6377
21D2             6378
21D2             6379
21D2             6380
21D2             6381
21D2             6382
21D2             6383
21D2             6384
21D2             6385
21D2             6386
21D2             6387
21D2             6388
21D2             6389
21D2             6390
21D2             6391
21D2             6392
21D2             6393
21D2             6394
21D2             6395
21D2             6396
21D2             6397
21D2             6398
21D2             6399
21D2             6400
21D2             6401
21D2             6402
21D2             6403
21D2             6404
21D2             6405
21D2             6406
21D2             6407
21D2             6408
21D2             6409
21D2             6410
21D2             6411
21D2             6412
21D2             6413
21D2             6414
21D2             6415
21D2             6416
21D2             6417
21D2             6418
21D2             6419
21D2             6420
21D2             6421
21D2             6422
21D2             6423
21D2             6424
21D2             6425
21D2             6426
21D2             6427
21D2             6428
21D2             6429
21D2             6430
21D2             6431
21D2             6432
21D2             6433
21D2             6434
21D2             6435
21D2             6436
21D2             6437
21D2             6438
21D2             6439
21D2             6440
21D2             6441
21D2             6442
21D2             6443
21D2             6444
21D2             6445
21D2             6446
21D2             6447
21D2             6448
21D2             6449
21D2             6450
21D2             6451
21D2             6452
21D2             6453
21D2             6454
21D2             6455
21D2             6456
21D2             6457
21D2             6458
21D2             6459
21D2             6460
21D2             6461
21D2             6462
21D2             6463
21D2             6464
21D2             6465
21D2             6466
21D2             6467
21D2             6468
21D2             6469
21D2             6470
21D2             6471
21D2             6472
21D2             6473
21D2             6474
21D2             6475
21D2             6476
21D2             6477
21D2             6478
21D2             6479
21D2             6480
21D2             6481
21D2             6482
21D2             6483
21D2             6484
21D2             6485
21D2             6486
21D2             6487
21D2             6488
21D2             6489
21D2             6490
21D2             6491
21D2             6492
21D2             6493
21D2             6494
21D2             6495
21D2             6496
21D2             6497
21D2             6498
21D2             6499
21D2             6500
21D2             6501
21D2             6502
21D2             6503
21D2             6504
21D2             6505
21D2             6506
21D2             6507
21D2             6508
21D2             6509
21D2             6510
21D2             6511
21D2             6512
21D2             6513
21D2             6514
21D2             6515
21D2             6516
21D2             6517
21D2             6518
21D2             6519
21D2             6520
21D2             6521
21D2             6522
21D2             6523
21D2             6524
21D2             6525
21D2             6526
21D2             6527
21D2             6528
21D2             6529
21D2             6530
21D2             6531
21D2             6532
21D2             6533
21D2             6534
21D2             6535
21D2             6536
21D2             6537
21D2             6538
21D2             6539
21D2             6540
21D2             6541
21D2             6542
21D2             6543
21D2             6544
21D2             6545
21D2             6546
21D2             6547
21D2             6548
21D2             6549
21D2             6550
21D2             6551
21D2             6552
21D2             6553
21D2             6554
21D2             6555
21D2             6556
21D2             6557
21D2             6558
21D2             6559
21D2             6560
21D2             6561
21D2             6562
21D2             6563
21D2             6564
21D2             6565
21D2             6566
21D2             6567
21D2             6568
21D2             6569
21D2             6570
21D2             6571
21D2             6572
21D2             6573
21D2             6574
21D2             6575
21D2             6576
21D2             6577
21D2             6578
21D2             6579
21D2             6580
21D2             6581
21D2             6582
21D2             6583
21D2             6584
21D2             6585
21D2             6586
21D2             6587
21D2             6588
21D2             6589
21D2             6590
21D2             6591
21D2             6592
21D2             6593
21D2             6594
21D2             6595
21D2             6596
21D2             6597
21D2             6598
21D2             6599
21D2             6600
21D2             6601
21D2             6602
21D2             6603
21D2             6604
21D2             6605
21D2             6606
21D2             6607
21D2             6608
21D2             6609
21D2             6610
21D2             6611
21D2             6612
21D2             6613
21D2             6614
21D2             6615
21D2             6616
21D2             6617
21D2             6618
21D2             6619
21D2             6620
21D2             6621
21D2             6622
21D2             6623
21D2             6624
21D2             6625
21D2             6626
21D2             6627
21D2             6628
21D2             6629
21D2             6630
21D2             6631
21D2             6632
21D2             6633
21D2             6634
21D2             6635
21D2             6636
21D2             6637
21D2             6638
21D2             6639
21D2             6640
21D2             6641
21D2             6642
21D2             6643
21D2             6644
21D2             6645
21D2             6646
21D2             6647
21D2             6648
21D2             6649
21D2             6650
21D2             6651
21D2             6652
21D2             6653
21D2             6654
21D2             6655
21D2             6656
21D2             6657
21D2             6658
21D2             6659
21D2             6660
21D2             6661
21D2             6662
21D2             6663
21D2             6664
21D2             6665
21D2             6666
21D2             6667
21D2             6668
21D2             6669
21D2             6670
21D2             6671
21D2             6672
21D2             6673
21D2             6674
21D2             6675
21D2             6676
21D2             6677
21D2             6678
21D2             6679
21D2             6680
21D2             6681
21D2             6682
21D2             6683
21D2             6684
21D2             6685
21D2             6686
21D2             6687
21D2             6688
21D2             6689
21D2             6690
21D2             6691
21D2             6692
21D2             6693
21D2             6694
21D2             6695
21D2             6696
21D2             6697
21D2             6698
21D2             6699
21D2             6700
21D2             6701
21D2             6702
21D2             6703
21D2             6704
21D2             6705
21D2             6706
21D2             6707
21D2             6708
21D2             6709
21D2             6710
21D2             6711
21D2             6712
21D2             6713
21D2             6714
21D2             6715
21D2             6716
21D2             6717
21D2             6718
21D2             6719
21D2             6720
21D2             6721
21D2             6722
21D2             6723
21D2             6724
21D2             6725
21D2             6726
21D2             6727
21D2             6728
21D2             6729
21D2             6730
21D2             6731
21D2             6732
21D2             6733
21D2             6734
21D2             6735
21D2             6736
21D2             6737
21D2             6738
21D2             6739
21D2             6740
21D2             6741
21D2             6742
21D2             6743
21D2             6744
21D2             6745
21D2             6746
21D2             6747
21D2             6748
21D2             6749
21D2             6750
21D2             6751
21D2             6752
21D2             6753
21D2             6754
21D2             6755
21D2             6756
21D2             6757
21D2             6758
21D2             6759
21D2             6760
21D2             6761
21D2             6762
21D2             6763
21D2             6764
21D2             6765
21D2             6766
21D2             6767
21D2             6768
21D2             6769
21D2             6770
21D2             6771
21D2             6772
21D2             6773
21D2             6774
21D2             6775
21D2             6776
21D2             6777
21D2             6778
21D2             6779
21D2             6780
21D2             6781
21D2             6782
21D2             6783
21D2             6784
21D2             6785
21D2             6786
21D2             6787
21D2             6788
21D2             6789
21D2             6790
21D2             6791
21D2             6792
21D2             6793
21D2             6794
21D2             6795
21D2             6796
21D2             6797
21D2             6798
21D2             6799
21D2             6800
21D2             6801
21D2             6802
21D2             6803
21D2             6804
21D2             6805
21D2             6806
21D2             6807
21D2             6808
21D2             6809
21D2             6810
21D2             6811
21D2             6812
21D2             6813
21D2             6814
21D2             6815
21D2             6816
21D2             6817
21D2             6818
21D2             6819
21D2             6820
21D2             6821
21D2             6822
21D2             6823
21D2             6824
21D2             6825
21D2             6826
21D2             6827
21D2             6828
21D2             6829
21D2             6830
21D2             6831
21D2             6832
21D2             6833
21D2             6834
21D2             6835
21D2             6836
21D2             6837
21D2             6838
21D2             6839
21D2             6840
21D2             6841
21D2             6842
21D2             6843
21D2             6844
21D2             6845
21D2             6846
21D2             6847
21D2             6848
21D2             6849
21D2             6850
21D2             6851
21D2             6852
21D2             6853
21D2             6854
21D2             6855
21D2             6856
21D2             6857
21D2             6858
21D2             6859
21D2             6860
21D2             6861
21D2             6862
21D2             6863
21D2             6864
21D2             6865
21D2             6866
21D2             6867
21D2             6868
21D2             6869
21D2             6870
21D2             6871
21D2             6872
21D2             6873
21D2             6874
21D2             6875
21D2             6876
21D2             6877
21D2             6878
21D2             6879
21D2             6880
21D2             6881
21D2             6882
21D2             6883
21D2             6884
21D2             6885
21D2             6886
21D2             6887
21D2             6888
21D2             6889
21D2             6890
21D2             6891
21D2             6892
21D2             6893
21D2             6894
21D2             6895
21D2             6896
21D2             6897
21D2             6898
21D2             6899
21D2             6900
21D2             6901
21D2             6902
21D2             6903
21D2             6904
21D2             6905
21D2             6906
21D2             6907
21D2             6908
21D2             6909
21D2             6910
21D2             6911
21D2             6912
21D2             6913
21D2             6914
21D2             6915
21D2             6916
21D2             6917
21D2             6918
21D2             6919
21D2             6920
21D2             6921
21D2             6922
21D2             6923
21D2             6924
21D2             6925
21D2             6926
21D2             6927
21D2             6928
21D2             6929
21D2             6930
21D2             6931
21D2             6932
21D2             6933
21D2             6934
21D2             6935
21D2             6936
21D2             6937
21D2             6938
21D2             6939
21D2             6940
21D2             6941
21D2             6942
21D2             6943
21D2             6944
21D2             6945
21D2             6946
21D2             6947
21D2             6948
21D2             6949
21D2             6950
21D2             6951
21D2             6952
21D2             6953
21D2             6954
21D2             6955
21D2             6956
21D2             6957
21D2             6958
21D2             6959
21D2             6960
21D2             6961
21D2             6962
21D2             6963
21D2             6964
21D2             6965
21D2             6966
21D2             6967
21D2             6968
21D2             6969
21D2             6970
21D2             6971
21D2             6972
21D2             6973
21D2             6974
21D2             6975
21D2             6976
21D2             6977
21D2             6978
21D2             6979
21D2             6980
21D2             6981
21D2             6982
21D2             6983
21D2             6984
21D2             6985
21D2             6986
21D2             6987
21D2             6988
21D2             6989
21D2             6990
21D2             6991
21D2             6992
21D2             6993
21D2             6994
21D2             6995
21D2             6996
21D2             6997
21D2             6998
21D2             6999
21D2             7000
21D2             7001
21D2             7002
21D2             7003
21D2             7004
21D2             7005
21D2             7006
21D2             7007
21D2             7008
21D2             7009
21D2             7010
21D2             7011
21D2             7012
21D2             7013
21D2             7014
21D2             7015
21D2             7016
21D2             7017
21D2             7018
21D2             7019
21D2             7020
21D2             7021
21D2             7022
21D2             7023
21D2             7024
21D2             7025
21D2             7026
21D2             7027
21D2             7028
21D2             7029
21D2             7030
21D2             7031
21D2             7032
21D2             7033
21D2             7034
21D2             7035
21D2             7036
21D2             7037
21D2             7038
21D2             7039
21D2             7040
21D2             7041
21D2             7042
21D2             7043
21D2             7044
21D2             7045
21D2             7046
21D2             7047
21D2             7048
21D2             7049
21D2             7050
21D2             7051
21D2             7052
21D2             7053
21D2             7054
21D2             7055
21D2             7056
21D2             70
```


0112 00 66 66 00 00 00	66	DB 000H, 066H, 066H, 000H, 000H, 000H ; BT_13
0118 00 00 7F DB DB DB	67	DB 000H, 000H, 07FH, 0DBH, 0DBH, 0DBH, 018H ; TH_14
7B 1B	68	DB 018H, 018H, 018H, 000H, 000H, 000H ; BT_14
0120 00 7C C6 60 38 6C	69	DB 000H, 07CH, 0C6H, 060H, 038H, 05CH, 0C6H, 0C6H ; TH_15
C6 6C	70	DB 06CH, 038H, 00CH, 0C6H, 07CH, 000H
012E 6C 38 00 C6 7C 00	72	DB 000H, 000H, 000H, 000H, 000H, 000H ; TH_16
0134 00 00 00 00 00 00	74	DB 06CH, 038H, 00CH, 0C6H, 07CH, 000H ; BT_16
013C FE FE FE 00 00 00	75	DB 000H, 000H, 018H, 03CH, 07EH, 018H, 018H, 018H ; TH_17
0142 00 18 3C 7E 18	76	DB 07EH, 03CH, 018H, 07EH, 000H, 000H ; BT_17
18 1B	77	DB 000H, 000H, 018H, 03CH, 07EH, 018H, 018H, 018H ; TH_18
014A 18 3C 18 7E 00 00	78	DB 018H, 018H, 018H, 000H, 000H, 000H ; BT_18
0150 00 18 18 3C 7E 18	79	DB 000H, 000H, 018H, 03CH, 07EH, 018H, 018H, 018H ; TH_18
18 1B	81	DB 018H, 018H, 018H, 000H, 000H, 000H ; BT_18
0158 18 18 00 00 00 00	82	DB 018H, 018H, 018H, 000H, 000H, 000H ; BT_18
015E 00 00 18 18 1B	83	DB 000H, 000H, 018H, 018H, 018H, 018H, 018H, 018H ; TH_19
0166 7E 3C 18 00 00 00	84	DB 07EH, 03CH, 018H, 000H, 000H, 000H ; BT_19
016C 00 00 00 18 OC	85	DB 000H, 000H, 000H, 018H, 00CH, 0FEH, 00CH ; TH_1A
FE OC	86	DB 018H, 000H, 000H, 000H, 000H, 000H ; BT_1A
0174 00 00 00 00 00 00	87	DB 000H, 000H, 000H, 030H, 060H, 0FEH, 060H ; TH_1B
017A 00 00 00 30 30 60	88	DB 018H, 000H, 000H, 000H, 000H, 000H ; BT_1B
FF 60	89	DB 000H, 000H, 000H, 000H, 000H, 000H ; TH_1B
0182 30 00 00 00 00 00	90	DB 030H, 000H, 000H, 000H, 000H, 000H ; BT_1B
0184 00 00 00 CO	92	DB 000H, 000H, 000H, 000H, 000H, 000H ; TH_1C
CO DC	93	DB 06CH, 000H, 000H, 000H, 000H, 000H ; BT_1C
0190 00 00 00 00 00 00	94	DB 000H, 000H, 000H, 028H, 05CH, 0FEH, 06CH ; TH_1D
0196 00 00 00 28 6C	95	DB 028H, 000H, 000H, 000H, 000H, 000H ; BT_1D
FE 6C	96	DB 000H, 000H, 010H, 038H, 038H, 07CH, 07CH ; TH_1E
0198 28 00 00 00 00 00	97	DB 038H, 010H, 000H, 000H, 000H, 000H ; BT_1F
01A4 28 00 18 30 38 9B	98	DB 018H, 018H, 000H, 000H, 000H, 000H ; BT_1F
7C 7C	99	DB 000H, 000H, 000H, 000H, 000H, 000H ; TH_20 SP
01AC FE FE 00 00 00 00	100	DB 000H, 000H, 000H, 000H, 000H, 000H ; TH_20 SP
01B2 00 00 FE FF 7C	101	DB 000H, 000H, 000H, 000H, 000H, 000H ; TH_21
7C 3B	102	DB 038H, 010H, 000H, 000H, 000H, 000H ; TH_21
01BA 38 10 00 00 00 00	103	DB 000H, 000H, 000H, 000H, 000H, 000H ; TH_21
	104	DB 000H, 000H, 000H, 000H, 000H, 000H ; TH_21
01C0 00 00 00 00 00 00	105	DB 000H, 000H, 000H, 000H, 000H, 000H ; TH_21
01C8 00 00 00 00 00 00	106	DB 000H, 000H, 000H, 000H, 000H, 000H ; TH_21
01CE 00 18 3C 3C 3C	108	DB 000H, 018H, 03CH, 03CH, 03CH, 018H, 018H ; TH_21
18 1B	109	DB 000H, 018H, 018H, 000H, 000H, 000H ; TH_21
01DE 08 18 00 00 00 00	110	DB 000H, 06CH, 06CH, 000H, 000H, 000H ; TH_21
01DC 08 66 24 00 00	110	DB 000H, 066H, 066H, 024H, 000H, 000H, 000H ; TH_22
00 00	112	DB 000H, 066H, 066H, 066H, 000H, 000H, 000H ; TH_22
01E4 00 00 00 00 00 00	113	DB 000H, 000H, 000H, 000H, 000H, 000H ; BT_22
01EA 00 00 00 6C FE 6C	114	DB 000H, 000H, 06CH, 06CH, 06CH, 06CH, 06CH, 06CH ; TH_23 #
6C 6C	115	DB 06CH, 06CH, 06CH, 06CH, 000H, 000H, 000H ; TH_23 #
01F2 18 18 6C 00 00 00	116	DB 018H, 018H, 0C6H, 0C2H, 0C0H, 07CH, 006H ; TH_24 S
18 1B 7C C6 C2 CO	117	DB 086H, 06CH, 07CH, 018H, 018H, 000H, 000H ; TH_24 S
7C 06	118	DB 000H, 000H, 000H, 000H, 000H, 000H ; TH_25 %
0200 86 C6 7C 18 18 00	119	DB 000H, 000H, 000H, 000H, 000H, 000H ; TH_25 %
0206 00 00 00 C2 C6 10	120	DB 000H, 000H, 000H, 000H, 000H, 000H ; TH_25 %
0C 18	121	DB 000H, 000H, 000H, 000H, 000H, 000H ; TH_25 %
020E 30 6C 00 00 00 00	122	DB 030H, 066H, 066H, 000H, 000H, 000H ; BT_25 %
0214 00 38 6C 3C 3B	123	DB 000H, 038H, 038H, 06CH, 06CH, 038H, 076H, 00CH ; TH_26 &
021C CC 00 00 00 00 00	125	DB 0CCH, 0CCN, 076H, 000N, 000H, 000H ; TH_26 &
0222 00 30 30 30 60 00	126	DB 000H, 030H, 030H, 060H, 060H, 000H, 000H, 000H ; TH_27 *
022A 00 00 00 00 00 00	128	DB 000H, 000H, 000H, 000H, 000H, 000H ; BT_27 *
0230 00 00 18 30 30 00	129	DB 000H, 000H, 000H, 030H, 030H, 030H, 030H, 030H ; TH_28 (
30 30	130	DB 030H, 018H, 000H, 000H, 000H, 000H ; TH_28 (
0238 30 18 00 00 00 00	131	DB 000H, 030H, 018H, 000H, 000H, 000H ; TH_28 (
023E 00 30 18 00 00 00	132	DB 000H, 000H, 000H, 000H, 000H, 000H ; TH_28 (
0246 18 30 00 00 00 00	134	DB 000H, 018H, 030H, 000H, 000H, 000H ; BT_29 *
024C 00 00 00 66 3C	135	DB 000H, 000H, 000H, 000H, 066H, 03CH, 0FFH, 03CH ; TH_2A *
FF 3C	136	DB 066H, 000H, 000H, 000H, 000H, 000H ; TH_2A *
0254 66 00 00 00 00 00	137	DB 000H, 000H, 000H, 000H, 018H, 018H, 07EH, 018H ; TH_2B +
025A 00 00 00 18 18 18	138	DB 018H, 018H, 000H, 000H, 000H, 000H ; TH_2B +
7E 18	139	DB 018H, 018H, 000H, 000H, 000H, 000H ; TH_2B +
0262 18 00 00 00 00 00	140	DB 018H, 018H, 000H, 000H, 000H, 000H ; TH_2B +
0266 00 00 00 00 00 00	141	DB 000H, 000H, 000H, 000H, 000H, 000H ; TH_2C
0270 18 18 18 30 00 00	143	DB 018H, 018H, 018H, 030N, 000H, 000H ; BT_2C
0276 00 00 00 00 00 00	144	DB 000H, 000H, 000H, 000H, 000H, 000H, 000H, 000H ; TH_2D -
027E 00 00 00 00 00 00	145	DB 000H, 000H, 000H, 000H, 000H, 000H, 000H, 000H ; TH_2D -
0284 00 00 00 00 00 00	146	DB 000H, 000H, 000H, 000H, 000H, 000H, 000H, 000H ; TH_2E *
00 00	148	DB 000H, 000H, 000H, 000H, 000H, 000H, 000H, 000H ; TH_2E *
028C 00 18 18 00 00 00	149	DB 000H, 018H, 018H, 000H, 000H, 000H ; BT_2E
0292 00 00 00 06 18 150	150	DB 000H, 000H, 002H, 006H, 00CH, 018H, 030H, 060H ; TH_2F /
029A 00 80 00 00 00 00	152	DB 000H, 080W, 000H, 000H, 000H, 000H ; BT_2F /
	153	DB 000H, 000H, 000H, 000H, 000H, 000H ; TH_30 0
02A0 00 00 7C C6 CE DE	154	DB 000H, 000H, 07CH, 0C6H, 0CEH, 0DEH, 0F6H, 0E6H ; TH_30 0
02A8 00 00 00 00 00 00	155	DB 000H, 000H, 000H, 000H, 000H, 000H ; TH_30 0
02AE 00 00 18 38 78 18	157	DB 000H, 018H, 038H, 078H, 018H, 018H, 018H ; TH_31 1
18 18	158	DB 018H, 018H, 07EH, 000H, 000H, 000H ; BT_31 1
02B6 18 7E 00 00 00 00	159	DB 000H, 000H, 07CH, 0C6H, 006H, 00CH, 018H, 030H ; TH_32 2
02B8 00 00 00 00 00 00	160	DB 060H, 000H, 000H, 000H, 000H, 000H ; TH_32 2
18 30	161	DB 000H, 000H, 000H, 000H, 000H, 000H ; TH_33 3
02C4 60 C6 FE 00 00 00	162	DB 000H, 000H, 000H, 000H, 000H, 000H ; TH_33 3
02CA 00 00 7C 06 06 06	163	DB 000H, 000H, 07CH, 0C6H, 006H, 00CH, 018H, 018H ; TH_33 3
30 30	164	DB 000H, 000H, 000H, 000H, 000H, 000H ; TH_34 4
02D2 00 00 00 00 00 00	165	DB 000H, 000H, 000H, 000H, 000H, 000H ; TH_34 4
02D8 00 00 00 00 00 00	166	DB 000H, 000H, 000H, 000H, 000H, 000H ; TH_34 4
CC FE	167	DB 000H, 000H, 000H, 000H, 000H, 000H ; TH_34 4
02E0 00 00 00 00 00 00	168	DB 000H, 000H, 000H, 000H, 000H, 000H ; TH_34 4
02E6 00 00 00 00 00 00	169	DB 000H, 000H, 000H, 000H, 000H, 000H ; TH_34 4
FC 06	170	DB 000H, 000H, 000H, 000H, 000H, 000H ; TH_34 4
02E6 00 C6 7C 00 00 00	171	DB 000H, 000H, 000H, 000H, 000H, 000H ; TH_35 5
02F4 00 30 60 00 CO	172	DB 000H, 000H, 038H, 060H, 00CH, 00CH, 0FCH, 0C6H ; TH_35 5
02FC C6 C6 7C 00 00 00	174	DB 000H, 000H, 000H, 000H, 000H, 000H ; TH_36 6
0302 00 00 FE C6 00 OC	175	DB 000H, 000H, 000H, 000H, 000H, 000H, 000H, 000H ; TH_37 7
18 30	176	DB 030H, 030H, 030H, 000H, 000H, 000H ; BT_37 7
030A 30 30 00 00 00 00	177	DB 000H, 000H, 07CH, 0C6H, 0C6H, 0C6H, 07CH, 0C6H ; BT_38 8
0310 00 00 00 00 00 00	178	DB 000H, 000H, 000H, 000H, 000H, 000H ; TH_38 8
7C C6	179	DB 000H, 000H, 000H, 000H, 000H, 000H ; TH_38 8
0318 C6 C6 7C 00 00 00	180	DB 000H, 000H, 000H, 000H, 000H, 000H ; TH_38 8
0316 00 00 00 C6 C6 C6	181	DB 000H, 000H, 000H, 000H, 000H, 000H ; TH_39 9
FE FE	182	DB 000H, 000H, 000H, 000H, 000H, 000H ; TH_39 9
0326 00 00 00 00 00 00	183	DB 000H, 000H, 07CH, 000H, 000H, 000H ; TH_39 9
032C 00 00 00 18 18 00	184	DB 000H, 000H, 000H, 000H, 000H, 000H ; TH_39 9
	185	DB 000H, 000H, 000H, 000H, 000H, 000H ; TH_39 9
0334 18 18 00 00 00 00	186	DB 018H, 018H, 000H, 000H, 000H, 000H ; TH_3A :
033A 00 00 00 18 18 00	187	DB 000H, 000H, 000H, 000H, 000H, 000H, 000H, 000H ; TH_3B :
	188	DB 000H, 000H, 000H, 000H, 000H, 000H, 000H, 000H ; TH_3B :
0342 18 18 30 00 00 00	189	DB 018H, 018H, 030H, 000H, 000H, 000H ; BT_3B :
0348 00 00 06 18 30	190	DB 000H, 000H, 006H, 00CH, 018H, 030H, 060H, 030H ; TH_3C <
60 30	191	

0350	18	06	06	00	00	00	192	DB	018H, 00CH, 006H, 000H, 000H, 000H ; BT_3C <
0356	00	00	00	00	00	00	193	DB	000H, 000H, 000H, 000H, 000H, 07EH, 000H, 000H ; TH_3D =
035E	00	00	00	00	00	00	194	DB	07EH, 000H, 000H, 000H, 000H, 000H ; BT_3D =
0364	00	00	60	30	18	0C	195	DB	000H, 000H, 060H, 030H, 018H, 00CH, 006H, 00CH ; TH_3E >
036C	18	30	60	00	00	00	196	DB	018H, 030H, 060H, 000H, 000H, 000H ; BT_3E >
0372	00	00	7C	C6	0C	0C	197	DB	000H, 000H, 07CH, 0C6H, 0C6H, 00CH, 018H, 018H ; TH_3F =
037A	18	18	00	00	00	00	198	DB	000H, 018H, 018H, 000H, 000H, 000H ; BT_3F ?
0380	00	00	7C	C6	0C	DE	199	DB	000H, 000H, 07CH, 0C6H, 0C6H, 0DEH, 0DEH, 0DEH ; TH_40 =
0388	00	00	7C	C6	0C	0C	200	DB	000H, 000H, 07CH, 0C6H, 0C6H, 00EH, 00EH, 00EH ; BT_40 =
038E	00	00	10	38	6C	C6	201	DB	000H, 000H, 010H, 038H, 06CH, 0C6H, 0C6H, 0FEH ; TH_41 A
0396	C6	C6	C6	00	00	00	202	DB	0C6H, 0C6H, 0C6H, 000H, 000H, 000H ; BT_41 B
039C	00	00	7C	66	66	66	203	DB	000H, 000H, 07CH, 066H, 066H, 066H, 07CH, 066H ; TH_42 B
03A4	66	66	FC	00	00	00	204	DB	066H, 066H, 0FCH, 000H, 000H, 000H ; BT_42 B
03AA	00	00	30	66	62	C0	205	DB	000H, 000H, 030H, 066H, 0C2H, 0C0H, 0C0H, 0C0H ; TH_43 C
03B2	00	00	66	3C	00	00	206	DB	0C2H, 066H, 03CH, 000H, 000H, 000H ; BT_43 C
03B8	00	00	F8	6C	66	66	207	DB	000H, 000H, 0F8H, 06CH, 066H, 066H, 066H, 066H ; TH_44 D
03C0	66	66	FC	00	00	00	208	DB	066H, 06CH, 0F8H, 000H, 000H, 000H ; BT_44 E
03C6	00	00	7E	66	62	68	209	DB	000H, 000H, 07EH, 066H, 062H, 068H, 078H, 068H ; TH_45 E
03CE	62	66	FE	00	00	00	210	DB	062H, 066H, 0FEH, 000H, 000H, 000H ; BT_45 E
03DE	00	00	FE	66	62	68	211	DB	000H, 000H, 0FEH, 066H, 062H, 068H, 078H, 068H ; TH_46 F
03DC	60	60	FO	00	00	00	212	DB	060H, 060H, 0FOH, 000H, 000H, 000H ; BT_46 F
03E2	00	00	30	66	C2	C0	213	DB	000H, 000H, 030H, 066H, 0C2H, 0C0H, 0C0H, 0C0H ; TH_47 G
03EA	C0	DE	00	00	00	00	214	DB	0C6H, 066H, 03AH, 000H, 000H, 000H ; BT_47 G
03F0	00	00	3A	00	00	00	215	DB	000H, 000H, 036H, 066H, 0C6H, 0C6H, 0FEH, 0C6H ; TH_48 H
03F8	C6	C6	C6	00	00	00	216	DB	0C6H, 066H, 06CH, 066H, 000H, 000H, 000H ; BT_48 H
03FE	00	00	3C	18	18	18	217	DB	000H, 000H, 03CH, 018H, 018H, 018H, 018H, 018H ; TH_49 I
0406	18	18	3C	00	00	00	218	DB	018H, 018H, 03CH, 000H, 000H, 000H ; BT_49 I
0406	00	00	1E	0C	0C	0C	219	DB	000H, 000H, 01EH, 00CH, 00CH, 00CH, 00CH, 00CH ; TH_4A J
0414	CC	OC	00	00	00	00	220	DB	0CCH, 0CCH, 078H, 000H, 000H, 000H ; BT_4A J
041A	00	00	66	66	6C	C6	221	DB	000H, 000H, 06EH, 066H, 0C6H, 0C6H, 078H, 06CH ; TH_4B K
0422	6C	66	E6	00	00	00	222	DB	06CH, 066H, 06EH, 000H, 000H, 000H ; BT_4B K
0428	00	00	F0	60	60	60	223	DB	000H, 000H, 0FOH, 060H, 060H, 060H, 060H, 060H ; TH_4C L
0430	62	66	FE	00	00	00	224	DB	062H, 066H, 0FEH, 000H, 000H, 000H ; BT_4C L
0436	00	00	66	FE	FE	FE	225	DB	000H, 000H, 06EH, 0FEH, 0FEH, 06EH, 0C6H ; TH_4D M
043E	00	00	C6	00	00	00	226	DB	0C6H, 0C6H, 0C6H, 000H, 000H, 000H ; BT_4D M
0444	00	00	C6	E6	FE	FE	227	DB	000H, 000H, 0C6H, 0E6H, 0F6H, 0FEH, 0DEH, 0C6H ; TH_4E N
044C	C6	C6	S6	00	00	00	228	DB	0C6H, 0C6H, 0C6H, 000H, 000H, 000H ; BT_4E N
0452	00	00	38	6C	C8	C8	229	DB	000H, 000H, 038H, 06CH, 0C6H, 0C6H, 0C6H, 0C6H ; TH_4F O
045A	C6	GC	38	00	00	00	230	DB	0C6H, 0C6H, 038H, 000H, 000H, 000H ; BT_4F O
0460	00	00	FC	66	66	66	231	DB	000H, 000H, 0FC, 066H, 066H, 066H, 07CH, 060H ; TH_50 P
0468	60	60	FO	00	00	00	232	DB	060H, 060H, 0FOH, 000H, 000H, 000H ; BT_50 P
046E	00	00	7C	C6	C6	C6	233	DB	000H, 000H, 07CH, 0C6H, 0C6H, 0C6H, 0C6H, 0D6H ; TH_51 Q
0476	00	00	OC	0E	00	00	234	DB	00EH, 07CH, 0C0H, 0E0H, 000H, 000H, 000H ; BT_51 Q
047C	00	00	FC	66	66	66	235	DB	000H, 000H, 0FC, 066H, 066H, 066H, 07CH, 06CH ; TH_52 R
0484	66	66	E6	00	00	00	236	DB	066H, 066H, 06EH, 000H, 000H, 000H ; BT_52 R
048A	00	00	7C	C6	60	60	237	DB	000H, 000H, 07CH, 0C6H, 0C6H, 060H, 038H, 00CH ; TH_53 S
0492	C6	7C	7C	00	00	00	238	DB	0C6H, 0C6H, 07CH, 000H, 000H, 000H ; BT_53 S
0498	00	00	7E	7C	5A	18	239	DB	000H, 000H, 07EH, 07CH, 05AH, 018H, 018H, 018H ; TH_54 T
04A0	00	00	3C	00	00	00	240	DB	018H, 018H, 03CH, 000H, 000H, 000H ; BT_54 T
04A6	00	00	C6	C6	C6	C6	241	DB	000H, 000H, 0C6H, 0C6H, 0C6H, 0C6H, 0C6H, 0C6H ; TH_55 U
04AE	C6	C6	C6	7C	00	00	242	DB	0C6H, 0C6H, 0C6H, 07CH, 000H, 000H, 000H ; BT_55 U
04B4	00	00	C6	C6	C6	C6	243	DB	000H, 000H, 0C6H, 0C6H, 0C6H, 0C6H, 0C6H, 0C6H ; TH_56 V
04BC	38	10	00	00	00	00	244	DB	06CH, 038H, 010H, 000H, 000H, 000H ; BT_56 V
04C2	00	00	66	66	C6	C6	245	DB	000H, 000H, 06CH, 06CH, 06CH, 06CH, 0D6H, 0D6H ; TH_57 W
04CA	00	00	6C	00	00	00	246	DB	0FEH, 07CH, 06CH, 000H, 000H, 000H ; BT_57 W
04D0	00	00	C6	C6	38	38	247	DB	000H, 000H, 0C6H, 0C6H, 0C6H, 038H, 038H, 038H ; TH_58 X
04D8	6C	C6	C6	00	00	00	248	DB	06CH, 0C6H, 0C6H, 000H, 000H, 000H ; BT_58 X
04DE	00	00	66	66	66	66	249	DB	000H, 000H, 06EH, 066H, 066H, 066H, 03CH, 018H ; TH_59 Y
04E6	18	18	3C	00	00	00	250	DB	018H, 018H, 03CH, 000H, 000H, 000H ; BT_59 Y
04EC	00	00	FE	C6	8C	8C	251	DB	000H, 000H, 0FEH, 0C6H, 0C6H, 030H, 030H, 060H ; TH_5A Z
04F4	00	00	32	FE	00	00	252	DB	0C2H, 0C6H, 0FEH, 000H, 000H, 000H ; BT_5A Z
04FA	00	00	3C	30	30	30	253	DB	000H, 000H, 03CH, 030H, 030H, 030H, 030H, 030H ; TH_5B [
0502	30	30	3C	00	00	00	254	DB	030H, 030H, 03CH, 000H, 000H, 000H ; BT_5B [
0508	00	00	80	80	EO	70	255	DB	000H, 000H, 080H, 080H, 0C0H, 0E0H, 070H, 038H, 01CH ; TH_5C \
0510	0E	02	02	00	00	00	256	DB	00EH, 002H, 002H, 000H, 000H, 000H ; BT_5C \
0516	00	00	3C	0C	0C	0C	257	DB	000H, 000H, 03CH, 00CH, 00CH, 00CH, 00CH, 00CH ; TH_5D]
051E	00	00	3C	3C	00	00	258	DB	00CH, 00CH, 03CH, 000N, 000H, 000H ; BT_5D]
0524	10	38	6C	C6	00	00	259	DB	010H, 038H, 06CH, 0C6H, 000H, 000H, 000H, 000H ; TH_5E
052C	00	00	00	00	00	00	260	DB	000H, 000H, 000H, 000H, 000H, 000H, 000H, 000H ; BT_5E
0532	00	00	00	00	00	00	261	DB	000H, 000H, 000H, 000H, 000H, 000H, 000H, 000H ; TH_5F -
053A	00	00	00	FF	FF	00	262	DB	000H, 000H, 000H, 0FFH, 000H ; BT_5F -
0540	30	30	18	00	00	00	263	DB	030H, 030H, 018H, 000H, 000H, 000H, 000H, 000H ; TH_60 !
0548	00	00	00	00	00	00	264	DB	000H, 000H, 000H, 000H, 000H, 000H, 000H, 000H ; TH_60 !
054E	00	00	00	00	00	78	265	DB	000H, 000H, 000H, 000H, 000H, 000H, 078H, 00CH, 07CH ; TH_61 LOWER_CASE A
0556	00	00	7C	76	00	00	266	DB	0CCH, 0CCH, 076H, 000H, 000H, 000H ; BT_61 LOWER_CASE A
055C	00	00	EO	60	60	78	267	DB	000H, 000H, 0E0H, 060H, 060H, 078H, 06CH, 066H ; TH_62 B
0564	66	66	7C	00	00	00	268	DB	066H, 066H, 07CH, 000H, 000H, 000H ; BT_62 L.C. B
056A	00	00	66	00	00	7C	269	DB	000H, 000H, 066H, 000H, 07CH, 06CH, 0C6H, 0C6H ; TH_63 L.C. C
0572	00	00	C6	7C	00	00	270	DB	0C6H, 0C6H, 07CH, 000H, 000H, 000H ; BT_63 L.C. C
0578	00	00	00	1C	00	00	271	DB	000H, 000H, 00CH, 00CH, 00CH, 03CH, 06CH, 0C0CH ; TH_64 L.C. D
0580	00	00	00	00	00	7C	272	DB	0CCH, 0CCH, 076H, 000H, 000H, 000H ; BT_64 L.C. D
0586	00	00	00	00	00	7C	273	DB	000H, 000H, 000H, 000H, 000H, 000H, 07CH, 0C6H, 0FEH ; BT_65 L.C. E

058E	CD	C6	7C	00	00	00	318	DB	0C00, 0C6H, 07CH, 000H, 000H, 000H, 000H, 000H ; BT_65 L.C. E	
0594	CD	F0	60	38	6C	64	60	DB	000H, 000H, 038H, 06CH, 064H, 060H, 0F0H, 060H ; TH_66 L.C. F	
059C	6D	60	F0	00	00	00	321	DB	060H, 060H, 0F0H, 000H, 000H, 000H ; BT_66 L.C. F	
05A2	00	00	60	00	00	00	322	DB	000H, 000H, 000H, 000H, 000H, 076H, 0CCH, 0CCH ; TH_67 L.C. G	
05A4	CC	CC	00	00	00	00	323	DB	0CCH, 07CH, 000H, 000H, 000H, 000H ; BT_67 L.C. G	
05B0	00	00	00	00	00	00	324	DB	000H, 000H, 000H, 000H, 000H, 06CH, 076H, 066H ; TH_68 L.C. H	
05B6	76	66	00	00	00	00	325	DB	066H, 066H, 0E6H, 000H, 000H, 000H ; BT_68 L.C. H	
05B8	66	66	E6	00	00	00	327	DB	000H, 000H, 018H, 018H, 000H, 038H, 018H, 018H ; TH_69 L.C. I	
05B9	00	00	18	18	18	38	328	DB	018H, 018H, 03CH, 000H, 000H, 000H ; BT_69 L.C. I	
05C6	18	18	3C	00	00	00	329	DB	000H, 000H, 000H, 000H, 000H, 000H, 000H ; BT_69 L.C. I	
05CC	00	00	00	00	00	00	331	DB	000H, 000H, 000H, 000H, 000H, 000H, 000H ; BT_69 L.C. I	
05D4	06	06	00	00	00	00	332	DB	006H, 006H, 066H, 066H, 03CH, 000H ; BT_6A L.C. J	
05DA	00	00	00	00	60	60	334	DB	000H, 000H, 0E0H, 060H, 060H, 066H, 06CH, 078H ; TH_6D L.C. K	
05E2	6C	78	00	00	00	00	335	DB	06CH, 066H, 0E6H, 000H, 000H, 000H ; BT_6A L.C. K	
05E8	00	00	38	18	18	18	336	DB	000H, 000H, 018H, 018H, 018H, 018H, 018H, 018H ; TH_6D L.C. K	
05F0	18	18	3C	00	00	00	339	DB	018H, 018H, 03CH, 000H, 000H, 000H ; BT_6C L.C. L	
05F6	00	00	00	00	00	00	340	DB	000H, 000H, 000H, 000H, 000H, 000H, 000H ; TH_6D L.C. M	
05FE	FE	D6	00	00	00	00	341	DB	0D6H, 0D6H, 0C6H, 000H, 000H, 000H ; BT_6D L.C. M	
0604	00	00	C6	00	00	00	342	DB	000H, 000H, 000H, 000H, 000H, 000H, 000H ; TH_6D L.C. M	
060C	66	66	66	00	00	00	345	DB	066H, 066H, 066H, 000H, 000H, 000H ; BT_6D L.C. M	
0612	00	00	00	00	00	00	346	DB	000H, 000H, 000H, 000H, 000H, 000H, 000H ; TH_6D L.C. M	
061A	C5	C6	00	00	00	00	348	DB	0C6H, 0C6H, 07CH, 000H, 000H, 000H ; BT_6F L.C. O	
0620	00	00	00	00	00	00	350	DB	000H, 000H, 000H, 000H, 000H, 000H, 000H ; TH_70 L.C. P	
0628	66	76	60	00	00	00	351	DB	066H, 07CH, 060H, 060H, 0F0H, 000H ; BT_70 L.C. P	
062E	00	00	00	00	00	76	353	DB	000H, 000H, 000H, 000H, 000H, 076H, 0CCH, 0CCH ; TH_71 L.C. Q	
0633	CC	CC	7C	00	00	1E	355	DB	0CCH, 07CH, 000H, 000H, 01EH, 000H ; BT_71 L.C. Q	
063C	00	00	00	00	00	DC	357	DB	000H, 000H, 000H, 000H, 000H, 000H, 000H ; TH_72 L.C. R	
063E	CC	7C	00	00	00	1E	359	DB	0CCH, 07CH, 000H, 000H, 01EH, 000H ; BT_72 L.C. R	
0644	60	60	F0	00	00	00	358	DB	060H, 060H, 0F0H, 000H, 000H, 000H ; BT_72 L.C. R	
064A	00	00	00	00	00	7C	359	DB	000H, 000H, 000H, 000H, 000H, 000H, 000H ; TH_73 L.C. S	
0652	C5	C6	7C	00	00	00	361	DB	01CH, 0C6H, 07CH, 000H, 000H, 000H ; BT_73 L.C. S	
0658	00	00	10	30	30	FC	362	DB	000H, 000H, 010H, 030H, 030H, 0FCH, 030H, 030H ; TH_74 L.C. T	
0660	30	30	3C	00	00	00	363	DB	030H, 036H, 01CH, 000H, 000H, 000H ; BT_74 L.C. T	
0664	00	00	00	00	00	CC	364	DB	000H, 000H, 000H, 000H, 000H, 000H, 000H ; TH_75 L.C. U	
0666	CC	CC	76	00	00	00	366	DB	0CCH, 0CCH, 076H, 000H, 000H, 000H ; BT_75 L.C. U	
066E	CC	CC	76	00	00	00	367	DB	0CCH, 0CCH, 076H, 000H, 000H, 000H ; BT_75 L.C. U	
0674	00	00	00	00	00	66	368	DB	000H, 000H, 000H, 000H, 000H, 066H, 066H, 066H ; TH_76 L.C. V	
067C	66	3C	18	00	00	00	370	DB	066H, 03CH, 018H, 000H, 000H, 000H ; BT_76 L.C. V	
0682	00	00	00	00	00	C6	371	DB	000H, 000H, 000H, 000H, 000H, 000H, 000H ; TH_77 L.C. W	
0684	C5	D6	00	00	00	00	372	DB	065H, 0F6H, 06CH, 000H, 000H, 000H ; BT_77 L.C. W	
0690	00	00	00	00	00	C6	374	DB	000H, 000H, 000H, 000H, 000H, 0C6H, 06CH, 038H ; TH_78 L.C. X	
0692	60	38	00	00	00	38	375	DB	038H, 0C6H, 06CH, 000H, 000H, 000H ; BT_78 L.C. X	
0698	38	6C	00	00	00	00	376	DB	000H, 000H, 000H, 000H, 000H, 0C6H, 0C6H, 0C6H ; TH_79 L.C. Y	
069E	00	00	00	00	00	C6	377	DB	000H, 000H, 000H, 000H, 000H, 000H, 000H ; TH_79 L.C. Y	
06A6	C6	7E	06	00	00	F8	379	DB	0C6H, 07EH, 000H, 000H, 0F8H, 000H ; BT_79 L.C. Z	
06AC	00	00	00	00	00	FE	380	DB	000H, 000H, 000H, 000H, 000H, 000H, 000H ; TH_7A L.C. Z	
06B4	CC	18	00	00	00	00	381	DB	030H, 046H, 0F6H, 000H, 000H, 000H ; BT_7A L.C. Z	
06B8	00	00	00	00	00	18	382	DB	000H, 000H, 000H, 000H, 000H, 018H, 018H, 018H ; TH_7B L.BRACK	
06C2	18	18	0E	00	00	00	385	DB	018H, 018H, 0E0H, 000H, 000H, 000H ; BT_7B L.BRACK	
06C6	00	00	18	18	18	18	386	DB	000H, 000H, 018H, 018H, 018H, 018H, 018H, 018H ; TH_7C I	
06D0	18	18	18	00	00	00	388	DB	018H, 018H, 018H, 000H, 000H, 000H ; BT_7C I	
06D6	00	00	70	18	18	18	389	DB	000H, 000H, 070H, 018H, 018H, 018H, 00EH, 018H ; TH_7D R TILDE	
06E1	00	00	70	00	00	00	390	DB	018H, 018H, 070H, 000H, 000H, 000H ; BT_7D R TILDE	
06E4	00	00	70	00	00	CC	393	DB	000H, 000H, 070H, 0CCH, 000H, 000H, 000H, 000H, 000H ; TH_7E TILDE	
06E6	00	00	00	00	00	00	394	DB	000H, 000H, 000H, 000H, 000H, 000H, 000H, 000H ; TH_7F DELTA	
06F2	00	00	00	00	00	10	38	395	DB	0C6H, 0F6H, 000H, 000H, 000H, 000H ; BT_7F DELTA
06FA	C6	C6	00	00	00	00	397	DB	000H, 000H, 000H, 000H, 000H, 000H, 000H, 000H ; TH_80 R	
0700	00	00	3C	66	CC	00	398	DB	0CCH, 0C6H, 03CH, 066H, 0C2H, 0C0H, 0C0H, 0C2H ; TH_80 R	
0708	66	3C	0C	06	7C	00	401	DB	066H, 03CH, 00CH, 000H, 076H, 000H ; BT_80 R	
070E	00	00	00	00	00	CC	402	DB	000H, 000H, 0CCH, 0CCH, 000H, 0CCH, 0CCH, 0CCH ; TH_81 R	
0716	CC	7C	76	00	00	00	403	DB	0CCH, 0C6H, 07CH, 000H, 000H, 000H ; BT_81 R	
071C	00	00	18	30	30	7C	404	DB	000H, 000H, 018H, 030H, 000H, 07CH, 0C6H, 0F6H ; TH_82 R	
0724	C6	C6	7C	00	00	00	407	DB	0C0H, 0C6H, 07CH, 000H, 000H, 000H ; BT_82 R	
072A	00	10	38	6C	00	78	408	DB	000H, 010H, 038H, 06CH, 000H, 078H, 0CCH, 07CH ; TH_83 R	
0732	CC	CC	7C	76	00	00	410	DB	0CCH, 0C6H, 076H, 000H, 000H, 000H ; BT_83 R	
0738	00	00	00	00	00	CC	411	DB	000H, 000H, 0CCH, 0CCH, 000H, 078H, 0CCH, 07CH ; TH_84 R	
0740	CC	7C	76	00	00	00	412	DB	0CCH, 0C6H, 07CH, 000H, 000H, 000H ; BT_84 R	
0746	00	00	30	18	00	78	413	DB	000H, 000H, 030H, 018H, 000H, 078H, 0CCH, 07CH ; TH_85 R	
074E	CC	7C	76	00	00	00	416	DB	0CCH, 0C6H, 076H, 000H, 000H, 000H ; BT_85 R	
0754	00	38	6C	38	00	78	417	DB	000H, 038H, 06CH, 038H, 000H, 078H, 0CCH, 07CH ; TH_86 R	
075C	CC	7C	76	00	00	00	419	DB	0CCH, 0C6H, 076H, 000H, 000H, 000H ; BT_86 R	
0762	00	00	00	00	36	66	420	DB	000H, 000H, 000H, 000H, 03CH, 066H, 060H, 066H ; TH_87 R	
0764	60	66	00	00	00	00	421	DB	03CH, 006H, 03CH, 000H, 000H, 000H ; BT_87 R	
076A	CC	7C	00	00	00	3C	422	DB	03CH, 006H, 03CH, 000H, 000H, 000H ; BT_88 R	
0770	00	10	38	6C	00	7C	423	DB	000H, 010H, 038H, 06CH, 000H, 07CH, 0C6H, 0F6H ; TH_88 R	
0774	C6	FE	00	00	00	00	424	DB	0C0H, 0C6H, 07CH, 000H, 000H, 000H ; BT_88 R	
0778	00	00	3C	66	00	38	425	DB	000H, 000H, 03CH, 066H, 000H, 038H, 018H, 018H ; TH_88 R	
077E	00	00	00	00	00	CC	426	DB	0C0H, 0C6H, 07CH, 000H, 000H, 000H ; BT_88 R	
0786	CC	7C	00	00	00	00	428	DB	0C0H, 0C6H, 07CH, 000H, 000H, 000H ; BT_88 R	
078C	00	60	30	18	00	7C	429	DB	000H, 060H, 030H, 018H, 000H, 07CH, 0C6H, 0F6H ; TH_88 R	
0794	CC	7C	76	00	00	00	430	DB	0C0H, 0C6H, 07CH, 000H, 000H, 000H ; BT_88 R	
079A	CC	7C	76	00	00	00	431	DB	0C0H, 0C6H, 07CH, 000H, 000H, 000H ; BT_88 R	
079E	00	00	00	00	66	00	38	432	DB	000H, 000H, 066H, 000H, 038H, 018H, 018H, 018H ; TH_88 R
07A2	18	18	3C	00	00	00	434	DB	018H, 018H, 03CH, 000H, 000H, 000H ; BT_88 R	
07A8	00	00	3C	66	00	38	435	DB	000H, 000H, 03CH, 066H, 000H, 038H, 018H, 018H ; TH_88 R	
07B0	18	18	3C	00	00	00	437	DB	018H, 018H, 03CH, 000H, 000H, 000H ; BT_88 R	
07B6	00	00	60	30	18	00	438	DB	000H, 060H, 030H, 018H, 000H, 038H, 018H, 018H ; TH_88 R	
07B8	18	18	3C	00	00	00	439	DB	018H, 018H, 03CH, 000H, 000H, 000H ; BT_88 R	
07BE	00	00	3C	00	00	00	440	DB	000H, 000H, 03CH, 000H, 000H, 000H ; BT_88 R	
07C4	00	00	C6	00	10	38	441	DB	000H, 000H, 000H, 000H, 000H, 000H, 000H, 000H ; TH_88 R	
07CC	C6	C6	00	00	00	00	442	DB	0F6H, 0C6H, 0C6H, 000H, 000H, 000H ; BT_88 R	
07CC	FE	C6	C6	00	00	00	443	DB	000H, 000H, 000H, 000H, 000H, 000H, 000H, 000H ; BT_88 R	

0702	36	38	30	00	38	6C	444
0704	CE	C6	00	00	00	00	445
0704A	FE	C6	00	00	00	00	446
07E0	18	30	60	00	FE	66	448
07E0A	60	7C	00	00	00	00	449
07E0B	68	66	FE	00	00	00	450
07E0C	68	72	00	00	00	00	452
07F6	08	68	00	00	00	00	453
07FC	00	3E	6C	00	CC	00	454
0804	CC	CC	CE	00	00	00	455
0804A	00	38	3C	00	00	00	456
0804B	00	38	3C	00	00	00	458
0812	CE	C6	7C	00	00	00	459
0818	00	00	C6	00	00	00	460
0820	CE	C6	7C	00	00	00	462
0826	00	60	30	18	00	00	463
082E	CE	C6	00	00	00	00	464
0834	CE	C6	7C	00	00	00	465
0834A	CC	CC	7C	00	00	00	467
083C	CC	CC	76	00	00	00	468
0842	00	60	30	18	00	00	469
0844	CC	CC	76	00	00	00	471
0850	00	00	C6	00	C6	00	472
0858	CE	C6	00	00	00	00	473
0858A	00	60	38	00	78	00	475
0860	00	00	C6	00	00	00	476
0866	CE	C6	38	00	00	00	477
0866C	00	00	C6	00	00	00	478
0874	CE	C6	7C	00	00	00	480
0874A	08	18	18	3C	66	60	481
0882	08	18	18	00	00	00	482
0888	00	00	C6	00	64	00	483
0890	00	60	60	00	00	00	485
0896	00	00	66	66	30	18	486
0896A	00	00	66	66	30	18	487
0898	7E	18	18	00	00	00	489
08A4	00	F8	CC	CC	FE	C4	490
08AC	CC	DE	C6	00	00	00	491
08B2	00	00	18	18	18	18	493
08B4	00	00	18	18	18	18	494
08B8	18	18	18	00	00	00	495
08C0	00	18	30	00	00	00	496
08C8	00	00	7C	00	00	00	498
08C8E	00	18	18	30	00	00	500
08D6	00	18	3C	00	00	00	502
08DC	00	18	30	60	00	00	503
08E4	CE	C6	00	00	00	00	504
08E4A	CE	C6	7C	00	00	00	505
08E4C	00	30	00	00	00	00	507
08F2	CE	CC	76	00	00	00	508
08F8	00	00	76	00	00	00	509
0900	66	66	66	00	00	00	511
0900A	76	00	00	00	00	00	512
0904	FE	DE	00	00	00	00	513
0914	00	56	C6	00	00	00	515
0914C	00	00	66	3C	3E	00	516
091C	00	00	00	00	00	00	517
0922	00	38	6C	38	00	00	518
0924	00	00	00	00	00	00	519
0924A	00	00	00	00	00	00	520
0930	00	00	30	30	00	00	521
0938	CE	C6	7C	00	00	00	522
0940	00	00	00	00	00	00	524
0946	CE	FE	00	00	00	00	525
0946C	00	00	00	00	00	00	526
0954	06	06	00	00	00	00	527
0954A	00	00	00	00	00	00	528
0954C	06	06	00	00	00	00	529
0954E	00	00	00	00	00	00	530
0962	DC	00	0C	18	3E	00	531
0968	00	00	00	00	00	00	532
0970	30	66	00	00	00	00	534
0970C	00	00	9E	3E	00	00	535
0976	00	00	18	18	00	00	536
0978	00	00	00	00	00	00	537
0978C	00	00	3C	18	00	00	538
0984	00	00	00	00	00	36	6C
0984A	00	00	00	00	00	00	539
0984C	00	00	00	00	00	00	540
098C	00	00	00	00	00	00	541
0992	00	00	00	00	00	00	542
0994	00	00	00	00	00	00	543
0994A	00	00	00	00	00	00	544
0994C	00	00	00	00	00	00	545
0994E	00	00	00	00	00	00	546
0994F	00	00	00	00	00	00	547
0994G	11	44	11	44	11	44	548
0994H	11	44	11	44	11	44	549
0994I	55	AA	55	AA	55	AA	550
0994J	55	AA	55	AA	55	AA	551
0994K	77	DD	77	DD	77	DD	552
0994L	77	DD	77	DD	77	DD	553
0994M	77	DD	77	DD	77	DD	554
0994N	18	18	18	18	18	18	555
0994O	18	18	18	18	18	18	556
0994P	18	18	18	18	18	18	557
0994Q	18	18	18	18	18	18	558
0994R	18	18	18	18	18	18	559
0994S	18	18	18	18	18	18	560
0994T	18	18	18	18	18	18	561
0994U	18	18	18	18	18	18	562
0994V	18	18	18	18	18	18	563
0994W	36	36	36	36	36	36	565
0994X	36	36	36	36	36	36	566
0994Y	00	00	00	00	00	00	567
0994Z	00	00	00	00	00	00	568

00A0	00 00 00 00 00 F8	570	DB	000H, 000H, 000H, 000H, 000H, 0F8H, 018H, 0F8H	; TH_B8
00A18	18 18 18 18 18 18	571	DB	018H, 018H, 018H, 018H, 018H, 018H	; BT_B8
00A1E	36 36 36 36 36 36	572	DB	036H, 036H, 036H, 036H, 036H, 036H	; TH_B9
00A26	36 36 36 36 36 36	573	DB	036H, 036H, 036H, 036H, 036H, 036H	; BT_B9
00A2C	36 36 36 36 36 36	574	DB	036H, 036H, 036H, 036H, 036H, 036H	; TH_BA
00A34	36 36 36 36 36 36	575	DB	036H, 036H, 036H, 036H, 036H, 036H	; BT_BA
00A3A	00 00 00 00 00 FE	576	DB	000H, 000H, 000H, 000H, 000H, 0F6H	; TH_BB
00A42	06 F6	577	DB	000H, 000H, 000H, 000H, 000H, 0F6H	; TH_BB
00A42	36 36 36 36 36 36	580	DB	036H, 036H, 036H, 036H, 036H, 036H	; BT_BB
00A44	36 36 36 36 36 36	581	DB	036H, 036H, 036H, 036H, 036H, 036H	; TH_BD
00A50	06 F6	582	DB	000H, 000H, 000H, 000H, 000H, 0F6H	; BT_BD
00A56	00 00 00 00 00 00	584	DB	000H, 000H, 000H, 000H, 000H, 000H	; TH_BD
00A56	36 36 36 36 36 36	585	DB	036H, 036H, 036H, 036H, 036H, 036H	; TH_BD
00A56	36 FE	586	DB	000H, 000H, 000H, 000H, 000H, 000H	; TH_BD
00A5E	00 00 00 00 00 00	587	DB	018H, 018H, 018H, 018H, 018H, 0F8H	; BT_BD
00A64	18 18 18 18 18 F8	589	DB	018H, 018H, 018H, 018H, 018H, 0F8H	; TH_BE
00A6C	00 00 00 00 00 00	590	DB	000H, 000H, 000H, 000H, 000H, 000H	; TH_BF
00A72	00 00 00 00 00 00	591	DB	000H, 000H, 000H, 000H, 000H, 000H	; TH_BF
00A7A	00 F8	592	DB	018H, 018H, 018H, 018H, 018H, 018H	; BT_BF
00A80	18 18 18 18 18 18	594	DB	018H, 018H, 018H, 018H, 018H, 01FH	; TH_CD
00A88	00 00 00 00 00 00	595	DB	000H, 000H, 000H, 000H, 000H, 000H	; TH_CD
00A8E	18 18 18 18 18 18	596	DB	018H, 018H, 018H, 018H, 018H, 01FH	; TH_CD
00A96	00 00 00 00 00 00	597	DB	000H, 000H, 000H, 000H, 000H, 000H	; TH_CD
00A9C	00 00 00 00 00 00	599	DB	000H, 000H, 000H, 000H, 000H, 000H	; TH_CD
00AA4	18 18 18 18 18 18	603	DB	018H, 018H, 018H, 018H, 018H, 018H	; BT_CD
00AA8	18 18 18 18 18 18	604	DB	018H, 018H, 018H, 018H, 018H, 01FH	; TH_CD
00AB2	18 18 18 18 18 18	605	DB	018H, 018H, 018H, 018H, 018H, 018H	; BT_CD
00AB8	00 00 00 00 00 00	607	DB	000H, 000H, 000H, 000H, 000H, 000H	; TH_CD
00AC0	00 00 00 00 00 00	609	DB	000H, 000H, 000H, 000H, 000H, 000H	; TH_CD
00AC6	00 00 00 00 00 00	609	DB	000H, 000H, 000H, 000H, 000H, 000H	; TH_CD
00AC6	18 FF	610	DB	018H, 018H, 018H, 018H, 018H, 01FH	; TH_CD
00AD4	18 18 18 18 18 18	611	DB	018H, 018H, 018H, 018H, 018H, 018H	; BT_CD
00ADC	18 18 18 18 18 18	612	DB	018H, 018H, 018H, 018H, 018H, 01FH	; TH_CD
00AE2	36 36 36 36 36 36	614	DB	036H, 036H, 036H, 036H, 036H, 036H	; TH_CD
00AE4	36 36 36 36 36 36	616	DB	036H, 036H, 036H, 036H, 036H, 036H	; TH_CD
00AE6	36 36 36 36 36 36	617	DB	036H, 036H, 036H, 036H, 036H, 036H	; TH_CD
00AF8	00 00 00 00 00 00	620	DB	000H, 000H, 000H, 000H, 000H, 000H	; TH_CD
00AFE	00 00 00 00 00 00	621	DB	000H, 000H, 000H, 000H, 000H, 000H	; TH_CD
00B06	36 37 36 36 36 36	622	DB	036H, 036H, 036H, 036H, 036H, 036H	; TH_CD
00B0C	36 36 36 36 36 36	624	DB	036H, 036H, 036H, 036H, 036H, 036H	; TH_CD
00B14	00 00 00 00 00 00	627	DB	000H, 000H, 000H, 000H, 000H, 000H	; BT_CD
00B1A	00 00 00 00 00 FF	628	DB	000H, 000H, 000H, 000H, 000H, 0FFH	; TH_CD
00B22	36 36 36 36 36 36	630	DB	036H, 036H, 036H, 036H, 036H, 036H	; TH_CD
00B28	36 36 36 36 36 37	631	DB	036H, 036H, 036H, 036H, 036H, 037H	; TH_CD
00B30	36 36 36 36 36 36	632	DB	036H, 036H, 036H, 036H, 036H, 036H	; TH_CD
00B36	00 00 00 00 00 FF	634	DB	000H, 000H, 000H, 000H, 000H, 0FFH	; TH_CD
00B3E	00 00 00 00 00 00	635	DB	000H, 000H, 000H, 000H, 000H, 000H	; TH_CD
00B44	36 36 36 36 36 37	636	DB	036H, 036H, 036H, 036H, 036H, 037H	; TH_CD
00B4C	36 36 36 36 36 36	639	DB	036H, 036H, 036H, 036H, 036H, 036H	; TH_CD
00B52	18 18 18 18 18 FF	640	DB	018H, 018H, 018H, 018H, 018H, 0FFH	; TH_CD
00B5A	00 00 00 00 00 00	642	DB	000H, 000H, 000H, 000H, 000H, 000H	; TH_CD
00B60	36 36 36 36 36 36	644	DB	036H, 036H, 036H, 036H, 036H, 036H	; TH_CD
00B68	00 00 00 00 00 00	646	DB	000H, 000H, 000H, 000H, 000H, 000H	; TH_CD
00B6E	00 00 00 00 00 FF	647	DB	000H, 000H, 000H, 000H, 000H, 0FFH	; TH_CD
00B76	18 18 18 18 18 18	648	DB	018H, 018H, 018H, 018H, 018H, 01FH	; TH_CD
00B84	00 00 00 00 00 00	651	DB	000H, 000H, 000H, 000H, 000H, 000H	; TH_CD
00B84	36 36 36 36 36 36	652	DB	036H, 036H, 036H, 036H, 036H, 036H	; TH_CD
00B8C	36 36 36 36 36 36	653	DB	036H, 036H, 036H, 036H, 036H, 036H	; TH_CD
00B92	00 00 00 00 00 00	655	DB	000H, 000H, 000H, 000H, 000H, 000H	; TH_CD
00B98	18 18 18 18 18 1F	656	DB	018H, 018H, 018H, 018H, 018H, 01FH	; TH_CD
00BAA	00 00 00 00 00 00	657	DB	000H, 000H, 000H, 000H, 000H, 000H	; BT_CD
00B46	00 00 00 00 00 01	658	DB	000H, 000H, 000H, 000H, 000H, 01FH	; TH_CD
00B4E	18 1F	660	DB	018H, 018H, 018H, 018H, 018H, 01FH	; TH_CD
00B5A	18 18 18 18 18 18	661	DB	018H, 018H, 018H, 018H, 018H, 01FH	; TH_CD
00B84	00 00 00 00 00 00	662	DB	000H, 000H, 000H, 000H, 000H, 000H	; TH_CD
00B9C	00 00 00 00 00 00	664	DB	000H, 000H, 000H, 000H, 000H, 000H	; TH_CD
00BC2	36 36 36 36 36 36	665	DB	036H, 036H, 036H, 036H, 036H, 036H	; TH_CD
00BCA	36 36 36 36 36 36	666	DB	036H, 036H, 036H, 036H, 036H, 036H	; TH_CD
00B98	18 18 18 18 18 FF	668	DB	018H, 018H, 018H, 018H, 018H, 0FFH	; TH_CD
00BDB	18 18 18 18 18 18	670	DB	018H, 018H, 018H, 018H, 018H, 01FH	; TH_CD
00BDB	18 18 18 18 18 18	671	DB	018H, 018H, 018H, 018H, 018H, 01FH	; TH_CD
00B6E	00 00 00 00 00 00	673	DB	000H, 000H, 000H, 000H, 000H, 000H	; TH_CD
00BEC	00 00 00 00 00 00	674	DB	000H, 000H, 000H, 000H, 000H, 000H	; TH_CD
00BF4	18 18 18 18 18 18	675	DB	018H, 018H, 018H, 018H, 018H, 01FH	; TH_CD
00BFA	FF FF FF FF FF FF	676	DB	0FFH, 0FFH, 0FFH, 0FFH, 0FFH, 0FFH	; TH_CD
00C02	FF FF FF FF FF FF	678	DB	0FFH, 0FFH, 0FFH, 0FFH, 0FFH, 0FFH	; TH_CD
00C08	00 00 00 00 00 00	680	DB	000H, 000H, 000H, 000H, 000H, 000H	; TH_CD
00C10	FF FF FF FF FF FF	682	DB	0FFH, 0FFH, 0FFH, 0FFH, 0FFH, 0FFH	; TH_CD
00C16	FO FO FO FO FO FO	683	DB	0FOH, 0FOH, 0FOH, 0FOH, 0FOH, 0FOH	; TH_CD
00C1E	FO FO FO FO FO FO	684	DB	0FOH, 0FOH, 0FOH, 0FOH, 0FOH, 0FOH	; TH_CD
00C24	OF OF OF OF OF OF	684	DB	0FOH, 0FOH, 0FOH, 0FOH, 0FOH, 0FOH	; TH_CD
00C28	OF OF OF OF OF OF	687	DB	0FOH, 0FOH, 0FOH, 0FOH, 0FOH, 0FOH	; TH_CD
00C32	FF FF FF FF FF FF	689	DB	0FFH, 0FFH, 0FFH, 0FFH, 0FFH, 0FFH	; TH_CD
00C3A	00 00 00 00 00 00	691	DB	000H, 000H, 000H, 000H, 000H, 000H	; TH_CD
00C40	00 00 00 00 00 76	693	DB	000H, 000H, 000H, 000H, 000H, 000H	; TH_CD
00C8A	DB 76 00 00 00 00	693	DB	0DBH, 0DBH, 0DBH, 0DBH, 0DBH, 0DBH	; TH_CD

0C4E	00 00 00 00 7C C6	696	DB	000H, 000H, 000H, 000H, 07CH, 0C6H, 0FCH, 0C6H ; TH_E
FC	6C	697	DB	0C6H, 0FCH, 0C6H, 0C6H, 040H, 000H ; BT_E
0C56	C3 FC	698	DB	000H, 000H, 000H, 000H, 0FCH, 0C6H, 0C6H, 0C6H ; TH_E
0C50	00 FE C6 C6	699	DB	0C6H, 0C6H, 0C6H, 0C6H, 0C6H, 0C6H, 0C6H, 0C6H ; TH_E
0C64	00 C0 00 00 00	700	DB	0C6H, 0C6H, 0C6H, 0C6H, 0C6H, 0C6H, 0C6H, 0C6H ; TH_E
0C6A	00 00 00 00 FE 6C	702	DB	0C6H, 0C6H, 0C6H, 0C6H, 0C6H, 0C6H, 0C6H, 0C6H ; TH_E
6C	6C	703	DB	06CH, 0C6H, 0C6H, 000H, 000H, 000H ; BT_E
0C72	6C 6C 6C 00 00 00	704	DB	000H, 000H, 000H, 000H, 000H, 000H ; TH_E
0C78	6C 6C 6C 6C 60 00	706	DB	000H, 000H, 000H, 000H, 000H, 000H ; TH_E
18	30	707	DB	060H, 0C6H, 0FCH, 000H, 000H, 000H ; BT_E
0C80	60 6C FE 00 00 00	707	DB	000H, 000H, 000H, 000H, 000H, 000H ; TH_E
0C80	00 00 00 00 00 7E	708	DB	0D8H, 0D8H, 070H, 000H, 000H, 000H ; BT_E
0C8E	00 00 00 00 00 00	709	DB	000H, 000H, 000H, 000H, 000H, 000H ; TH_E
0C94	00 00 00 00 66 66	711	DB	07CH, 060H, 060H, 000H, 000H, 000H ; BT_E
66	66	712	DB	000H, 000H, 000H, 000H, 000H, 000H ; TH_E
0C9C	7C 60 60 60 00 00	713	DB	018H, 018H, 018H, 000H, 000H, 000H ; BT_E
0C42	00 00 00 00 76 DC	714	DB	000H, 000H, 000H, 000H, 000H, 000H ; TH_E
18	18	715	DB	000H, 000H, 000H, 000H, 000H, 000H ; TH_E
0CAA	18 18 18 00 00 00	716	DB	000H, 000H, 000H, 000H, 000H, 000H ; TH_E
0CBA	00 00 00 1E 3A 66	717	DB	000H, 000H, 000H, 000H, 000H, 000H ; TH_E
66	66	718	DB	03CH, 018H, 07EH, 000H, 000H, 000H ; BT_E
0CBB	18 1E 7E 00 00 00	719	DB	000H, 000H, 038H, 0C6H, 0C6H, 0C6H, 0FCH, 0C6H ; TH_E
0CBE	00 00 38 6C C6 C6	720	DB	0C6H, 0C6H, 038H, 000H, 000H, 000H ; BT_E
0CC6	C6 6C 38 00 00 00	722	DB	000H, 000H, 038H, 0C6H, 0C6H, 0C6H, 0C6H, 0C6H ; TH_E
0CCC	6C 6C 38 00 00 00	723	DB	06CH, 0C6H, 0C6H, 0C6H, 0C6H, 0C6H, 0C6H, 0C6H ; TH_E
6C	6C	724	DB	000H, 000H, 000H, 000H, 000H, 000H ; TH_E
0CD4	66 6C EE 00 00 00	725	DB	06CH, 0C6H, 0E0H, 000H, 000H, 000H ; BT_E
0CDA	00 00 1E 30 18 00	726	DB	000H, 000H, 01EH, 030H, 018H, 00CH, 03EH, 066H ; TH_E
1E	30	727	DB	066H, 066H, 03CH, 000H, 000H, 000H ; BT_E
0CE2	66 66 3C 00 00 00	728	DB	000H, 000H, 000H, 000H, 000H, 000H ; TH_E
0CE8	00 00 00 00 00 7E	729	DB	000H, 000H, 000H, 000H, 000H, 000H ; TH_E
DB	DB	730	DB	07EH, 000H, 000H, 000H, 000H, 000H ; BT_E
0CF0	70 00 00 00 00 00	731	DB	000H, 000H, 000H, 000H, 000H, 000H ; TH_E
0CF6	00 03 06 7E 0B	732	DB	000H, 000H, 000H, 000H, 000H, 000H ; TH_E
F3	F3	733	DB	07EH, 000H, 000H, 000H, 000H, 000H ; BT_E
0CFE	7E 60 60 00 00 00	734	DB	000H, 000H, 01CH, 030H, 006H, 060H, 07CH, 060H ; TH_E
0D04	00 00 00 1C 30 60	735	DB	000H, 000H, 01CH, 030H, 006H, 060H, 07CH, 060H ; TH_E
0D0C	00 00 00 00 00 00	736	DB	000H, 000H, 000H, 000H, 000H, 000H ; TH_E
0D12	00 30 1C 7C C6	738	DB	060H, 030H, 01CH, 000H, 000H, 000H ; BT_E
C6	C6	739	DB	000H, 000H, 000H, 000H, 000H, 000H ; TH_E
0D1A	C6 C6 00 00 00 00	740	DB	0C6H, 0C6H, 0C6H, 000H, 000H, 000H ; BT_E
0D20	00 00 00 FE 00 00	742	DB	000H, 000H, 000H, 000H, 000H, 000H ; TH_F
FE	00 00 00 00 00 00	743	DB	000H, 000H, 000H, 000H, 000H, 000H ; TH_F
0D28	00 00 00 00 00 00	744	DB	000H, 000H, 000H, 000H, 000H, 000H ; TH_F
0D2E	00 00 00 18 18 7E	745	DB	000H, 000H, 000H, 000H, 000H, 000H ; TH_F
18	18	746	DB	000H, 000H, 000H, 000H, 000H, 000H ; TH_F
0D36	00 00 FF 00 00 00	747	DB	000H, 000H, 0FFH, 000H, 000H, 000H ; BT_E
0D3C	00 00 30 18 00 06	748	DB	000H, 000H, 030H, 018H, 00CH, 006H, 00CH, 018H ; TH_F
0D44	30 00 00 00 00 00	749	DB	030H, 000H, 07EH, 000H, 000H, 000H ; BT_F
0D4A	00 00 00 18 30 60	751	DB	000H, 000H, 000H, 018H, 030H, 060H, 030H, 018H ; TH_F
30	18	752	DB	000H, 000H, 000H, 018H, 030H, 060H, 030H, 018H ; TH_F
0D52	00 00 00 7E 00 00 00	753	DB	000H, 000H, 000H, 000H, 000H, 000H ; TH_F
0D58	00 00 00 18 18 18	754	DB	000H, 000H, 000H, 018H, 018H, 018H, 018H, 018H ; TH_F
18	18	755	DB	018H, 018H, 018H, 018H, 018H, 018H, 018H, 018H ; TH_F
0D60	18 18 18 18 18 18	756	DB	018H, 018H, 018H, 018H, 018H, 018H, 018H, 018H ; TH_F
18	18	757	DB	018H, 018H, 018H, 018H, 018H, 018H, 018H, 018H ; TH_F
0D6E	08 70 00 00 00 00	758	DB	000H, 000H, 000H, 000H, 000H, 000H ; TH_F
0D74	00 00 18 18 00	760	DB	000H, 000H, 000H, 018H, 018H, 000H, 000H, 000H ; TH_F
7E	7E	761	DB	000H, 000H, 000H, 000H, 000H, 000H ; TH_F
0D7C	00 00 00 00 00 00	762	DB	018H, 018H, 000H, 000H, 000H, 000H ; TH_F
0D82	00 00 00 00 00 76 DC	763	DB	000H, 000H, 000H, 000H, 000H, 000H, 000H, 000H ; TH_F
76	76	764	DB	000H, 000H, 000H, 000H, 000H, 000H, 000H, 000H ; TH_F
0D8A	DC 00 00 00 00 00	765	DB	0DCH, 000H, 000H, 000H, 000H, 000H ; TH_F
0D90	00 38 6C 3C 38 00	766	DB	000H, 000H, 038H, 0C6H, 0C6H, 038H, 000H, 000H, 000H ; TH_F
38	6C	767	DB	000H, 000H, 000H, 000H, 000H, 000H, 000H, 000H ; TH_F
0D98	00 00 00 00 00 00	768	DB	000H, 000H, 000H, 000H, 000H, 000H, 000H, 000H ; TH_F
0D9E	00 00 00 00 00 00	769	DB	000H, 000H, 000H, 000H, 000H, 000H, 000H, 000H ; TH_F
18	18	770	DB	000H, 000H, 000H, 000H, 000H, 000H, 000H, 000H ; TH_F
0D46	00 00 00 00 00 00	771	DB	000H, 000H, 000H, 000H, 000H, 000H, 000H, 000H ; TH_F
0D4C	00 00 00 00 00 00	772	DB	000H, 000H, 000H, 000H, 000H, 000H, 000H, 000H ; TH_F
0D84	00 00 00 00 00 00	773	DB	000H, 000H, 000H, 000H, 000H, 000H, 000H, 000H ; TH_F
0D8A	00 00 00 00 00 00	774	DB	000H, 000H, 000H, 000H, 000H, 000H, 000H, 000H ; TH_F
0D9C	00 00 00 00 00 00	775	DB	000H, 000H, 000H, 000H, 000H, 000H, 000H, 000H ; TH_F
0D22	6C 3C 1C 00 00 00	777	DB	06CH, 01CH, 01CH, 000H, 000H, 000H ; TH_F
0D2C	68 6C 6C 6C 6C	778	DB	000H, 000H, 068H, 0C6H, 0C6H, 06CH, 0C6H, 000H, 000H ; TH_F
6C	6C	779	DB	000H, 000H, 000H, 000H, 000H, 000H, 000H, 000H ; TH_F
0D90	00 00 00 00 00 00	780	DB	000H, 000H, 000H, 000H, 000H, 000H, 000H, 000H ; TH_F
0D96	00 00 00 00 00 00	781	DB	000H, 000H, 000H, 000H, 000H, 000H, 000H, 000H ; TH_F
F8	00	782	DB	000H, 000H, 000H, 000H, 000H, 000H, 000H, 000H ; TH_F
0DDE	00 00 00 00 00 00	783	DB	000H, 000H, 000H, 000H, 000H, 000H, 000H, 000H ; TH_F
0D44	00 00 00 00 00 7C 7C	784	DB	000H, 000H, 000H, 000H, 000H, 000H, 000H, 000H ; TH_F
7C	7C	785	DB	000H, 000H, 000H, 000H, 000H, 000H, 000H, 000H ; TH_F
0DEC	7C 7C 00 00 00 00	786	DB	07CH, 07CH, 000H, 000H, 000H, 000H, 000H, 000H ; TH_F
0DFF	00 00 00 00 00 00	787	DB	000H, 000H, 000H, 000H, 000H, 000H, 000H, 000H ; TH_F
0DFA	00 00 00 00 00 00	788	DB	000H, 000H, 000H, 000H, 000H, 000H, 000H, 000H ; TH_F
0D90	00 00 00 00 00 00	789	DB	000H, 000H, 000H, 000H, 000H, 000H, 000H, 000H ; TH_F
0E00	00 00 00 00 00 00	790	CODE	ENDS

```

1 PAGE 120
2 SUBTITL MONOCHROME CHARACTER GENERATOR - ALPHA SUPPLEMENT
3
4 CODE SEGMENT PUBLIC CGMN_FDG
5 PUBLIC CGMN_FDG
6 LABEL BYTE
7
8 ; STRUCTURE OF THIS FILE
9 ; DB XXH WHERE XX IS THE HEX CODE FOR THE FOLLOWING CHAR
10 ; [BYTES 0 - 13 OF THAT CHARACTER]
11 ; DB ...
12 ; DB 00H INDICATES NO MORE REPLACEMENTS TO BE DONE
13
14
15
16
17
18
19
20
21
22
23
24
25
26
27
28
0000 1D 00 00 00 24 66 0000,000H,000H,000H,024H,066H,0FFH,066H ; TH_1D
0001 FF 66 00 00 00 00 0000,000H,000H,000H,000H,000H,000H,000H ; BT_1D
0009 24 00 00 00 00 00 000H,000H,000H,000H,000H,000H,000H,000H ; BT_2D
000F 2D 00 00 00 00 00 000H,063H,063H,063H,022H,000H,000H,000H ; TH_22 "
0010 63 63 63 63 22 00 000H,063H,063H,063H,022H,000H,000H,000H ; TH_22 "
0011 00 00 00 00 00 00 000H,000H,000H,000H,000H,000H,000H,000H ; BT_22 "
001E 00 00 00 18 18 18 000H,000H,000H,018H,018H,018H,0FFH,018H ; TH_2B +
001F FF 18 00 00 00 00 000H,000H,000H,018H,018H,018H,0FFH,018H ; TH_2B +
0027 18 18 00 00 00 00 000H,018H,018H,000H,000H,000H,000H,000H ; BT_2B +
0028 00 00 00 00 00 00 000H,020H,020H,000H,000H,000H,000H,000H ; TH_2B -
002E 00 00 00 00 00 00 000H,000H,000H,000H,000H,000H,000H,000H ; TH_2D -
002F FF 00 00 00 00 00 000H,000H,000H,000H,000H,000H,000H,000H ; TH_2D -

```

0036	00 00 00 00 00 00	29	DB	000H, 000H, 000H, 000H, 000H, 000H	; BT_20 -
003C	4D	30	DB	010H	
003D	00 00 C3 E7 FF DB	31	DB	000H, 000H, 0C3H, 0E7H, 0FFH, 0DBH, 0C3H, 0C3H	; TH_40 M
C3	C3	32	DB	0C3H, 0C3H, 0C3H, 000H, 000H, 000H	; BT_40 M
0045	54	33	DB	05AH	
0048	00 00 FF BD 99 18	35	DB	000H, 000H, 0FFH, 0DBH, 099H, 018H, 018H, 018H	; TH_54 T
18	18	36	DB	018H, 018H, 03CH, 000H, 000H, 000H	; BT_54 T
0054	18 18 3C 00 00 00	37	DB	056H	
005A	56	38	DB	000H, 000H, 0C3H, 0C3H, 0C3H, 0C3H, 0C3H, 0C3H	; TH_56 V
005B	00 00 C3 C3 C3 C3	39	DB	0C3C, 0C3C, 0C3C, 0C3C, 0C3C, 0C3C, 0C3C, 0C3C	
C3	C3	40	DB	066H, 03CH, 018H, 000H, 000H, 000H	; BT_56 V
0063	66 3C 18 00 00 00	41	DB	057H	
0065	00 00 C3 C3 C3 C3	43	DB	000H, 000H, 0C3H, 0C3H, 0C3H, 0C3H, 0DBH, 0DBH	; TH_57 W
DB	DB	44	DB	0FFH, 066H, 066H, 000H, 000H, 000H	; BT_57 W
0072	FF 66 66 00 00 00	45	DB	058H	
0078	56	46	DB	000H, 000H, 0C3H, 0C3H, 066H, 03CH, 018H, 03CH	; TH_58 X
0079	00 00 C3 C3 66 3C	47	DB	066H, 03CH, 03CH, 000H, 000H, 000H	; BT_58 X
18 3C	18	48	DB	059H	
0081	66 C3 C3 00 00 00	49	DB	000H, 000H, 0C3H, 0C3H, 0C3H, 066H, 03CH, 018H	; TH_59 Y
0087	59	50	DB	018H, 018H, 03CH, 000H, 000H, 000H	; BT_59 Y
0088	00 00 C3 C3 C3 66	51	DB	05AH	
3C 18	18	52	DB	000H, 000H, 0C3H, 0C3H, 0C3H, 0C3H, 03CH, 018H	; TH_59 Y
0090	18 18 3C 00 00 00	53	DB	061H, 03CH, 03CH, 000H, 000H, 000H	; BT_5A Z
0096	5A	54	DB	060H	
0097	00 FF F3 86 OC	55	DB	000H, 000H, 0FFH, 0C3H, 086H, 00CH, 018H, 030H	; TH_5A Z
86 30	30	56	DB	066H, 03CH, 0FFH, 000H, 000H, 000H	; BT_5A Z
009F	61 C3 FF 00 00 00	57	DB	07H	
00A5	60	58	DB	000H, 000H, 000H, 000H, 000H, 000H	; TH_60 L.C. M
00A6	00 00 00 00 00 E6	59	DB	0DBH, 0DBH, 0DBH, 000H, 000H, 000H	; BT_60 L.C. M
00B4	76	60	DB	076H	
00B5	00 00 00 00 C3	63	DB	000H, 000H, 000H, 000H, 000H, 0C3H, 0C3H, 0C3H	; TH_76 L.C. V
C3 C3	18	64	DB	066H, 03CH, 018H, 000H, 000H, 000H	; BT_76 L.C. V
00BD	00 00 00 00 00 00	65	DB	077H	
00C3	77	66	DB	000H, 000H, 000H, 000H, 000H, 0C3H, 0C3H, 0DBH	; TH_77 L.C. W
00C4	00 00 00 00 00 C3	67	DB	066H, 03CH, 018H, 000H, 000H, 000H	; TH_77 L.C. W
CD	DB	68	DB	0DBH, 0FFH, 066H, 000H, 000H, 000H	; BT_77 L.C. W
00C5	FF F7 66 00 00 00	69	DB	091H	
0002	91	70	DB	000H, 000H, 000H, 000H, 000H, 03BH, 018H, 07EH	; TH_91
00D3	00 00 00 00 6E 3B	71	DB	0DBH, 0DBH, 0DBH, 000H, 000H, 000H	; BT_91
18 7E	72	DB	098H		
00D6	DB DC 77 00 00 00	73	DB	000H, 018H, 018H, 07EH, 000H, 000H	; TH_98
00D7	18 18 7E C3 C0	74	DB	000H, 018H, 018H, 07EH, 0C3H, 0C0H, 0C0H, 0C3H	; TH_98
CO C3	75	DB	07EH, 018H, 018H, 000H, 000H, 000H	; BT_98	
00EA	75 18 18 00 00 00	77	DB	090H	
00F0	78	78	DB	000H, 000H, 0C3H, 066H, 03CH, 018H, 0FFH, 018H	; TH_90
00F7	00 00 C3 63 C3 18	79	DB	0DBH, 0FFH, 066H, 000H, 000H, 000H	; TH_90
FF 18	80	DB	090H, 0C3H, 018H, 000H, 000H, 000H	; TH_90	
00F9	FF 9E 18 00 00 00	81	DB	0E9H	
0100	00 FC 66 66 7C 62	82	DB	000H, 0FCM, 066H, 066H, 07CH, 062H, 066H, 06FH	; TH_9E
66 6F	66 6F	84	DB	066H, 066H, 03FH, 000H, 000H, 000H	; BT_9E
0108	66 6F F3 00 00 00	85	DB	07FH	
010E	F1	86	DB	000H, 000H, 018H, 018H, 018H, 018H, 0FFH, 018H	; TH_F1
010F	00 00 18 18 18 FF	87	DB	018H, 000H, 0FFH, 000H, 000H, 000H	; BT_F1
18 18	18	88	DB	0F6H	
0117	18 00 FF 00 00 00	89	DB	000H, 000H, 018H, 018H, 018H, 000H, 000H, 0FFH, 000H	; TH_F6
011D	F6	90	DB	000H, 018H, 018H, 018H, 018H, 000H, 000H, 0FFH, 000H	; TH_F6
011E	FF 00 00 18 18 00 00	91	DB	000H, 018H, 018H, 018H, 018H, 000H, 000H, 0FFH, 000H	; TH_F6
0126	00 18 18 00 00 00	92	DB	000H, 018H, 018H, 000H, 000H, 000H	; BT_F6
012C	00	93	DB	000H	; NO MORE
012D	95	95	CODE	ENDS	
	96	96	CODE	END	

1	PAGE 120				
2	CODE	SUBTITL DOUBLE DOT CHARACTER GENERATOR			
3	SEGMENT	PUBLIC			
4	CGDDOT	CGDDOT, INT_1F_1			
5	LABEL	BYTE			
6	0000	00 00 00 00 00 00	DB	000H, 000H, 000H, 000H, 000H, 000H	; DOUBLE DOT
7	00 00	8	DB	000H, 000H, 000H, 000H, 000H, 000H	; _D_00
8	0008	7E 81 A5 81 BD 99	DB	07EH, 018H, 0A5H, 081H, 0BDH, 099H, 081H, 07EH	; _D_01
9	81 7E	10	DB	07EH, 0FFH, 0DBH, 0FFH, 0C3H, 0E7H, 0FFH, 07EH	; _D_02
10	00 00 FF 07 C3 E7	11	DB	06CH, 0FEH, 0FEH, 0FEH, 07CH, 03BH, 010H, 000H	; _D_03
11	FF 7E	12	DB	010H, 03BH, 07CH, 0FEH, 07CH, 03BH, 010H, 000H	; _D_04
12	0018	6C FE FE FE 7C 38	DB	038H, 07CH, 038H, 0FEH, 07CH, 038H, 010H, 000H	; _D_05
13	6C FE	13	DB	010H, 010H, 038H, 07CH, 0FEH, 07CH, 038H, 010H, 000H	; _D_06
14	10 00	14	DB	000H, 000H, 018H, 03CH, 03CH, 018H, 000H, 000H	; _D_07
15	10 38	15	DB	0FFH, 0FFH, 07EH, 0C3H, 0C3H, 0E7H, 0FFH, 07EH	; _D_08
16	0028	38 7C 38 FE FE 7C	DB	000H, 03CH, 066H, 042H, 042H, 066H, 03CH, 000H	; _D_09
17	38 7C	17	DB	0FFH, 03CH, 099H, 0BDH, 0BDH, 099H, 0C3H, 0FFH	; _D_0A
18	0030	18 10 38 7C 7C 7C	DB	00FH, 00FH, 00FH, 07DH, 0CCH, 0CCH, 0CCH, 078H	; _D_0B
19	0038	00 00 18 3C 3C 18	DB	03CH, 066H, 066H, 066H, 03CH, 018H, 07EH, 018H	; _D_0C
20	00 00	21	DB	03FH, 033H, 03FH, 030H, 030H, 070H, 0F0H, 0E0H	; _D_0D
21	0004	FF FF E7 C3 C3 E7	DB	07FH, 063H, 07FH, 063H, 063H, 067H, 0E6H, 0C0H	; _D_0E
22	0048	00 3C 66 42 42 66	DB	099H, 05AH, 03CH, 0E7H, 0E7H, 03CH, 05AH, 099H	; _D_0F
23	3C 00	26	DB	080H, 0E0H, 0F8H, 0FEH, 0F8H, 0E0H, 080H, 000H	; _D_10
24	0050	FF C3 99 BD BD 99	DB	002H, 00E0H, 03EH, 0FEH, 03EH, 00EH, 002H, 000H	; _D_11
25	CF 00	28	DB	018H, 03CH, 07EH, 018H, 018H, 07EH, 03CH, 018H	; _D_12
26	CC 78	30	DB	066H, 066H, 066H, 066H, 066H, 066H, 000H, 066H, 000H	; _D_13
27	7E 18	31	DB	07FH, 0DBH, 0DBH, 07BH, 018H, 018H, 000H, 000H	; _D_14
28	0066	32	DB	03EH, 033H, 03FH, 030H, 030H, 070H, 0F0H, 0E0H	; _D_15
29	3E 32 3F 30 30 70	33	DB	07FH, 063H, 07FH, 063H, 063H, 067H, 0E6H, 0C0H	; _D_16
30	3E 32	34	DB	018H, 03CH, 07EH, 018H, 018H, 07EH, 03CH, 018H	; _D_17
31	70 EO	35	DB	018H, 03CH, 07E 18 18 7E 3C	; _D_18
32	7F 63 7F 63 63 67	36	DB	018H, 03CH, 07E 18 18 7E 3C	; _D_18
33	6E CO	37	DB	018H, 03CH, 07E 18 18 7E 3C	; _D_18
34	5A 99	38	DB	018H, 03CH, 07E 18 18 7E 3C	; _D_18
35	99 5A 3C E7 3C	39	DB	018H, 03CH, 07E 18 18 7E 3C	; _D_18
36	3C 00	40	DB	018H, 03CH, 07E 18 18 7E 3C	; _D_18
37	00 00	41	DB	018H, 03CH, 07E 18 18 7E 3C	; _D_18
38	00 02	42	DB	018H, 03CH, 07E 18 18 7E 3C	; _D_18
39	00 00	43	DB	018H, 03CH, 07E 18 18 7E 3C	; _D_18
40	00 00	44	DB	018H, 03CH, 07E 18 18 7E 3C	; _D_18
41	00 00	45	DB	018H, 03CH, 07E 18 18 7E 3C	; _D_18
42	00 00	46	DB	018H, 03CH, 07E 18 18 7E 3C	; _D_18
43	00 00	47	DB	018H, 03CH, 07E 18 18 7E 3C	; _D_18
44	00 00	48	DB	018H, 03CH, 07E 18 18 7E 3C	; _D_18
45	00 00	49	DB	018H, 03CH, 07E 18 18 7E 3C	; _D_18
46	00 00	50	DB	018H, 03CH, 07E 18 18 7E 3C	; _D_18
47	00 00	51	DB	018H, 03CH, 07E 18 18 7E 3C	; _D_18
48	00 00	52	DB	018H, 03CH, 07E 18 18 7E 3C	; _D_18
49	00 00	53	DB	018H, 03CH, 07E 18 18 7E 3C	; _D_18
50	00 00	54	DB	018H, 03CH, 07E 18 18 7E 3C	; _D_18
51	00 00	55	DB	018H, 03CH, 07E 18 18 7E 3C	; _D_18
52	00 00	56	DB	018H, 03CH, 07E 18 18 7E 3C	; _D_18

00C8	18 00	18 18 18 18 7E 3C	57	DB	018H,018H,018H,018H,07EH,03CH,018H,00H	; D_19
00D0	00 00	00 0C FE 0C 18	59	DB	000H,018H,00CH,0FEH,00CH,018H,00H,00H	; D_1A
00D0	00 00	00 30 60 FE 60 30	61	DB	000H,030H,060H,0FEH,060H,030H,00H,00H	; D_1B
00E0	00 00	00 CO CO CO FE	62	DB	000H,000H,0COH,0COH,0COH,0FEH,00H,00H	; D_1C
00E8	00 24	FF 66 FF 66 24	63	DB	000H,024H,066H,0FFH,066H,024H,00H,00H	; D_1D
00F0	00 18	3C 7E FF FF	67	DB	000H,018H,03CH,07EH,0FFH,0FFH,00H,00H	; D_1E
00F8	00 00	FF FF 7E 3C 18	70	DB	000H,0FFH,0FFH,07EH,03CH,018H,00H,00H	; D_1F
			71			
0100	00 00	00 00 00 00 00	72	DB	000H,000H,000H,000H,000H,000H,00H,00H	; SP_D_20
0108	30 78	78 30 30 00	73	DB	030H,078H,078H,030H,030H,00H,030H,00H	; ! D_21
0108	30 78	78 30 30 00	75	DB	06CH,06CH,06CH,00H,00H,00H,00H,00H	; # D_22
0108	30 78	78 30 30 00	76	DB	06CH,06CH,0FEH,06CH,0FEH,06CH,06CH,00H	; # D_23
0110	6C 6C	6C 6C 00 00 00	77	DB	030H,07CH,07CH,030H,07CH,030H,00H,00H	; S_D_24
0118	6C 6C	FE 6C FE 6C	79	DB	000H,0C6H,0CCH,018H,030H,066H,0C6H,00H	; PER CENT_D_25
0118	6C 6C	FE 6C 00 00	80	DB	038H,06CH,038H,07CH,06CH,0CCH,07CH,00H	; & D_26
0120	30 7C	7C 08 7B 08 FB	81	DB	060H,030H,060H,060H,060H,030H,060H,00H	; D_27
0128	6C 6C	CC 18 30 66	82	DB	000H,066H,03CH,0FFH,03CH,066H,00H,00H	; * D_2A
0130	36 3C	38 76 DC CC	84	DB	000H,030H,030H,0FFH,03CH,030H,030H,00H	; + D_2B
0138	60 60	60 CO 00 00	85	DB	060H,060H,0COH,0COH,00H,00H,00H,00H	; ^ D_27
0140	18 30	60 60 60 30	88	DB	018H,030H,050H,060H,060H,030H,018H,00H	; (D_28
0148	60 30	18 18 18 30	89	DB	060H,030H,018H,018H,018H,030H,060H,00H	;) D_29
0150	60 66	3C FF 3C 66	90	DB	000H,066H,03CH,0FFH,03CH,066H,00H,00H	; * D_2A
0158	00 00	30 30 FC 30 30	93	DB	000H,030H,030H,0FFH,03CH,030H,030H,00H	; + D_2B
0160	00 00	00 00 00 00 30	94	DB	000H,000H,000H,000H,000H,000H,00H,00H	; , D_2C
0168	00 00	00 00 00 00 30	97	DB	000H,000H,000H,000H,000H,000H,00H,00H	; , D_2C
0170	00 00	00 00 00 00 30	98	DB	000H,000H,000H,000H,000H,000H,00H,00H	; - D_2D
0178	06 0C	18 30 60 CO	100	DB	000H,000H,000H,000H,000H,000H,00H,00H	; . D_2E
	80	80	102	DB	006H,0COH,018H,030H,060H,0COH,080H,00H	; / D_2F
0180	7C 6C	CE DE FE E6	104	DB	07CH,0C6H,0CEH,0DEH,0F6H,0E6H,07CH,00H	; 0 D_30
0188	7C 00	70 30 30 30 30	107	DB	030H,070H,030H,030H,030H,030H,030H,00H	; 1 D_31
0190	FC 00	OC 38 60 CC	109	DB	078H,0CCH,00CH,038H,060H,0CCH,0FFH,00H	; 2 D_32
0198	78 CC	00 38 00 CC	111	DB	078H,0CCH,00CH,038H,060H,0CCH,078H,00H	; 3 D_33
01A0	1E 00	6C CC FE 0C	112	DB	01CH,03CH,06CH,0CCH,0FEH,00CH,01EH,00H	; 4 D_34
01A8	FC CO	F8 00 CC 0C	113	DB	0FCH,0COH,0F8H,00CH,0OCH,0CCH,078H,00H	; 5 D_35
01B0	78 00	00 FC F8 CC CC	115	DB	038H,060H,0COH,0F8H,0CCH,0CCH,078H,00H	; 6 D_36
01B8	78 00	00 00 00 00 30	117	DB	0FCH,0COH,0OCH,018H,030H,030H,030H,00H	; 7 D_37
01C0	78 CC	CC 7C 0C CC	120	DB	078H,0CCH,0CCH,078H,0CCH,0CCH,078H,00H	; 8 D_38
01C8	78 CC	CC 7C 0C 18	122	DB	078H,0CCH,0CCH,07CH,0CCH,018H,070H,00H	; 9 D_39
01D0	00 30	30 00 00 00 30	125	DB	000H,030H,030H,000H,000H,030H,030H,00H	; : D_3A
01D8	00 30	30 00 00 00 30	127	DB	000H,030H,030H,000H,000H,030H,030H,00H	; : D_3B
01E0	18 30	60 60 60 30	129	DB	018H,030H,060H,0COH,060H,030H,018H,00H	; < D_3C
01E8	00 00	FC 00 00 FC	130	DB	000H,000H,0FCH,000H,000H,0FCH,000H,00H	; = D_3D
01F0	60 30	18 0C 18 30	131	DB	060H,030H,018H,00CH,018H,030H,060H,00H	; > D_3E
01F8	78 CC	OC 18 30 00	132	DB	078H,0CCH,0OCH,018H,030H,000H,030H,00H	; ? D_3F
0200	7C 6C	DE DE DC CO	134	DB	07CH,0C6H,0DEH,0DEH,0DEH,0COH,078H,00H	; @ D_40
0208	7C 00	OC CC FC CC	140	DB	030H,078H,0CCH,0CCH,0FFH,0CCH,0CCH,00H	; A D_41
0210	FC 66	66 7C 66 66	142	DB	0FCH,066H,066H,07CH,066H,066H,0FFH,00H	; B D_42
0218	3C 66	CO CO 66 CC	143	DB	03CH,066H,0COH,0COH,0COH,0COH,066H,03CH,00H	; C D_43
0220	FF 6C	66 66 66 66 6C	145	DB	0F8H,06CH,066H,066H,066H,06CH,0F8H,00H	; D D_44
0228	FF 62	68 7B 68 62	147	DB	0FEH,062H,068H,078H,068H,062H,0FEH,00H	; E D_45
0230	FE 62	68 7B 68 60	151	DB	0FEH,062H,068H,078H,068H,060H,0FGH,00H	; F D_46
0238	3C 66	CO CO 66 CC	153	DB	03CH,066H,0COH,0COH,0CCH,066H,03CH,00H	; G D_47
0240	CC 00	CC FC CC CC	154	DB	0CCH,0CCH,0CCH,0FCH,0CCH,0CCH,0CCH,00H	; H D_48
0248	7B 30	30 30 30 30	157	DB	078H,030H,030H,030H,030H,030H,078H,00H	; I D_49
0250	78 00	00 OC CC CC	158	DB	01EH,00CH,0OCH,0OCH,0CCH,0CCH,078H,00H	; J D_4A
0258	E6 66	6C 7B 66 6C	159	DB	0E6H,066H,06CH,078H,06CH,066H,0E6H,00H	; K D_4B
0260	FE 60	60 60 62 66	162	DB	0F0H,060H,060H,060H,062H,066H,0FEH,00H	; L D_4C
0268	C6 FE	FE DE G6	164	DB	0C6H,0EEH,0FEH,0FEH,0D6H,0C6H,0C6H,00H	; M D_4D
0270	CG 6E	F6 DE CC G6	166	DB	0C6H,0EEH,0F6H,0DEH,0CEH,0C6H,0C6H,00H	; N D_4E
0278	3C 6C	CC 6C 6C 6C	169	DB	038H,06CH,006H,06CH,038H,00H,038H,00H	; O D_4F
0280	FC 66	66 7C 60 60	171	DB	0FCH,066H,066H,07CH,060H,060H,0F0H,00H	; P D_50
0288	7B CC	CC CC DC 7B	174	DB	078H,0CCH,0CCH,0CCH,0DCH,078H,01CH,00H	; Q D_51
0290	FC 66	66 7C 66 66	176	DB	0FCH,066H,066H,07CH,06CH,066H,0E6H,00H	; R D_52
0298	7B CC	E0 70 1C CC	178	DB	078H,0CCH,0E0H,070H,01CH,0CCH,078H,00H	; S D_53
02A0	FB 84	30 30 30 30	179	DB	0FCB,084H,030H,030H,030H,030H,078H,00H	; T D_54
02A8	CG CC	CC CC CC CC	182	DB	0CCH,0CCH,0CCH,0CCH,0CCH,0CCH,0CCH,00H	; U D_55

0280	FC 00	183	DB	0CCH, OCCH, OCCH, OCCH, OCCH, 078H, 030H, 000H ; V D_56
0280	CC CC CC CC CC 78	184	DB	0C6H, OC6H, OC6H, OC6H, 0FEH, 0EEN, OC6H, 000H ; W D_57
0288	00 00	185	DB	0C6H, OC6H, OC6H, 038H, 038H, 06CH, 0C6H, 000H ; X D_58
02C0	C6 00	186	DB	0CCH, OCCH, OCCH, 078H, 030H, 030H, 078H, 000H ; Y D_59
02C8	CC CC CC 78 30 30	187	DB	0FEH, OC6H, 08CH, 018H, 032H, 066H, 0FEH, 000H ; Z D_5A
02D0	FE 6C	188	DB	078H, 060H, 060H, 060H, 060H, 060H, 078H, 000H ; I D_5B
02D8	78 60 60 60 60	189	DB	0C0H, 060H, 030H, 018H, 00CH, 006H, 002H, 000H ; BACKSLASH D_5C
02E0	00 00 30 18 06	190	DB	078H, 018H, 018H, 018H, 018H, 078H, 000H ; J D_5D
02E8	02 00	191	DB	010H, 038H, 06CH, OC6H, 000H, 000H, 000H, 000H ; CIRCUMFLEX D_5E
02F0	00 00 6C C6 00 00	192	DB	000H, 000H, 000H, 000H, 000H, 000H, 000H, 0FFH ; _ D_5F
02F8	00 00 00 00 00 00	193	DB	203
02F8	00 FF	194	DB	204
0300	00 30 18 00 00 00	195	DB	030H, 030H, 018H, 000H, 000H, 000H, 000H, 000H ; ' D_60
0308	00 00 00 78 0C 7C CC	196	DB	000H, 000H, 078H, 00CH, 07CH, OCCH, 076H, 000H ; LOWER CASE A D_61
0310	76 00	197	DB	0E0H, 060H, 060H, 07CH, 066H, 066H, 0DCH, 000H ; L.C. B D_62
0310	00 00 60 76 66 66	198	DB	000H, 000H, 078H, OCCH, 0C0H, OCCH, 078H, 000H ; L.C. C D_63
0318	00 00 78 CC CC CC	199	DB	01CH, OCCH, 00CH, 07CH, OCCH, OCCH, 076H, 000H ; L.C. D D_64
0320	1C 00 00 0C 7C CC CC	200	DB	000H, 000H, 078H, OCCH, OFCH, 0C0H, 078H, 000H ; L.C. E D_65
0328	00 00 78 CC FC C0	201	DB	038H, OC6H, 060H, 0F0H, 060H, 060H, 0F0H, 000H ; L.C. F D_66
0330	38 6C 60 F0 60 60	202	DB	000H, 000H, 076H, OCCH, 0CCH, 07CH, 00CH, 0F8H ; L.C. G D_67
0338	00 00 76 CC CC 7C	203	DB	0E0H, 060H, 060H, 076H, 066H, 066H, 0E6H, 000H ; L.C. H D_68
0340	E0 60 6C 76 66 66	204	DB	030H, 000H, 070H, 030H, 030H, 030H, 078H, 000H ; L.C. I D_69
0348	6C 00 00 00 70 30 30	205	DB	00CH, 000H, 00CH, 00CH, 00CH, OCCH, 078H ; L.C. J D_6A
0350	0C 00 0C 0C 0C CC	206	DB	0E0H, 060H, 066H, 06CH, 078H, 06CH, 0E6H, 000H ; L.C. K D_6B
0358	CC 78	207	DB	070H, 030H, 030H, 030H, 030H, 030H, 078H, 000H ; L.C. L D_6C
0360	78 00	208	DB	000H, 000H, 0CCH, 0FEH, 0FEH, 0D6H, OC6H, 000H ; L.C. M D_6D
0368	00 00 00 CC FE FE D6	209	DB	000H, 000H, 0F8H, OCCH, OCCH, OCCH, OCCH, 000H ; L.C. N D_6E
0370	C6 00	210	DB	000H, 000H, 078H, OCCH, OCCH, OCCH, 0CCH, 078H, 000H ; L.C. O D_6F
0378	00 00 78 CC CC CC	211	DB	237
0380	78 00	212	DB	000H, 000H, 0DCH, 066H, 066H, 07CH, 060H, 0F0H ; L.C. P D_70
0388	00 00 00 76 CC CC 7C	213	DB	000H, 000H, 076H, OCCH, OCCH, 0CCH, 07CH, 00CH, 01EH ; L.C. Q D_71
0390	00 00 00 DC 76 66 60	214	DB	000H, 000H, 0DCH, 076H, 066H, 060H, 0F0H, 000H ; L.C. R D_72
0398	F0 00	215	DB	000H, 000H, 07CH, OCCH, 078H, 00CH, 0F8H, 000H ; L.C. S D_73
03A0	10 30 7C 30 30 34	216	DB	010H, 030H, 07CH, 030H, 030H, 034H, 018H, 000H ; L.C. T D_74
03A8	00 00 CC CC CC CC	217	DB	000H, 000H, 0CCH, OCCH, OCCH, OCCH, 076H, 000H ; L.C. U D_75
03B0	00 00 CC CC CC 7C	218	DB	000H, 000H, 0CCCH, OCCH, OCCH, 078H, 030H, 000H ; L.C. V D_76
03B8	00 00 C6 D6 FE FE	219	DB	000H, 000H, 0C6H, 0D6H, 0FEN, 0FEN, 06CH, 000H ; L.C. W D_77
03C0	6C 00 00 0C 6C 38 6C	220	DB	000H, 000H, 0C6H, 06CH, 038H, 06CH, 0C6H, 000H ; L.C. X D_78
03C8	00 00 CC CC CC 7C	221	DB	000H, 000H, 0CCH, OCCH, OCCH, 0CCH, 07CH, 00CH, 0F8H ; L.C. Y D_79
03D0	7C 98 30 64	222	DB	000H, 000H, 0FCH, 098H, 030H, 064H, 0FCH, 000H ; L.C. Z D_7A
03D8	FC 00	223	DB	01CH, 030H, 030H, 0E0H, 030H, 030H, 01CH, 000H ; L BRAK D_7B
03D8	1C 30 30 00 30 30	224	DB	018H, 018H, 018H, 000H, 018H, 018H, 018H, 000H ; I D_7C
03E0	18 18 18 00 18 18	225	DB	0E0H, 030H, 030H, 01CH, 030H, 030H, 0E0H, 000H ; R BRAK D_7D
03E8	E0 30 30 1C 30 30	226	DB	076H, 0DCH, 000H, 000H, 000H, 000H, 000H, 000H ; TILDE D_7E
03F0	76 DC 00 00 00 00	227	DB	000H, 010H, 038H, 0C6H, 0C6H, 0C6H, 0FCH, 000H ; DELTA D_7F
03F8	00 10 38 6C C6 C6	228	DB	270
0400	FE 00	229	INT_1F_1	LABEL BYTE
0400	78 CC CO CC 78 18	230	DB	078H, OCCH, 0C0H, OCCH, 078H, 018H, 00CH, 078H ; D_80
0408	00 CC 00 CC CC CC	231	DB	000H, OCCH, 000H, OCCH, OCCH, OCCH, 07EH, 000H ; D_81
0410	1C 00 00 78 CC FC C0	232	DB	01CH, 000H, 078H, OCCH, OFCH, 0C0H, 078H, 000H ; D_82
0418	78 00	233	DB	07EH, 0C3H, 03CH, 006H, 03EH, 066H, 031H, 000H ; D_83
0420	00 00 00 78 0C 7C CC	234	DB	0CCH, 000H, 078H, 00CH, 07CH, OCCH, 07EH, 000H ; D_84
0428	E0 00 00 78 0C 7C CC	235	DB	0E0H, 000H, 078H, 00CH, 07CH, OCCH, 07EH, 000H ; D_85
0430	78 00 00 78 0C 7C CC	236	DB	030H, 030H, 078H, 00CH, 07CH, OCCH, 07EH, 000H ; D_86
0438	00 00 00 78 CC CO 78	237	DB	000H, 000H, 078H, 0C0H, 0C0H, 078H, 00CH, 038H ; D_87
0440	7E C3 3C 66 7E 60	238	DB	07EH, 0C3H, 03CH, 066H, 07EH, 060H, 03CH, 000H ; D_88
0448	CC 00 00 78 CC FC C0	239	DB	0CCH, 000H, 078H, 0CCH, OFCH, 0C0H, 078H, 000H ; D_89
0450	E0 00 00 78 CC FC C0	240	DB	0E0H, 000H, 078H, 0CCH, OFCH, 0C0H, 078H, 000H ; D_8A
0458	CC 00 00 70 30 30 30	241	DB	0CCH, 000H, 070H, 030H, 030H, 030H, 078H, 000H ; D_8B
0460	7E C6 38 18 18 18	242	DB	07CH, 0C6H, 038H, 018H, 018H, 018H, 03CH, 000H ; D_8C
0468	E0 00 00 70 30 30 30	243	DB	0E0H, 000H, 070H, 030H, 030H, 030H, 078H, 000H ; D_8D
0470	C6 38 6C C6 FE C6	244	DB	0C6H, 038H, 06CH, 0C6H, 0FEN, 0C6H, 0C6H, 000H ; D_8E
0478	30 30 00 78 CC FC	245	DB	030H, 030H, 000H, 078H, OCCH, OFCH, OCCH, 000H ; D_8F
0480	1C 00 FC 60 78 60	246	DB	01CH, 000H, 0FCH, 060H, 078H, 060H, 0FCH, 000H ; D_90
0488	FC 00 00 7F 0C 7F CC	247	DB	000H, 000H, 07FH, 00CH, 07FH, OCCH, 07FH, 000H ; D_91

0490	7F 00	309	DB	03EH, 06CH, 0CCH, 0FEH, 0CCH, 0CCH, 0CEH, 000H : D_92
	3E 6C CC FE CC CC	310		
	CE 00	311		
0498	78 CC 00	312	DB	078H, 0CCH, 000H, 078H, 0CCH, 0CCH, 078H, 000H : D_93
	78 CC 00	313		
04A0	00 00 00	314	DB	000H, 0CCH, 000H, 078H, 0CCH, 0CCH, 078H, 000H : D_94
	00 78 CC CC	315		
04A8	00 E0 00	316	DB	000H, 0E0H, 000H, 078H, 0CCH, 0CCH, 078H, 000H : D_95
	00 78 CC CC	317		
04B0	78 CC 00	318	DB	078H, 0CCH, 000H, 0CCH, 0CCH, 0CCH, 07EH, 000H : D_96
	00 CC CC CC	319		
04B8	00 E0 00	320	DB	000H, 0E0H, 000H, 0CCH, 0CCH, 0CCH, 07EH, 000H : D_97
	00 CC CC CC	321		
04C0	00 CC 00	322	DB	000H, 0CCH, 000H, 0CCH, 0CCH, 07CH, 00CH, 0F8H : D_98
	00 CC CC TC	323		
04C8	C3 18 3C 66 66 3C	324	DB	0C3H, 018H, 03CH, 066H, 066H, 03CH, 018H, 000H : D_99
	18 00	325		
04D0	CC 00 CC CC CC CC	326	DB	0CCH, 000H, 0CCH, 0CCH, 0CCH, 0CCH, 0CCH, 078H, 000H : D_9A
	00 00 00	327		
04D8	78 18 7E CO 00 7E	328	DB	018H, 018H, 07EH, 0C0H, 0C0H, 07EH, 018H, 018H : D_9B
	18 18	329		
04E0	38 6C 64 F0 60 E6	330	DB	038H, 06CH, 064H, 0F0H, 060H, 0E6H, 0FCH, 000H : D_9C
	FC 00	331		
04E8	CC 00 78 FC 30 FC	332	DB	0CCH, 0CCH, 078H, 0FCH, 030H, 0FCH, 030H, 030H : D_9D
	00 30	333		
04F0	F8 CC FA C6 CF	334	DB	0F8H, 0CCH, 0CCH, 0FAH, 0C6H, 0CFH, 0C6H, 0C7H : D_9E
	C6 C7	335		
04F8	0E 1B 18 3C 18 18	336	DB	00EH, 018H, 018H, 03CH, 018H, 018H, 0DBH, 070H : D_9F
	08 70	337		
0500	1C 00 00 78 OC 7C CC	338	DB	01CH, 000H, 078H, 0CCH, 07CH, 0CCH, 07EH, 000H : D_A0
	7E 00	339		
0508	58 00 70 30 30 30	340	DB	038H, 000H, 070H, 030H, 030H, 030H, 078H, 000H : D_A1
	30 30	341		
0510	00 1C 00 00 78 CC CC	342	DB	000H, 01CH, 000H, 078H, 0CCH, 0CCH, 078H, 000H : D_A2
	78 00	343		
0518	00 00 00 00 CC CC CC	344	DB	000H, 01CH, 000H, 0CCH, 0CCH, 0CCH, 07EH, 000H : D_A3
	00 00	345		
0520	00 F8 00 F8 CC CC	346	DB	000H, 0F8H, 000H, 0F8H, 0CCH, 0CCH, 0CCH, 000H : D_A4
	CC 00	347		
0528	FC 00 CC EC FC DC	348	DB	0FCH, 000H, 0CCH, 0ECH, 0FCH, 0CCH, 0CCH, 000H : D_A5
	00 00	349		
0530	3C 6C 6C 3E 00 7E	350	DB	03CH, 06CH, 06CH, 03EH, 000H, 07EH, 000H, 000H : D_A6
	00 00	351		
0538	38 6C 6C 38 00 7C	352	DB	038H, 06CH, 06CH, 038H, 000H, 07CH, 000H, 000H : D_A7
	30 00	353		
0540	30 00 30 60 CO CC	354	DB	030H, 000H, 030H, 060H, 0C0H, 0CCH, 078H, 000H : D_A8
	78 00	355		
0548	00 00 00 FC CO CO	356	DB	000H, 000H, 000H, 0FCH, 0C0H, 0C0H, 000H, 000H : D_A9
	00 00	357		
0550	00 00 00 00 FC 0C 0C	358	DB	000H, 000H, 000H, 000H, 0FCH, 0C0H, 0C0H, 000H, 000H : D_AA
	00 00	359		
0558	C3 6C CC DE 33 66	360	DB	0C3H, 06CH, 0CCH, 0DEH, 033H, 066H, 0CCH, 000H : D_AB
	C3 0F	361		
0560	C3 00 CC DB 37 6F	362	DB	0C3H, 06CH, 0CCH, 0DBH, 037H, 06FH, 0C0H, 003H : D_AC
	00 03	363		
0568	18 18 00 18 18 18	364	DB	018H, 018H, 000H, 018H, 018H, 018H, 018H, 000H : D_AD
	18 00	365		
0570	00 33 66 CC 66 33	366	DB	000H, 033H, 066H, 0CCH, 066H, 033H, 000H, 000H : D_AE
	00 00	367		
0578	00 CC 66 33 66 CC	368	DB	000H, 0CCH, 066H, 033H, 066H, 0CCH, 000H, 000H : D_AF
	00 00	369		
0580	22 88 22 88 22 88	370	DB	022H, 088H, 022H, 088H, 022H, 088H, 022H, 088H : D_B0
	22 88	371		
0588	55 AA 55 AA 55 AA	372	DB	055H, 0AAH, 055H, 0AAH, 055H, 0AAH, 055H, 0AAH : D_B1
	55 AA	373		
0590	00 00 DB EE DB 77	374	DB	0DBH, 077H, 0DBH, 0EEH, 0DBH, 077H, 0DBH, 0EEH : D_B2
	DB EE	375		
0598	18 18 18 18 18 18	376	DB	018H, 018H, 018H, 018H, 018H, 018H, 018H, 018H : D_B3
	18 18	377		
05A0	18 18 18 18 F8 18	378	DB	018H, 018H, 018H, 018H, 0F8H, 018H, 018H, 018H : D_B4
	18 18	379		
05A8	18 18 F8 18 F8 18	380	DB	018H, 018H, 0F8H, 018H, 0F8H, 018H, 018H, 018H : D_B5
	18 18	381		
05B0	36 36 36 36 F6 36	382	DB	036H, 036H, 036H, 036H, 06FH, 036H, 036H, 036H : D_B6
	36 36	383		
05B8	00 00 00 FE 36	384	DB	000H, 000H, 000H, 000H, 0FEH, 036H, 036H, 036H : D_B7
	36 36	385		
05C0	00 00 FB 18 FB 18	386	DB	000H, 000H, 0FBH, 018H, 0FBH, 018H, 018H, 018H : D_B8
	18 18	387		
05C8	00 00 F6 06 F6 36	388	DB	036H, 036H, 0F6H, 006H, 0F6H, 036H, 036H, 036H : D_B9
	36 36	389		
05D0	36 36 36 36 36 36	390	DB	036H, 036H, 036H, 036H, 036H, 036H, 036H, 036H : D_BA
	36 36	391		
05D8	00 00 FE 06 F6 36	392	DB	000H, 000H, 0FEH, 006H, 0F6H, 036H, 036H, 036H : D_BB
	36 36	393		
05E0	36 36 F6 06 FE 00	394	DB	036H, 036H, 0F6H, 006H, 0FEH, 000H, 000H, 000H : D_BC
	00 00	395		
05E8	36 36 36 36 FE 00	396	DB	036H, 036H, 036H, 036H, 0FEH, 000H, 000H, 000H : D_BD
	36 36	397		
05F0	18 18 FB 18 FB 00	398	DB	018H, 018H, 0F8H, 018H, 0F8H, 000H, 000H, 000H : D_BE
	00 00	399		
05F8	00 00 00 00 FE 18	400	DB	000H, 000H, 000H, 000H, 0FBH, 018H, 018H, 018H : D_BF
	18 18	401		
0600	18 18 18 18 1F 00	402		
	18 18	403		
0608	18 18 18 18 FF 00	404	DB	018H, 018H, 018H, 018H, 01FH, 000H, 000H, 000H, 000H : D_C0
	18 18	405		
0610	00 00 00 00 FF 18	406	DB	018H, 018H, 018H, 018H, 0FFH, 000H, 000H, 000H, 000H : D_C1
	18 18	407		
0618	18 18 18 18 1F 18	408	DB	018H, 018H, 018H, 018H, 01FH, 018H, 018H, 018H : D_C2
	18 18	409		
0620	00 00 00 00 FF 00	410	DB	000H, 000H, 000H, 000H, 0FFH, 000H, 000H, 000H, 000H : D_C4
	00 00	411		
0628	18 18 18 18 FF 18	412	DB	018H, 018H, 018H, 018H, 0FFH, 018H, 018H, 018H : D_C5
	18 18	413		
0630	18 18 1F 18 1F 18	414	DB	018H, 018H, 01FH, 018H, 01FH, 018H, 018H, 018H : D_C6
	18 18	415		
0638	36 36 36 37 36 36	416	DB	036H, 036H, 036H, 036H, 037H, 036H, 036H, 036H : D_C7
	36 36	417		
0640	00 00 37 30 3F 00	418	DB	036H, 036H, 037H, 030H, 03F, 000H, 000H, 000H : D_C8
	00 00	419		
0648	00 00 3F 30 37 36	420	DB	000H, 000H, 003F, 030H, 037H, 000H, 000H, 000H : D_C9
	36 36	421		
0650	36 36 F7 00 FF 00	422	DB	036H, 036H, 0F7H, 000H, 0FFH, 000H, 000H, 000H : D_CA
	36 36	423		
0658	00 00 FF 00 FF 36	424	DB	000H, 000H, 0FFH, 000H, 0FFH, 000H, 036H, 036H : D_CB
	36 36	425		
0660	36 36 37 30 37 36	426	DB	036H, 036H, 037H, 030H, 037H, 036H, 036H, 036H : D_CC
	36 36	427		
0668	00 00 FF 00 FF 00	428	DB	000H, 000H, 0FFH, 000H, 0FFH, 000H, 000H, 000H : D_CD
	00 00	429		
0670	36 36 FF 00 FF 36	430	DB	036H, 036H, 0FFH, 000H, 0FFH, 000H, 036H, 036H : D_CE
	36 36	431		
	00 00	432		
	00 00	433		
	36 36	434		

0678 18 18 FF 00 FF 00	435	DB	018H,018H,0FFH,000H,0FFH,000H,000H,000H ; D_CF
00 00	436	DB	036H,036H,036H,036H,0FFH,000H,000H,000H ; D_D0
0680 36 36 36 36 FF 00	438	DB	000H,000H,0FFH,000H,0FFH,018H,018H,018H ; D_D1
0688 00 00 FF 00 FF 18	439	DB	000H,000H,000H,000H,0FFH,036H,036H,036H ; D_D2
0690 00 00 00 00 FF 36	440	DB	036H,036H,036H,036H,03FH,000H,000H,000H ; D_D3
0698 36 36 36 36 3F 00	441	DB	018H,018H,01FH,018H,018H,01FH,000H,000H,000H ; D_D4
06A0 18 18 1F 18 00	442	DB	000H,000H,01FH,018H,018H,01FH,018H,018H,018H ; D_D5
06A8 00 00 1F 18 1F 18	443	DB	000H,000H,000H,000H,03FH,036H,036H,036H ; D_D6
06B0 18 18 00 00 00 3F 36	444	DB	036H,036H,036H,036H,0FFH,036H,036H,036H ; D_D7
06B8 36 36 36 36 FF 36	451	DB	018H,018H,0FFH,018H,018H,018H,018H,018H,018H ; D_D8
06C0 18 18 FF 18 1F 18	452	DB	018H,018H,018H,018H,018H,018H,000H,000H,000H ; D_D9
06C8 18 18 18 18 F8 00	453	DB	000H,000H,000H,000H,01FH,018H,018H,018H,018H ; D_DA
06D0 00 00 00 00 00 1F 18	454	DB	0FFH,0FFH,0FFH,0FFH,0FFH,0FFH,0FFH,0FFH,0FFH ; D_DB
06D8 FF FF FF FF FF FF	455	DB	000H,000H,000H,000H,0FFH,0FFH,0FFH,0FFH,0FFH ; D_DC
06E0 00 00 00 00 00 FF 00	456	DB	0FFH,0FOH,0FOH,0FOH,0FOH,0FOH,0FOH,0FOH,0FOH ; D_DD
06E8 FO FO FO FO FO	457	DB	00FH,00FH,00FH,00FH,00FH,00FH,00FH,00FH,00FH ; D_DE
06F0 FO OF OF OF OF	458	DB	0FFH,0FFH,0FFH,0FFH,0FFH,0FFH,0FFH,0FFH,0FFH ; D_DF
06F8 FO FF FF FF 00 00	459	DB	000H,000H,000H,000H,000H,000H,000H,000H,000H ; D_E0
0700 00 00 76 DC C8 DC	470	DB	000H,000H,076H,0DCH,0C8H,0DCH,076H,000H ; D_E1
0708 00 00 00 00 FF C8	471	DB	000H,078H,0CCH,0FBH,0CCH,0FBH,0C0H,0C0H ; D_E2
00 00 C0 C0 C0 C0	472	DB	000H,0CCH,0CCH,0C0H,0C0H,0C0H,0C0H,0C0H,0C0H ; D_E3
0710 00 00 00 00 00 00	473	DB	0FCH,0CCH,0D0H,030H,060H,0CCH,0FCH,000H ; D_E4
0718 00 00 6C 6C 6C 6C	474	DB	000H,000H,07EH,0D8H,0D8H,0D8H,070H,000H ; D_E5
00 00 6C 6C 6C 6C	475	DB	000H,066H,066H,066H,066H,07CH,060H,0C0H ; D_E6
0720 FC CC 60 30 60 CC	476	DB	000H,076H,0DCH,018H,018H,018H,018H,000H ; D_E7
FC 00 00 00 00 00 00	477	DB	0FCH,030H,078H,0CCH,0CCH,078H,030H,0FCH ; D_E8
0728 00 00 7E DB D8 D8	478	DB	038H,06CH,0C6H,0FEH,0C6H,06CH,038H,000H ; D_E9
70 00 00 00 00 00 00	479	DB	038H,06CH,0C6H,0C6H,0C6H,06CH,06CH,000H ; D_EA
0730 00 66 66 66 66 7C	480	DB	01CH,030H,018H,07CH,0CCH,0CCH,078H,000H ; D_EB
60 C0 00 00 00 00 00	481	DB	000H,000H,07EH,0DBH,0DBH,07EH,000H,000H ; D_EC
0738 00 76 DC 18 18 18	482	DB	006H,00CH,07EH,0DBH,0DBH,07EH,060H,0C0H ; D_ED
0740 FC 30 78 CC CC 78	483	DB	038H,060H,0C0H,0FBH,0C0H,060H,038H,000H ; D_EE
30 FC 00 00 00 00 00	484	DB	078H,0CCH,0CCH,0CCH,0CCH,0CCH,0CCH,000H ; D_EF
0748 38 6C C6 FE C6 6C	485	DB	000H,0FCH,000H,0FCH,000H,0FCH,000H,000H ; D_F0
38 00 00 00 00 00 00	486	DB	030H,030H,0FCH,030H,030H,000H,0FCH,000H ; D_F1
0750 1C C6 C6 C6 6C	487	DB	060H,030H,018H,030H,060H,000H,0FCH,000H ; D_F2
EE 00 00 00 00 00 00	488	DB	018H,030H,060H,030H,018H,000H,0FCH,000H ; D_F3
0758 1C 30 18 7C CC C8	489	DB	00EH,018H,018H,018H,018H,018H,018H,018H ; D_F4
78 00 00 00 00 00 00	490	DB	018H,018H,018H,018H,018H,018H,018H,018H ; D_F5
0760 00 00 7E DB D8 7E	491	DB	030H,030H,000H,0FCH,000H,030H,030H,000H ; D_F6
00 00 00 00 00 00 00	492	DB	000H,076H,0DCH,000H,076H,0DCH,000H,000H ; D_F7
0768 00 06 0C DB 07 DC	493	DB	038H,06CH,0C6H,038H,000H,000H,000H,000H ; D_F8
60 C0 00 00 00 00 00	494	DB	000H,000H,000H,018H,018H,000H,000H,000H ; D_F9
0770 38 60 C0 F8 C0 60	495	DB	000H,000H,000H,018H,018H,000H,000H,000H ; D_FA
38 60 C0 F8 C0 60 00	496	DB	000H,000H,000H,018H,018H,000H,000H,000H ; D_FA
0778 78 CC CC CC CC CC	497	DB	000H,000H,000H,000H,000H,000H,000H,000H ; D_FA
CC 00 00 00 00 00 00	498	DB	000H,000H,000H,000H,000H,000H,000H,000H ; D_FA
0780 00 FC 00 FC 00 FC	499	DB	000H,000H,000H,000H,000H,000H,000H,000H ; D_FA
00 00 00 00 00 00 00	500	DB	000H,000H,000H,000H,000H,000H,000H,000H ; D_FA
0788 30 30 FC 30 30 00	501	DB	000H,000H,000H,000H,000H,000H,000H,000H ; D_FA
FC 00 00 00 00 00 00	502	DB	000H,000H,000H,000H,000H,000H,000H,000H ; D_FA
0790 60 60 18 30 60 00	503	DB	000H,000H,000H,000H,000H,000H,000H,000H ; D_FA
FC 00 00 00 00 00 00	504	DB	000H,000H,000H,000H,000H,000H,000H,000H ; D_FA
0798 18 30 60 18 00 18	505	DB	000H,000H,000H,000H,000H,000H,000H,000H ; D_FA
FC 00 00 00 00 00 00	506	DB	000H,000H,000H,000H,000H,000H,000H,000H ; D_FA
07A0 0E 18 1B 18 18 18	507	DB	000H,000H,000H,000H,000H,000H,000H,000H ; D_FA
07A8 18 18 18 18 18 0B	508	DB	000H,000H,000H,000H,000H,000H,000H,000H ; D_FA
D8 00 00 00 00 00 00	509	DB	000H,000H,000H,000H,000H,000H,000H,000H ; D_FA
07B0 30 30 00 FC 00 30	510	DB	000H,000H,000H,000H,000H,000H,000H,000H ; D_FA
30 30 00 00 00 00 00	511	DB	000H,000H,000H,000H,000H,000H,000H,000H ; D_FA
07B8 00 76 DC 00 76 DC	512	DB	000H,000H,000H,000H,000H,000H,000H,000H ; D_FA
00 00 00 00 00 00 00	513	DB	000H,000H,000H,000H,000H,000H,000H,000H ; D_FA
07C0 38 6C 6C 38 00 00	514	DB	000H,000H,000H,000H,000H,000H,000H,000H ; D_FA
07C8 00 00 00 18 18 00	515	DB	000H,000H,000H,000H,000H,000H,000H,000H ; D_FA
07D0 00 00 00 00 00 18 00	516	DB	000H,000H,000H,000H,000H,000H,000H,000H ; D_FA
07D8 00 00 0C 0C EC 6C	517	DB	000H,000H,000H,000H,000H,000H,000H,000H ; D_FA
3C 00 00 00 00 00 00	518	DB	078H,06CH,06CH,06CH,06CH,06CH,06CH,000H,000H ; D_FC
07E0 78 6C 6C 6C 6C 00	519	DB	070H,018H,030H,060H,078H,000H,000H,000H ; D_FD
00 00 00 00 00 00 00	520	DB	000H,000H,000H,000H,000H,000H,000H,000H ; D_FA
07E8 70 18 30 60 78 00	521	DB	000H,000H,000H,000H,000H,000H,000H,000H ; D_FA
70 18 30 60 78 00 00	522	DB	000H,000H,000H,000H,000H,000H,000H,000H ; D_FA
07F0 00 00 3C 3C 3C 3C	523	DB	000H,000H,000H,000H,000H,000H,000H,000H ; D_FA
00 00 00 00 00 00 00	524	DB	000H,000H,000H,000H,000H,000H,000H,000H ; D_FA
07F8 00 00 00 00 00 00 00	525	DB	000H,000H,000H,000H,000H,000H,000H,000H ; D_FA
00 00 00 00 00 00 00	526	DB	000H,000H,000H,000H,000H,000H,000H,000H ; D_FA
0800 00 00 00 00 00 00	527	DB	000H,000H,000H,000H,000H,000H,000H,000H ; D_FA
00 00 00 00 00 00 00	528	DB	000H,000H,000H,000H,000H,000H,000H,000H ; D_FA
00 00 00 00 00 00 00	529	DB	000H,000H,000H,000H,000H,000H,000H,000H ; D_FA
00 00 00 00 00 00 00	530	DB	000H,000H,000H,000H,000H,000H,000H,000H ; D_FA
00 00 00 00 00 00 00	531	DB	000H,000H,000H,000H,000H,000H,000H,000H ; D_FA
00 00 00 00 00 00 00	532	DB	000H,000H,000H,000H,000H,000H,000H,000H ; D_FA
00 00 00 00 00 00 00	533	DB	000H,000H,000H,000H,000H,000H,000H,000H ; D_FA
00 00 00 00 00 00 00	534	DB	000H,000H,000H,000H,000H,000H,000H,000H ; D_FA
00 00 00 00 00 00 00	535	DB	000H,000H,000H,000H,000H,000H,000H,000H ; D_FA
00 00 00 00 00 00 00	536	DB	000H,000H,000H,000H,000H,000H,000H,000H ; D_FA
00 00 00 00 00 00 00	537	DB	000H,000H,000H,000H,000H,000H,000H,000H ; D_FA

PAGE 120
 SUBTTL END ADDRESS
 PUBLIC
 END_ADDRESS
 LABEL BYTE
 CODE ENDS
 END

Index

A

Attribute Address Register 56
Attribute Controller
 description 3
 registers 56

B

BIOS
 description 4
 vectors with special
 meanings 103
BIOS listing 103
Bit Mask Register 54

C

character generator
 ROM 1
Character Map Select
 Register 21
Clocking Mode Register 19
Color Compare Register 48
Color Don't Care Register 53
color mapping 10
Color Plane Enable
 Register 60

compatibility issues 74
configuration switches 80
CRT Controller
 description 3
 registers 24
CRT Controller Address
 Register 24
CRT Controller Overflow
 Register 30
Cursor End Register 33
Cursor Location High
 Register 35
Cursor Location Low
 Register 35
Cursor Start Register 32

D

Data Rotate Register 49
direct drive connector 83
display buffer 4

E

Enable Set/Reset Register 47
End Horizontal Blanking
 Register 27
End Horizontal Retrace
 Register 29

End Vertical Blanking
Register 40

F

feature connector 76
Feature Control Register 14

G

Graphics Controller
description 3
registers 45
Graphics 1 and 2 Address
Register 46
Graphics 1 Position
Register 45
Graphics 2 Position
Register 46

H

Horizontal Display Enable End
Register 26
Horizontal Pel Panning
Register 60
Horizontal Total Register 25

I

Input Status Register One 15
Input Status Register Zero 14
Interface 76
feature connector 76

L

Light Pen High Register 36
light pen interface 84
Light Pen Low Register 37
Line Compare Register 43

M

Map Mask Register 20
Maximum Scan Line
Register 32
Memory Mode Register 23
Miscellaneous Output
Register 12
Miscellaneous Register 52
Mode Control Register 41, 58
Mode Register 50
modes
 alphanumeric 8
 graphics 8
 IBM Color Display 5
 IBM Enhanced Color
 Display 6
 IBM Monochrome
 Display 6

O

Offset Register 38
Overscan Color Register 59

P

Palette Registers 57
Preset Row Scan Register 31
programming
 considerations 62
 compatibility issues 74
 creating a split screen 73
 creating a 512 character set 70
 creating an 80 by 43
 alphanumeric mode 71
 programming registers 62
RAM loadable character generator 69
vertical interrupt feature 72

R

RAM loadable character generator 69
Read Map Select Register 50
registers
 Attribute Controller 56
 CRT Controller 24
 external 12

Graphics Controller 45
Sequencer 18
Reset Register 18

S

Sequencer
 description 3
 registers 18
Sequencer Address Register 18
Set/Reset Register 47
specifications 79
 configuration switch
 settings 81
 configuration switches 80
 direct drive connector 83
 light pen interface 84
 system board switches 79
Start Address High Register 34
Start Address Low Register 34
Start Horizontal Blanking Register 26
Start Horizontal Retrace Pulse Register 28
Start Vertical Blanking Register 39
support logic 4

U

Underline Location Register 39

V

Vertical Display Enable End
Register 38
vertical interrupt feature 72

Vertical Retrace End
Register 36
Vertical Retrace Start
Register 36
Vertical Total Register 30

(

(

(

)

)

)