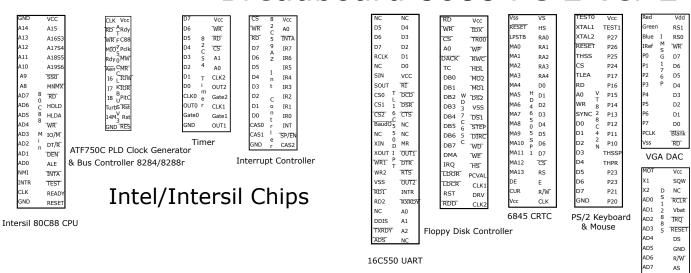
Breadboard 8088 PC 2 Ver 2.0



Memory Chips RAM & EEPROM

PLD Programmable Logic Devices

MoteA DSB

DSA

ator

STEP

Whata WGate

WP RDD

SIDE

TOW AU TA19 RtA A4 2RTW A5 2RTR

A6 _VIO4 A7 1IO5 A8 CHOW A9 ITTO A102TDE A1AvIrq8 GND³Irq8 DSKCHO IDE Vc AO AIAO A1 FIA1

A2 ²IA2 A3 _LCS1

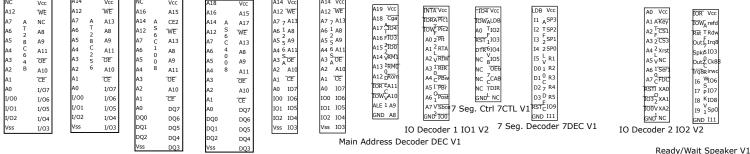
TORVCS3
TOWGucba
17 Cucat
18 DUOE
19 ELOE

GND I11

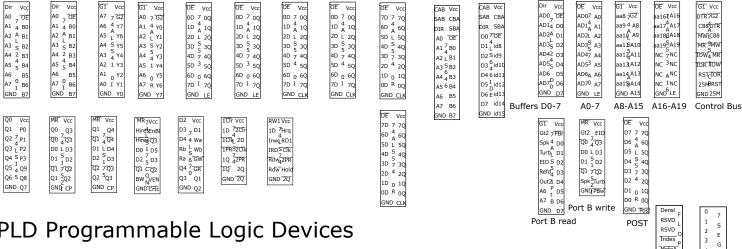
B C

D

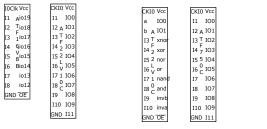
E (F)



74 Series Logic Chips (0I indicate zero indexed)



PLD Programmable Logic Devices



Other Chips and components										IO19 Vcc IOR Crts IOW 645	Out1 Vcc Rst Arefd T SpkrFIO1	CLK Vcc Rst _A DW TORTABR	AT7 Vcc AT6 NoD AT5 , U NL	Out1 Vcc Rst Arefd T Spkr-dmc	TOR Vcc TOWA AO TATERTA
PBRMVcc TD CST TOL2 RST GNI3 RST	E 1 D 2 DP 3 C 4 G 5 D4 6	O S L 4 0 5 6 2	12 D1 11 A 10 F 9 D2 8 D3 7 B	E 1 D 2 DP 3 C 4 G 5 D4 6	O S L 4 0 5 6	12 D1 11 A 10 F 9 D2 8 D3 7 B	0B 0C 1B 1C 2B U2C 3B N3C 4B 2 4C 5B 0 5C 6B 3 6C	NC UNC PAO L G PA5 2 F PA6 8 E PA4 3 D PA2 A C PA1 5 B	des Fwbo den 2 rdo rdi Lwdo wdi VIO4 16 0105 17 CIO6 18 MO7 19 AIO8	A0 F CFtc A1 2 FDC A2 V PRD A3 1 PWR A4 I Key A5 O A11 A6 V A10	Out2 ² IO2 A0 L ¹ IO3 A1 VIO4 A2 OLDER I7 CLDOR I8 BFDCS FDC1 TC	TOW ₂ AEN 14 2RDD 15 LAB1 Lock Rdm S0 Den W S1 DRw1 Hrq MIrwo	AT4F RVV AT3 ² ADT AT2V VID AT1 ¹ P4 AT0 _A P3 VEN ^T P2 BW M P1	Out2 ² ddir 2 A0 LDt/r A1 VDen A2 OLder TORCLdor MMR _B Fdcs FDC1 TC	A4 2REW A5 2RFR A6 VRtcs A7 1105 A8 C106 A9 1107 A100108
1NCM8Vcc	1NCM8V	'c¢	1NCM8Vo	1NCM8	Vcc.	1NCM8Vcc	7B 7C GND COM	PA3 e A GND ^G COM	I10 O I09	A7 3 A9 GND A8	TC VSpO GND2I11	Hold RD1 GND2 Rdw	BIEN PO GND G7W	TC VSpO GND3I11	A1AvIrq8 GND ² Irq8