Breadboard 8088 PC 2 Ver 1.0

PSTB RA0

RA2

RA3

D0

D2

D4 D5

R /W

CLK

6845 CRTC

S P D6 1 D7

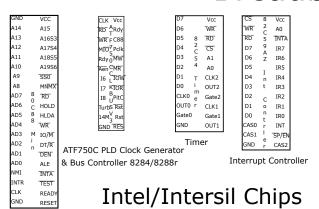
MA1

маз

MA10

MA11

MA13 RS



Intersil 80C88 CPU

IC	NC		ŔĎ	Vcc
5	D4		WR	TDX
6	D3		CS CS	TR00
7	D2		A0	WP
CLK	D1		DACK	RWC
IC	D0		TC	THDL
IN	VCC		DB0	MO2
OUT	RT		DB1	MO1
:S0 T	DCD		DB2 W	7057
	DSR		DB3 3 DB4 7 DB5 6	VSS
S2 6 aud0 5	CTS		DB4 7	DS1
			DB5 C	STEP
IC 5	NC		DRP 2	DIRC
IN D	MR		DB7 C	WD
	OUT1		DMA	WE
7RT P T	DTR		IRQ	THS"
VR2	RTS		LDOR	PCVAL
'SS	OUT2		LDCR	CLK1
DI	INTR		RST	DRV
.D2	RXRDY		RDD	CLK2
IC	A0	l '		
DIS	A1			
XRDY	A2	Flop	py Disl	c Contro
DS	NC	Ι ΄		

oller

1	TEST0	Vcc	Red		Vdd
	XTAL1	TEST1	Green		RS1
	XTAL2	P27	Blue	I	RS0
i	RESET	P26	IRef	M S	WR
	THSS	P25	P0	G	D7
	CS	P24	P1	7	D6
	TLEA	P17	P2	6	D5
	RD	P16	P3	Р	D4
	A0 V	P15	P4		D3
	WR 8	P14	P5		D2
	SYNC 2	P13	P6		D1
	DO C	P12	P7		D0
	D1 4	P11	PCLK		Blank
	D2 2 N	P10	Vss		RD
	D3	THSSP	VG	ΔΓ	DAC
	D4	THPR		~ L	AC
	D5	P23	MOT		Vcc
	D6	P23	X1		SQW
	D7	P21	X2	D S	NC
	GND	P20	AD0	5	RCLR

Vcc SQW RCLR AD1 Vbat TRQ AD3 RESE DS \n5 AD6 R/W AS CS

Realtime Clock PLD Programmable Logic Devices

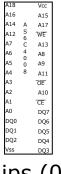
Memory Chips RAM & EEPROM

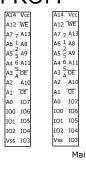
				-		
NC		Vcc	1	A14		Vcc
A12		WE		A12		WE
A7	Α	NC		Α7	Α	A13
A6	T 2	A8		A6	T 2	A8
A5	8	Α9		A5	8	A9
A4	C 6	A11		Α4	C 2 5	A11
А3	4	OE		А3	5	OE
A2	В	A10		A2	6	A10
A1		CE		A1		CE
A0		I/07		A0		I/07
I/00		I/06		I/00		I/06
I/01		I/05		I/01		I/05
I/02		I/04		I/O2		I/04
Vss		I/03		Vss		I/03
_			•			

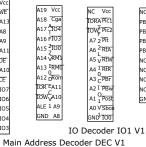


A0 7 G2

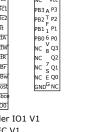
A1 4 Y0 A2 L Y1 A3 S Y2 A4 4 Y3 A5 1 Y4

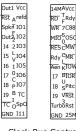






16C550 UART





PS/2 Keyboard

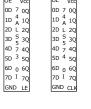
Clock Bus Controller CLKBUS V3

74 Series Logic Chips (0I indicate zero indexed)



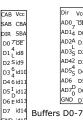








SAB	CBA
DIR	SBA
A0 '	Œ
A1 7	B0
A2 L	В1
A3 S	B2
A4 4	ВЗ
A5 6	В4
A6	B5
A7	B6
GND	В7



ADO₇ TOE AD1⁴ A1 AD2L A2 AD1/ DI AD2A D1 AD3_S D2 AD42 D3 AD3^S A3 AD47 A4 AD5⁴ D4 AD5³ A5 AD6 D: AD6_A A6 AD7⁰ A7 GND LE A0-7

DTR7G2 aa167A1 aa17⁴A1 aa18<u>B</u>A1 C88ATR MW[C88 aa9 4 A8 aa10 A9 aa19^SA1 NC 7NC NC ³NC aa115A10 aa12<mark>5</mark>A1 MR SMW aa131A1 IOR TOV aa14A1 aa158A1 NC ANC RSTCTOR 25MBRS GND A15 A8-A15 A16-A19 Control Bus

Q1 P0 Q0 Q3 Q0 4 Q3 Q2 7 P1 Q3 L P2 Q4 S P3 D0 L D3
D1 S D2
Q1 7Q2
Q1 5Q2
GNQ CP Q5 ¹₄ Q9 Q6 5 Q8

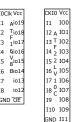


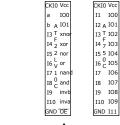


DZ VCC	TCIL	VCC
D3 ₇ D1	1D 7	2Clr
D4 4 Wa	1CIk	2D
Rb C Wb	1PRS	2Clk
Ra 6 GW	1Q 4	2PR
Q4 7-GR	1Q	2Q
Q3 Q1	GND	2Q
GND Q2		

RW1 Vcc
1D 7Hrq
Irwq RD1
IRDSCIK
Rdw ₄ 2PR
Rdw Hold
GND ZQ

PLD Programmable Logic Devices







SIDE

DSKCHO IDE Vc

AO ^AIAO A1 FIA1 A2 ²IA2 A3 _LCS1

IORYCS3
IOW ucba
I7 Cucat
I8 DUOE
I9 ETOE



Other Chips and components

PBR _{MVcc}	E 1	0	12 D
TD CST	D 2	S	11 A
TOL2 RST	DP 3	4	10 F
GND RST	C 4	0 5	9 D2
CHIZICH	G 5	6	8 D3
	D4 6	2	7 B







CM8Vcc 145 4 4 5 H Ds Out	1NCM8Vcc 14.3 0 MHz 4 5 H GNDsOut

11 A

10 F 9 D2 8 D3

0B	0C	NC	UNC
1B	1C	PA0	L N G
2B	L 2C	PA5	2 F
	N 3C	PA6	8 E
	2 8 4C	PA4	3 D
	0 5C	PA2	A C
6B	3 6C	PA1	ςΒ
7B	7C	PA3	
GNI	COM	GNE	cor

rbi Vcc	IO19 Vcc
wbi A rbo	
dcs F wbo	10W ² 645
den ² rdo	
rdi Lwdo	A1 2 FDC
wdiVIO4	A2 V PRE
16 0105	A3 1 7FW
17 CIO6	A4 I Key
18 MIO7	A5 O A11
19 AIO8	A6 V A10
110 IO9	A7 3 A9
GND I11	GND A8

с	Out1 Vcc	CLK Vcc	AT7 Vcc
s	Rst Arefd	Rst _A DW	AT6 NoD
5	SpkrFI01	TORTABR	AT5 ^A UNL
īc	Out2 ² IO2	TOW AEN	AT4F RVV
đ	A0 LIO3	I4 2RDD	AT32 ADT
Б	A1 VIO4	I5 VAB1	AT2V VID
7F	A2 0Ldcr	Locki Rdm	AT1 ¹ P4
У	17 CLdor	S0 Den	ATO _A P3
1	I8 BFdcs	S1 DRw1	VEN ^T P2
0	FDC1 TC	Hrq MIrwc	BW _M P1
9	TC VSpO	Hold RD1	BIEN PO
3	GND ₂ I11	GND ² Rdw	GND ² G7W

	MIT VCC
Rst ADW	AT6 NoD
TORTABR	
TOW AEN	AT4F RVV
I4 2 RDE	AT3 ² ADT
I5 LAB1	AT2V VID
Locks Rdm	
S0 Den	AT0∆ P3
S1 DRw1	VEN ^T P2
Hrq MIrwo	BW _M P1
HoldyRD1	BIEN PO
GND ² Rdw	GNB G7W

Out1 Vcc	TOR Vcc	TOR Vcc
Rst Arefd	TOWA AO	TOWA AO
SpkrFdmc	TATO RtA	TA19RtA
Out2 ² ddir	A4 2REW	A4 2RtW
A0 Dt/r	A5 2RFR	A5 ZRFR
A1 VDen	A6 VRtcs	A6 VIO4
A2 oLdcr	A7 1IO5	A7 1IO5
TORCLdor	A8 CIO6	A8 CITOW
MMR _R Fdcs	A9 I IO7	A9 I IIor
FDC1 TC	A100108	A100TDE
TC USpO	A1AvIrq8	A1AVIrq8
GND3I11	GND ² Irq8	GND ³ Irq8