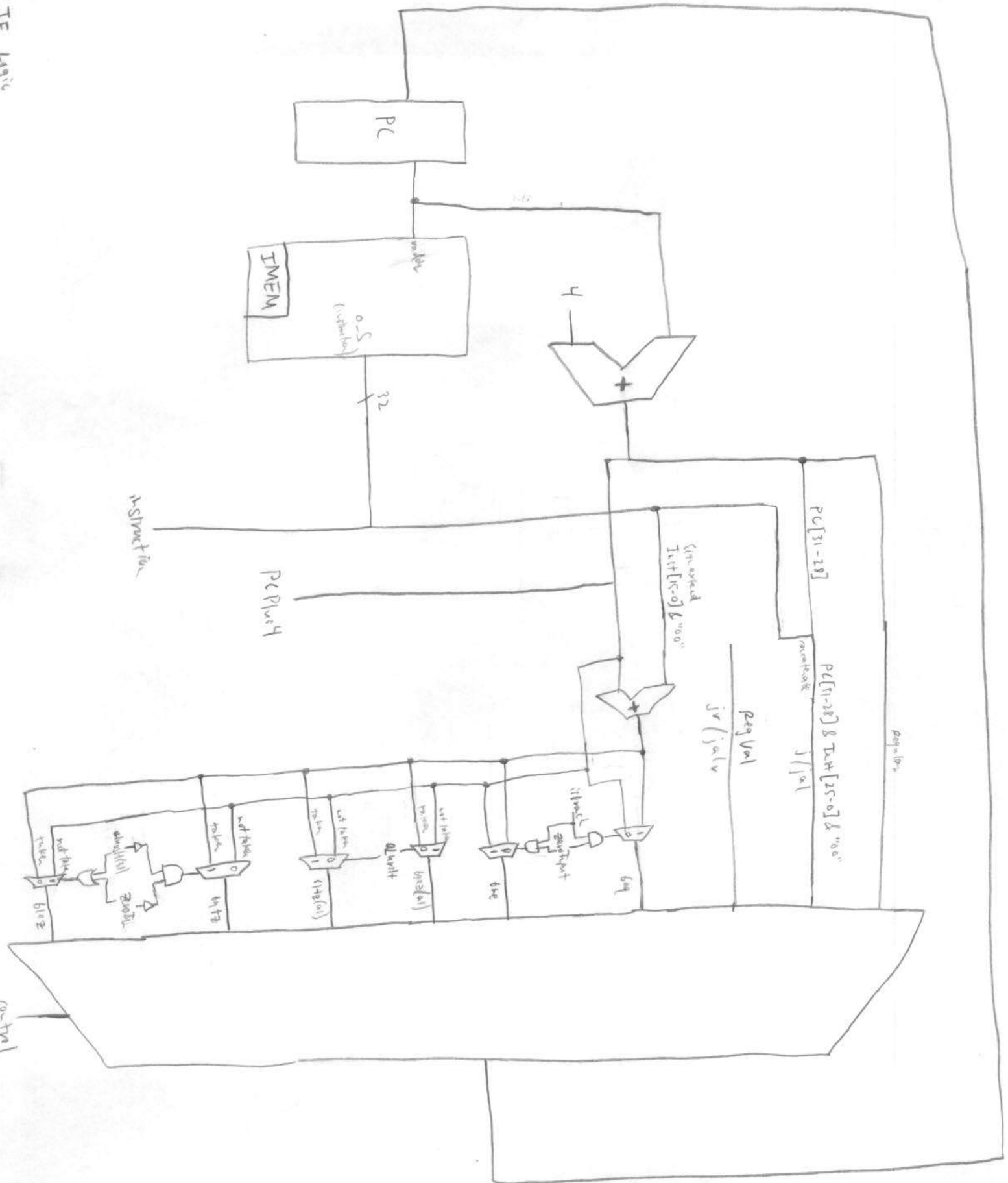


IF Logic





- ① S-Reg Value
- ② S-ALU zero
- ③ S-ALU Result
- ④ S-dmem-op
- ⑤ S-forward
- ⑥ S-byte enable
- ⑦ S-forward
- ⑧ S-RF-op2
- ⑨ S-ALU B
- ⑩ S-stall
- ⑪ S-shift-op
- ⑫ S-mult-op
- ⑬ S-selend-op
- ⑭ S-finalmax-op
- ⑮ S-RFdata
- ⑯ S-wrRegIn
- ⑰ S-Dataval BS