12/5/23, 8:14 PM Quiz 10

① Students have either already taken or started taking this quiz, so take care when editing it. If you change any quiz questions in a significant way, you might want to consider re-grading students' quizzes who took the old version of the quiz. Points 7 Published **Details** Questions ✓ Show question details **Question** 1 pts Physical frames are mapped to virtual pages in RAM. True ıswer False **Question** 1 pts What specific problem with segmentation does paging solve? ıswer External fragmentation and Internal Fragmentation Access Times Stale memory accesses Contiguous memory access **Question** 1 pts You are given the virtual address (VA) = 30 (binary: 11110), assuming that the 3 least significant bits are used for offset, what will be the value of the VPN in binary. ıswer 0 11 01 0 10 00 Cannot be determined. **Question** 1 pts You are given the virtual address (VA) = 30 (binary: 11110), assuming that the 4 least significant bits are used for offset, what will be the value of the PFN in binary. Cannot be determined.

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	O 111	
	O 011	
-	O 01	
	Question	1 pts
::	Question	1 pts
	Assuming that the TLB access pattern gives the following sequence of hits (H) and misses (M), MHHHMHHHMHHH What will be the hit rate for this TLB?	
wer	O 75%	
-	O 50%	
	O 65%	
	Cannot be determined.	
	Question	1 pts
	Why is there a possibility of containing two or more TLB entries with the same VPN value?	
ver	Because TLB could be shared, multiple processes might have the same VPN value which might be mapped to a different PFN value.	
	O No this statement is wrong. There is no such possibility.	
	O No the possibility is for same PFN and not the same VPN in one or more TLB entries.	
	O None of the given options.	
	Question	1 pts
	TLB is a flawless cache that does not perform poorly in any situation?	⊗ ×
	○ True	
wer	○ False	
	+ New question + New question group Q Find questions	
	Notify users this quiz has changed	
□ N	today discrist this quiz has ortanged	