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Question 1 pts

To ensure cache coherence means to:

- ensure that all processors in a multiprocessor system have the same view of memory
- store data, that is frequently accessed, in a cache
- to write data to main memory immediately after it is changed in the cache
- ensure that a single cache is used by all processors in a multi-processor system

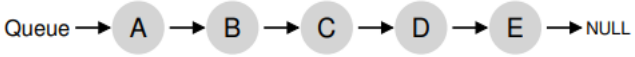
Question 1 pts

Which of the following is a way to ensure cache coherence?

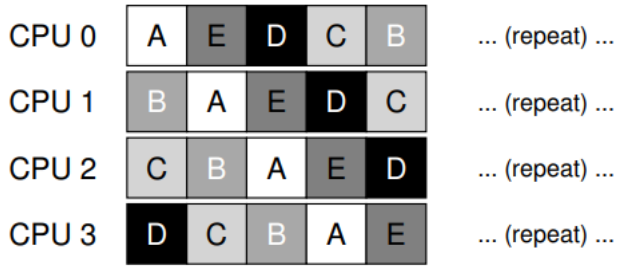
- Bus snooping
- Cache affinity
- Locking
- single-queue multi-processor scheduling

Question 1 pts

Suppose a single-queue multi-processor scheduler (SQMS) has five jobs in its queue:



to be scheduled for four processors. The scheduler ends up running the jobs as follows:



What is the problem with this SQMS scheduler?

- The scheduler is not taking into account cache affinity in its scheduling decisions
- The scheduler is running all jobs in round robin without prioritizing shorter jobs
- The scheduler is creating a "load imbalance"
- The scheduler has good throughput but a bad response time

Question 1 pts

Which of the following is NOT true about a multi-queue multi-processor scheduler (MQMS)?

- MQMS always performs better than SQMS (single-queue multi-processor scheduler)
- MQMS avoids the problem of locking and cache contention
- MQMS is inherently able to prioritize cache affinity
- MQMS can sometimes lead to load imbalance

Question 1 pts

Which of the following is NOT true about the address space abstraction for memory?

- It ensures that data addresses used by the program are the same as the physical addresses of the data
- It allows multiple programs to run concurrently without interfering with each other's memory
- It allows the operating system to manage memory more efficiently
- It makes it easier for programmers to write code

Question 1 pts

For the following code segment:

```
#include <stdio.h>
#include <stdlin.h>

int g = 10;

int main(int argc, char** argv)
{
    int x = 3;
    int* p = (int *)malloc(sizeof(int));
    return 0;
}
```

Which of the following is true?

- (1) code segment location: main, (2) data segment location: &g, (3) location of heap: p, (4) location of stack: &x
- (1) code segment location: &g, (2) data segment location: main, (3) location of heap: p, (4) location of stack: &x
- (1) code segment location: main, (2) data segment location: &g, (3) location of heap: &x, (4) location of stack: p
- (1) code segment location: &g, (2) data segment location: main, (3) location of heap: &x, (4) location of stack: p

Question 1 pts

Which of the following is NOT a valid use case for the pipe() system call?

- To create a communication channel between two unrelated processes
- To communicate between two related processes
- To implement a producer-consumer pattern
- To implement a command pipeline

Question 1 pts

Which of the following is the correct way to read data from a named pipe with the name /tmp/myfifo?

- read("/tmp/myfifo", buffer, sizeof(buffer))
- open("/tmp/myfifo", buffer, sizeof(buffer))
- open("/tmp/myfifo", O_RDONLY)
- read("/tmp/myfifo", O_RDONLY)

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