12/5/23, 8:24 PM Quiz 12

① Students have either already taken or started taking this quiz, so take care when editing it. If you change any quiz questions in a significant way, you might want to consider re-grading students' quizzes who took the old version of the quiz. Points 6 Published **Details** Questions ✓ Show question details **Question** 1 pts Which of the following is typically found in a Page Table Entry (PTE)? Virtual address Physical addres Page status bits ıswer All of the above **Question** 1 pts If the page size is 4 KB and the virtual address space is 32 bits, how many entries are needed in the page table? 2^10 ıswer 2^20 2^22 2^10 **Question** 1 pts How does increasing the size of the TLB impact the TLB miss rate? Reduces the TLB miss penalty Increases the TLB miss rate Has no effect on the TLB miss rate ıswer O Decreases the TLB miss rate

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ii.	Question	1 pts
V	Which TLB replacement policy aims to retain entries that are used most frequently?	
-	FIFO LRU Random Replacement	
	○ MRU	
::	Question	1 pts
V	Vhat is the function of address space ID in a TLB entry?	
	O Identify the process ID	
	Identify the page table entry	
	Identify the virtual address	
	Identify the physical page frame number	
::	Question	1 pts
٧	Which of the following statements is true about mutexes in Linux?	
	Mutexes are used to protect shared resources from concurrent access.	
	Mutexes can be nested, meaning that a mutex can be locked while another mutex is already locked.	
	Mutexes are implemented using spinlocks, which are a type of lock that keeps the CPU busy until the lock can be acquired.	
	Mutexes are non-reentrant, meaning that a mutex cannot be locked by the same thread more than once at a time.	
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