												Fong
	Reference	VIL Min (V)	VIL Max (V)	VIH Min (V)	VIH Max (V)	VOL Max (V)	VOH Min (V)	IOL Max (mA)	IOH Min (mA)			
	LVCMOS33	-0.3	0.8	2	3.45	0.4	VCCO - 0.4 = 2.9	12	12			
	S29AL008J	-0.1	0.8	0.7 * VCC = 1.89	VCC + 0.3 = 2.4	0.45	0.85 * VCC = 2.295	4	-2			
	CY7C1049G	-0.3	0.8	2	VCC + 0.3 = 2.4	0.4	2.4	8	-4			
in V	Reference	GND	VOL, max	0' Noise Margin	VIL, max	Vt	VIH, min	1' Noise Margin	VOH, min	VCC	VCCO	
	LVCMOS33	0	0.4	**0.4**	0.8	1.5	2	**0.9**	2.9	2.7	3.3	
	S29AL008J	0	0.45	**0.35**	0.8	1.5	1.89	**0.405**	2.295	2.7	3.3	
	CY7C1049G	0	0.4	**0.4**	0.8	1.5	2	**0.4**	2.4	2.7	3.3	
			Logic Zero Case			Logic One Case						
	Output	Input	VIL, max	VOL, max	0' Noise Margin	VOH, min	VIH, min	1' Noise Margin				
	S29AL008J	LVCMOS33	0.8	0.4	0.8 - 0.4 = **0.4**	2.295	2	2.295 - 2 = **0.295**				
	LVCMOS33	S29AL008J	0.8	0.4	0.8 - 0.4 = **0.4**	2.9	1.89	2.9 - 1.89 = **1.01**				
	CY7C1049G	LVCMOS33	0.8	0.4	0.8 - 0.4 = **0.4**	2.4	2	2.4 - 2 = **0.4**				
	LVCMOS33	CY7C1049G	0.8	0.45	0.8 - 0.45 = **0.35**	2.9	2	2.9 - 2 = **0.9**				
Power Supply for	Supply	Circuits	Device	Current (max/typical)								
Artix-7	3.3V	FPGA I/O, USB ports	IC10: LTC3633	2A/0.1 to 1.5A								