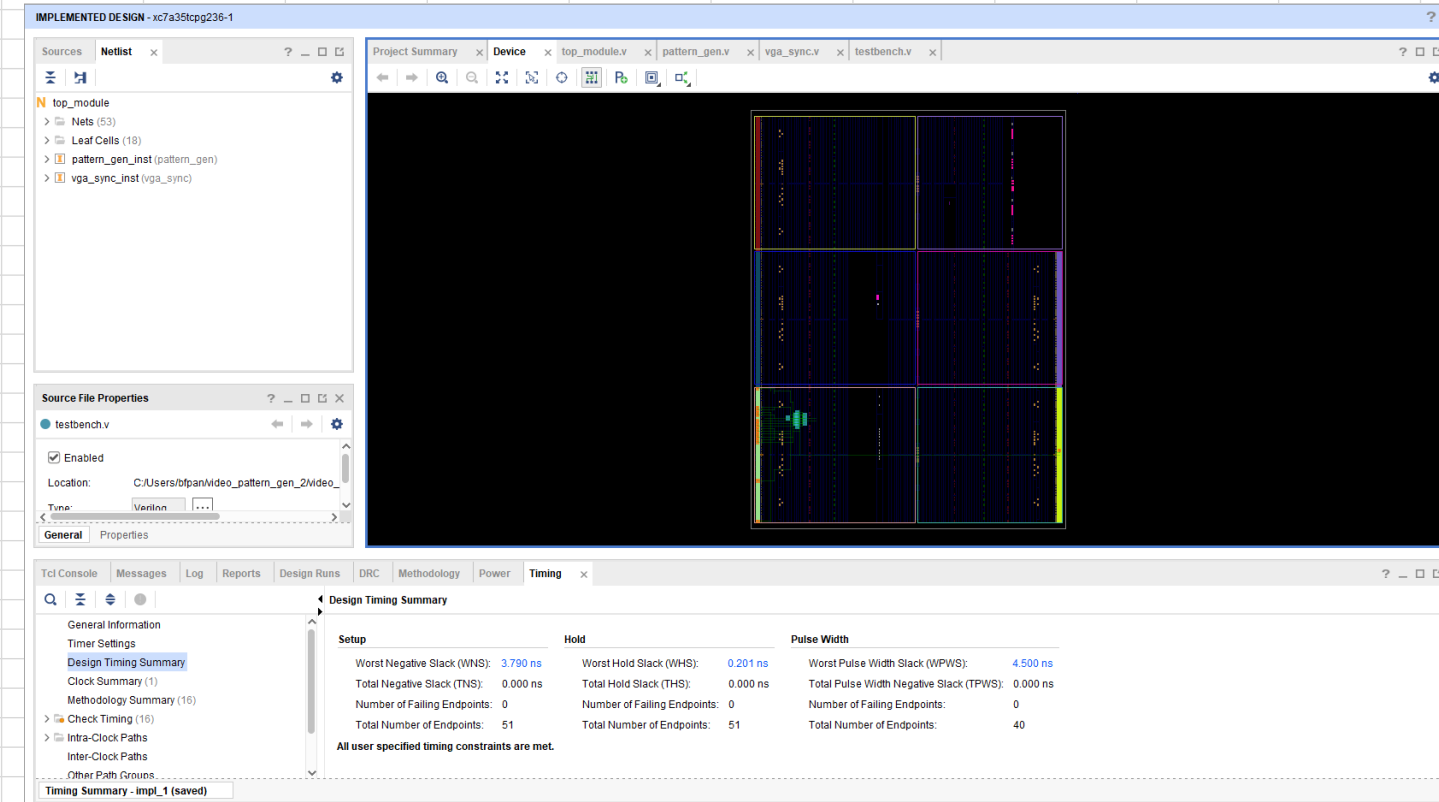
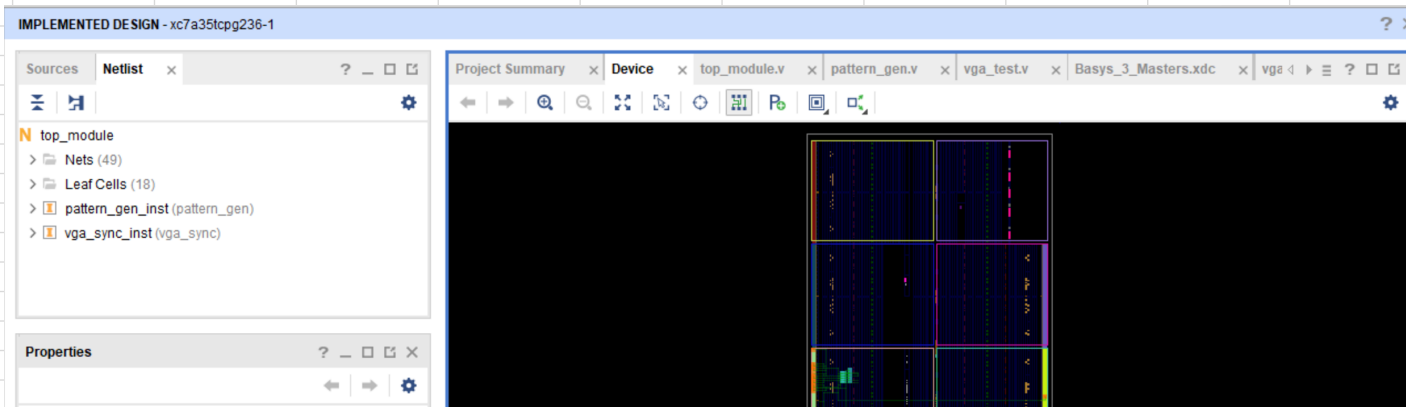


TIMING REPORT FOR OVERALL PATTERN GENERATOR DESIGN



TIMING FOR MICROBLAZE ALONE



Select an object to see properties

Tcl Console Messages Log Reports Design Runs Methodology Power Timing x ? _ □ □

Design Timing Summary

General Information
Timer Settings
Design Timing Summary
Clock Summary (1)
Methodology Summary (16)
Check Timing (16)
Intra-Clock Paths
Inter-Clock Paths
Other Path Groups
Timing Summary - impl_1 (saved)

| Setup | Hold | Pulse Width |
|--------------------------------------|----------------------------------|---|
| Worst Negative Slack (WNS): 3.198 ns | Worst Hold Slack (WHS): 0.203 ns | Worst Pulse Width Slack (WPWS): 4.500 ns |
| Total Negative Slack (TNS): 0.000 ns | Total Hold Slack (THS): 0.000 ns | Total Pulse Width Negative Slack (TPWS): 0.000 ns |
| Number of Failing Endpoints: 0 | Number of Failing Endpoints: 0 | Number of Failing Endpoints: 0 |
| Total Number of Endpoints: 51 | Total Number of Endpoints: 51 | Total Number of Endpoints: 40 |

All user specified timing constraints are met.

TIMING FOR PATTERN GEN CODE ALONE

IMPLEMENTED DESIGN - xc7a35tqpg236-1

Sources Netlist x ? _ □ □

top_module
Nets (22)
Leaf Cells (18)
vga_sync_inst (vga_sync)

Properties ? _ □ □ x
Select an object to see properties

Project Summary x Device x top_module.v x Basys_3_Masters.xdc x vga_controller.v x vga_test.v x pattern_gen.v x ? _ □ □

Timing x ? _ □ □

Design Timing Summary

| Setup | Hold | Pulse Width |
|--------------------------------------|----------------------------------|---|
| Worst Negative Slack (WNS): 6.236 ns | Worst Hold Slack (WHS): 0.152 ns | Worst Pulse Width Slack (WPWS): 4.500 ns |
| Total Negative Slack (TNS): 0.000 ns | Total Hold Slack (THS): 0.000 ns | Total Pulse Width Negative Slack (TPWS): 0.000 ns |
| Number of Failing Endpoints: 0 | Number of Failing Endpoints: 0 | Number of Failing Endpoints: 0 |
| Total Number of Endpoints: 24 | Total Number of Endpoints: 24 | Total Number of Endpoints: 25 |

All user specified timing constraints are met.