Łukasz Przeniosło
Curriculum vitae

WORK EXPERIENCE

● JULY 2017 – PRESENT

Przenioslo Electronics & Software, Szczecin
Owner (freelancer)

Hardware and software design according to the client's needs and/ or specifications (remotely and onsite).

● JULY 2017 – PRESENT

Apptimia Sp. z o.o., Szczecin
Electronics design & software specialist

Embedded software and hardware development for UAV and IoT devices (project lead).

● JUNE 2013 – JULY 2017

Mechatronic Engineering Sp. z o.o., Szczecin
Hardware & software engineer

Development and maintenance of hardware for the produced SMT machinery, writing firmware for the created hardware, writing testing PC applications, building prototypes.

● JULY 2012 – JUNE 2013

Mechatronic Engineering Sp. z o.o., Szczecin
Hardware & manufacture engineer

SMT machinery hardware assembly, faulty parts service, existing designs debugging.

EDUCATION

2016+	**Electrical engineering**
	PH.D. DIPLOMA
	ZUT, Szczecin
2014–16	**Electrical engineering**
	MA DIPLOMA
	ZUT, Szczecin
2010–14	**Electronics engineering**
	BA DIPLOMA
	ZUT, Szczecin
2006–10	**Electronics engineering**
	TECHNICAL SCHOOL DIPLOMA
	ZSTiO nr 6, Gorzów Wlkp

COURSES & LICENSES

2018	**C++ Institute CLA & CLP**
	Advanced C11 programming course.
2017	**Unmanned Aerial Vehicle Operator**
	Visual Line of sight (VLOS) license.
2012	**Tieto Programming Academy**
	Embedded C programming course.

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HONORS AND SCHOLARSHIPS

2017	**Szczecin's President best thesis award**
	Received for MA thesis: "Universal smart electric motors controller for industry applications"
2015	**Minister of science and higher education scholarship**
	Received in 2015 for academic achievements
2013–16	**Szczecin's President scholarship**
	Received in years: 2013 (5th ed.), 2015 (10th ed.), 2016 (12th ed.) for academic achievements

SKILLS

HW	PCB design & production, soldering, rapid prototyping, Altium Designer, AVR, PIC, ARM, PowerPC, STM32, FPGA, C2000, MEMS, RF/analog, power electronics, low power, BMS
SW	Assembly, C, Modern C++, Qt + QML, Matlab + Simulink, Plecs, Spice, GIT, VHDL, DSP, FreeRTOS, motor control, Buildroot, Linux drivers, architecture design, IoT, OPCUA
MISC	Documentation (LaTeX, Doxygen, Office), HW/SW product management (design, pricing, production), good with presentations

COMMUNICATION

POLISH	Native language
ENGLISH	Full professional proficiency
GERMAN	Elementary proficiency

SOCIAL NETWORKING

LINKEDIN	przenioslo
GITHUB	bremenpl

I hereby give consent for my personal data included in the job offer to be processed for the purposes of recruitment under the Data Protection Act 1997 (Dz. U. no. 133, item 883).