

Łukasz Przeniosło

Curriculum vitae

WORK EXPERIENCE

JULY 2017 – PRESENT

Apptimia, Szczecin

Hardware & software engineer

Embedded software and hardware development for UAV and IoT devices.

JUNE 2013 – JULY 2017

Mechatronic Systems, Szczecin

Hardware & software engineer

Development and maintenance of hardware for the produced STM machinery (Pick & Place, stencil printers, reflow ovens, PCB conveyors), writing firmware for the created hardware, writing testing PC applications, building prototypes.

JULY 2012 – JUNE 2013

Mechatronic Systems, Szczecin

Hardware & manufacture engineer

SMT machinery hardware assembly, faulty parts service, existing designs debugging.

EDUCATION

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|--------------|---|
| 2016 – PRES. | Electrical engineering
PH.D. DIPLOMA
ZUT, Szczecin |
| 2014 – 2016 | Electrical engineering
MA DIPLOMA
ZUT, Szczecin |
| 2010 – 2014 | Electronics engineering
BA DIPLOMA
ZUT, Szczecin |
| 2006 – 2010 | Electronics engineering
TECHNICAL SCHOOL DIPLOMA
ZSTiO nr 6, Gorzów Wlkp |

COURSES

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| 2012 | Tieto Programming Academy
<i>Embedded C programming course.</i> |
| 2012 | Microsoft IT Academy Program
<i>C# programming course.</i> |

COMMUNICATION

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| POLISH | Native language |
| ENGLISH | Full professional proficiency (ILR scale) |
| GERMAN | Elementary proficiency (ILR scale) |

📍	Szczecin, Poland
☎	+48 792 456 829
✉	lukasz@przenioslo.com
🌐	www.przenioslo.com

HONORS AND SCHOLARSHIPS

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| 2017 | Szczecin's President best thesis award
<i>Received for MA thesis.</i> |
| 2016 | Szczecin's President scholarship
<i>12th edition.</i> |
| 2015 | Minister of science and higher education scholarship
<i>Received for year 2015.</i> |
| 2015 | Szczecin's President scholarship.
<i>10th edition.</i> |
| 2014 | ZUT scientific groups competition
<i>1st place.</i> |
| 2013 | Szczecin's President scholarship
<i>5th edition.</i> |
| 2013 | ZUT scientific groups competition
<i>3rd place.</i> |
| 2012 | ZUT scientific groups competition
<i>1st place.</i> |

SKILLS

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| HARDWARE | PCB design/ production, fast prototyping, Altium Designer, Autodesk Eagle, AVR, PIC, ARM, FPGA, 8051, C2000, Soldering, Power electronics, RF design |
| SOFTWARE | Assembler, C/C++, C#, TeX TeX, Matlab, Qt, Linux, SVN, GIT, VHDL, FreeRTOS, DSP, Motor control, system architecture design, networking |

SOCIAL NETWORKING

- | | |
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| LINKEDIN | przenioslo |
| GITHUB | bremenpl |
| ACADEMIA | Łukasz Przeniosło |
| HACKADAY | przenioslo |

I hereby give consent for my personal data included in the job offer to be processed for the purposes of recruitment under the Data Protection Act 1997 (Dz. U. no. 133, item 883).