

# Łukasz Przeniosło

## Curriculum vitae

### WORK EXPERIENCE

JULY 2017 – PRESENT

Apptimia Sp. z o.o., Szczecin

#### Hardware & software engineer

Embedded software and hardware development for UAV and IoT devices.

JUNE 2013 – JULY 2017

Mechatronic Engineering Sp. z o.o., Szczecin

#### Hardware & software engineer

Development and maintenance of hardware for the produced SMT machinery, writing firmware for the created hardware, writing testing PC applications, building prototypes.

JULY 2012 – JUNE 2013

Mechatronic Engineering Sp. z o.o., Szczecin

#### Hardware & manufacture engineer

SMT machinery hardware assembly, faulty parts service, existing designs debugging.

### EDUCATION

- 2016 – PRES. **Electrical engineering**  
PH.D. DIPLOMA  
ZUT, Szczecin
- 2014 – 2016 **Electrical engineering**  
MA DIPLOMA  
ZUT, Szczecin
- 2010 – 2014 **Electronics engineering**  
BA DIPLOMA  
ZUT, Szczecin
- 2006 – 2010 **Electronics engineering**  
TECHNICAL SCHOOL DIPLOMA  
ZSTiO nr 6, Gorzów Wlkp

### COURSES & LICENSES

- 2017 **Unmanned Aerial Vehicle Operator**  
Visual Line of sight (VLOS).
- 2012 **Tieto Programming Academy**  
Embedded C programming course.
- 2012 **Microsoft IT Academy Program**  
C# programming course.

### COMMUNICATION

- POLISH Native language
- ENGLISH Full professional proficiency
- GERMAN Elementary proficiency

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### HONORS AND SCHOLARSHIPS

- 2017 **Szczecin's President best thesis award**  
Received for MA thesis.
- 2016 **Szczecin's President scholarship**  
12th edition.
- 2015 **Minister of science and higher education scholarship**  
Received for year 2015.
- 2015 **Szczecin's President scholarship.**  
10th edition.
- 2014 **ZUT scientific groups competition**  
1st place.
- 2013 **Szczecin's President scholarship**  
5th edition.
- 2013 **ZUT scientific groups competition**  
3rd place.
- 2012 **ZUT scientific groups competition**  
1st place.

### SKILLS

- HARDWARE PCB design/ production, fast prototyping, Altium Designer, Autodesk Eagle, AVR, PIC, ARM, FPGA, 8051, C2000, Soldering, Power electronics, RF design
- SOFTWARE Assembler, C/C++, C#, ~~TeX~~TeX, Matlab, Qt, Linux, SVN, GIT, VHDL, FreeRTOS, DSP, Motor control, system architecture design, networking

### SOCIAL NETWORKING

- LINKEDIN przenioslo
- GITHUB bremenpl
- ACADEMIA przenioslo
- HACKADAY przenioslo

I hereby give consent for my personal data included in the job offer to be processed for the purposes of recruitment under the Data Protection Act 1997 (Dz. U. no. 133, item 883).