

HIN	VLINEN	BRK	VLIN	CONDITION
1	1	1	0	Work
0	1	1	1	Work
0	X	0	0	Break
0	0	1	0	OFF

No series resistor needed

D10  
MCL103

U13  
IR25601

HI S

C42  
10uF (1206)

C43  
22uF (2220)

Q3A  
IRF8313

MOT

Q3B  
IRF8313

LOS

R21  
10R

R22  
10R

C49  
470nF

C48  
100nF

12V

VLIN

GND

U6A  
SN74AHC1G86

U7A

MC74VHC1

HIN

VLINEN

BRK

3V3

C12  
100nF

GND

U7B

MC74VHC1

3V3

C13  
100nF

GND

U6B

SN74AHC1G86

IR23601 has internal 100 ns deadtime. Use this chip only for the channels that dont have internal DT (CH4).

Above circuit is used to create a complementary PWM signal for the 4th channel. Deadtime is not applied yet.