

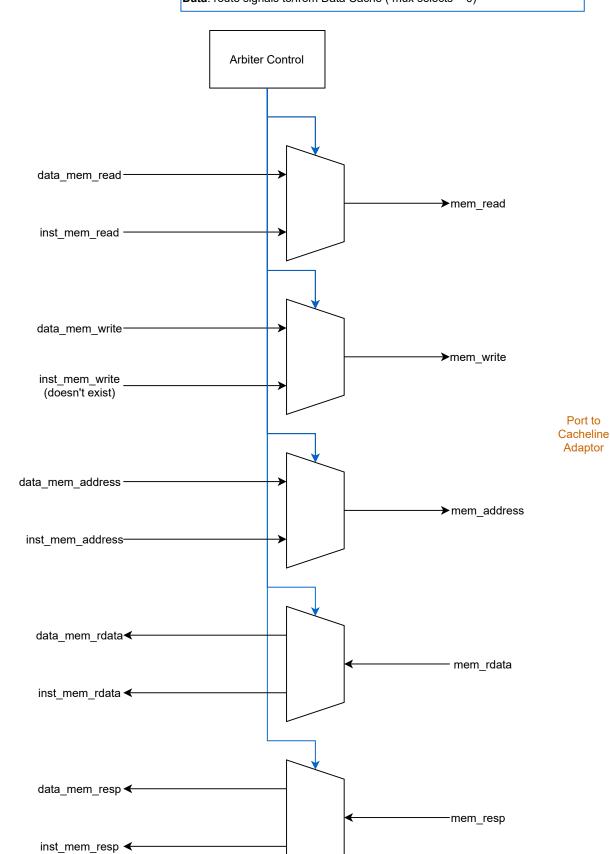
Note: Prioritize Instruction requests over Data requests

State Outputs:

Idle: Output all Zeros

Instruction: route signals to/from Instruction Cache (mux selects = 1)

Data: route signals to/from Data Cache (mux selects = 0)



Port to

Caches