State\_machine:machine Mux1 SEL[1..0] 1:0 OUT delay\_expired DATA[3..0] stop\_button Mux0 2:1 SEL[1..0] OUT start\_button DATA[3..0] state[2..0] Mux2 SEL[2..0] OUT DATA[7..0] state[0]~reg[2..0] clock -CLK Q 3'h0 SCLR