Blank Quiz

MP Test

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8086 based system needs clock signal of 6 MHz. The crystal frequency should be ____   1 point

- ○ 6 MHz
- ○ 12 MHz
- ● 18 MHz
- ○ 24 MHz

Clear selection

If you are asked to implement the memory system for a 8086 microprocessor, what memory configuration will you use?   1 point

- ● One 1M Bytes chip
- ○ Two 512KBytes chips
- ○ One 1M Word chip
- ○ Two 512KWord chips

Clear selection

A word data is stored on odd boundaries, then 8086 takes ___ memory read cycles.

1 point

○ 1

◉ 2

○ 3

○ 4

Clear selection

8086 is a _____ processor.

1 point

○ Scalar

○ Super scalar

◉ Vector

○ Array

Clear selection

____ signal is used enable data onto the most significant half of data bus, D8-D15.

1 point

○ A0

◉ BHE#

○ DEN#

○ S2

Clear selection

MOV CX, [BX] is an example of _____ addressing mode.   1 point

- ⦿ Register Indirect Addressing

- ○ Based Addressing

- ○ Indexed Addressing

- ○ Based Index Addressing

Clear selection

Instruction pointer content is automatically incremented by ____ as the   1 point
execution of the next instruction takes place.

- ⦿ 1

- ○ 2

- ○ Size of current instruction

- ○ Size of next instruction

Clear selection

By default _____ resister is used as counter register.   1 point

- ○ AX

- ○ BX

- ⦿ CX

- ○ DX

Clear selection

EQU assembly directive is _____   1 point

○ used to indicate the beginning of a code/ data/stack segment

○ used to inform the assembler the name of the program/ data segment that should be used for a specific segment.

○ is used to assign the starting address (Effective address) for a program/ data segment

● is used to attach the value to the variable.

Clear selection

Dedicated memory locations of 8086 are _____   1 point

● 00000 to 0007F, FFFF0 to FFFFB

○ 00000 to 0007F, 0FFFF to 0FFFB

○ 0FFF0 to 0FF7F, 0FFFF to 0FFFB

○ 00000 to 00F7F, 0FFFF to 0FFFB

Clear selection

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