

Blank Quiz

Total points 7/10

MP Test

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✓ 8086 based system needs clock signal of 6 MHz. The crystal frequency should be ____ 1/1

- ☐ 6 MHz
- ☐ 12 MHz
- ☒ 18 MHz
- ☐ 24 MHz



✗ If you are asked to implement the memory system for a 8086 microprocessor, what memory configuration will you use? 0/1

- ☒ One 1M Bytes chip
- ☐ Two 512KBytes chips
- ☐ One 1M Word chip
- ☐ Two 512KWord chips



Correct answer

- ☒ Two 512KBytes chips



✓ A word data is stored on odd boundaries, then 8086 takes ___ memory read cycles. 1/1

☐ 1

☒ 2

☐ 3

☐ 4



✗ 8086 is a _____ processor.

0/1

☐ Scalar

☐ Super scalar

☒ Vector

☐ Array



Correct answer

☒ Scalar

✓ ___ signal is used enable data onto the most significant half of data bus, 1/1
D8-D15.

☐ A0

☒ BHE#

☐ DEN#

☐ S2



✓ MOV CX, [BX] is an example of _____ addressing mode.

1/1

- ☒ Register Indirect Addressing
- ☐ Based Addressing
- ☐ Indexed Addressing
- ☐ Based Index Addressing



✗ Instruction pointer content is automatically incremented by _____ as the execution of the next instruction takes place.

0/1

- ☒ 1
- ☐ 2
- ☐ Size of current instruction
- ☐ Size of next instruction



Correct answer

- ☒ Size of current instruction

✓ By default _____ register is used as counter register.

1/1

- ☐ AX
- ☐ BX
- ☒ CX
- ☐ DX



✓ EQU assembly directive is _____

1/1

- ☐ used to indicate the beginning of a code/ data/stack segment
- ☐ used to inform the assembler the name of the program/ data segment that should be used for a specific segment.
- ☐ is used to assign the starting address (Effective address) for a program/ data segment
- ☒ is used to attach the value to the variable. ✓

✓ Dedicated memory locations of 8086 are _____

1/1

- ☒ 00000 to 0007F, FFFF0 to FFFFB ✓
- ☐ 00000 to 0007F, 0FFFF to 0FFFB
- ☐ 0FFF0 to 0FF7F, 0FFFF to 0FFFB
- ☐ 00000 to 00F7F, 0FFFF to 0FFFB

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