

ECE 6410 HW02

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2026-01-22

1) SOURCES OF NOISE
MINIMIZED BY
RATIOSMETRIC

- 1) POWER SUPPLY, SPECIFICALLY VARIATIONS OR DRIFT
- 2) GAIN ERROR IN EXCITATION VOLTAGE
- 3) REFERENCE VOLTAGE DRIFT FROM INTERNAL REFERENCE ADJUS, TEMPERATURE, AND ICR REFERENCE TOLERANCE.

ELIMINATED BY

DIFFERENTIAL CIRCUITS

- 1) COMMON-MODE ELECTROMAGNETIC INTERFERENCE
- 2) GROUND POTENTIAL DIFFERENCES
- 3) CABLE PICKUP NOISE

2)

a) FOR A BALANCED BRIDGE, THE OTHER RESISTORS SHOULD BE 350 Ω AS WELL

$$b) S = \frac{\Delta V_{out}}{\Delta R_{strain}}$$

$$\text{BRIDGE RATIO } K = \frac{R_{strain}}{R_0}$$

$$K_2 = K_{max} \left(\frac{K_{min}}{K_{max}} \right)^{\frac{1}{n-1}}$$

$$K_2 = 1 - \left(\frac{10}{1} \right)^{\frac{1}{2-1}}$$

$$R_s = K \cdot R_0$$

NEED TO NORMALIZE $\frac{V}{R} \cdot \frac{R_{ref}}{V_{ref}} \left\{ \frac{h_p}{V_{in}} \right\}$

K	R ₀ (Ω)	V _{out} (V)
1.01	352	-0.014
1.26	441	-0.579
1.58	553	-1.124
2	700	-1.667
2.51	879	-2.152
3.16	1106	-2.596
3.98	1393	-2.992
5.01	1757	-3.736
6.31	2209	-4.632
7.94	2799	-5.091

3)

USE A HALF BRIDGE, THIS INCLUDES 2 STRAIN GAUGES, ONE GAUGE WILL BE ACTIVE, MEANING IT IS PLACED IN A POSITION TO MEASURE STRAIN. THE SECOND GAUGE WILL BE INACTIVE, MEANING IT WILL EXPERIENCE CHANGES IN TEMPERATURE BUT NOT STRAIN

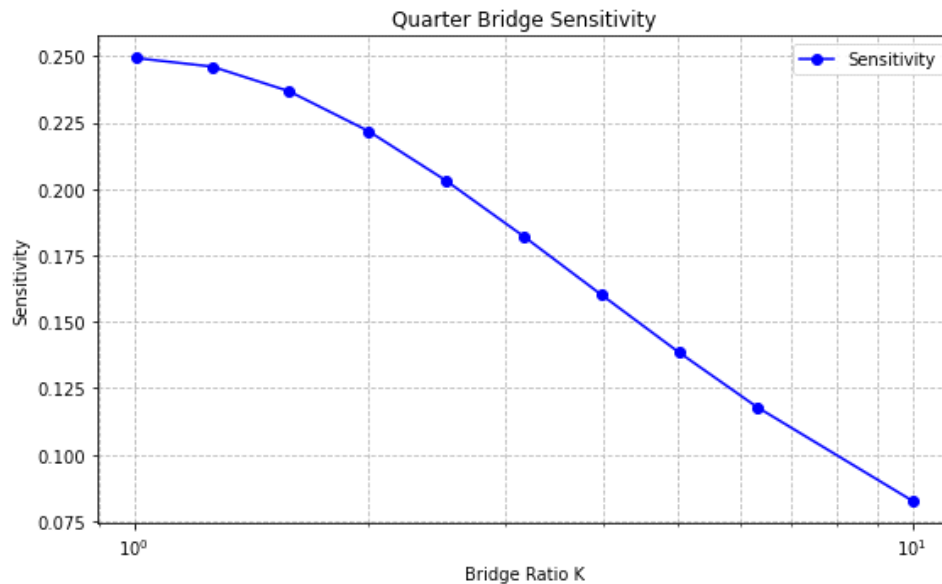
- R₁ - ACTIVE STRAIN GAUGE
- R₂ - INACTIVE STRAIN GAUGE
- R₃ - MATCHING RESISTOR
- R₄ - MATCHING RESISTOR



THE MATCHING RESISTORS SHOULD HAVE THE SAME TEMPERATURE COEFFICIENT OF RESISTANCE (TCR) AS THE STRAIN GAUGES, IF POSSIBLE

AT NO STRAIN BUT CHANGING TEMPERATURE, BOTH GAUGES CHANGE BY THE SAME AMOUNT.

AT STRAIN AND CHANGING TEMPERATURE, ONLY THE ACTIVE GAUGE WILL CHANGE AS A RESULT OF THE STRAIN.



3.

b) THE INACTIVE STRAIN GAUGE WILL BE MOUNTED ON A MATERIAL THAT IS THERMALLY CONNECTED TO THE ENVIRONMENT BEING TESTED, BUT IT WON'T EXPERIENCE THE EFFECTS OF STRAIN. THIS WILL ENSURE THE INACTIVE GAUGE IS THERMALLY COUPLED WITH THE ACTIVE GAUGE AS WELL AS MECHANICALLY ISOLATED.

c) $R(T) = R(1 + g\epsilon)(1 + \alpha(T - T_0))$ [Ω]

$Z_1 = R(1 + g\epsilon)(1 + \alpha(T - T_0))$ * (ACTIVE)

$Z_2 = R(1 + \alpha(T - T_0))$ * (NONACTIVE)

$Z_3 = R$

$Z_4 = R$

$$\therefore V_o = V_{ref} \left(\frac{R(1 + \alpha(T - T_0))}{R(1 + g\epsilon)(1 + \alpha(T - T_0)) + R(1 + \alpha(T - T_0))} - \frac{R}{R + R} \right)$$

$$\Rightarrow V_{ref} \left(\frac{1 + g\epsilon}{2 + g\epsilon} - \frac{1}{2} \right) = V_{out}$$

WE CAN SEE THAT TEMPERATURE IS NOT IN THE SIMPLIFIED EQUATION. THIS ALGEBRAICALLY PROVES THAT THE OUTPUT (V_{out}) IS NOT AFFECTED BY TEMPERATURE.

d)		(2X STRAIN)	
$^{\circ}C$	$V_{out} (V)$	$120 \Omega @ 20^{\circ}C$	$415 \Omega @ 20^{\circ}C$
20	2.757	$255 \Omega @ -50^{\circ}C$	$278 \Omega @ -50^{\circ}C$
-50	2.75	$1405 \Omega @ 80^{\circ}C$	$1650 \Omega @ 80^{\circ}C$
800	401 mV		

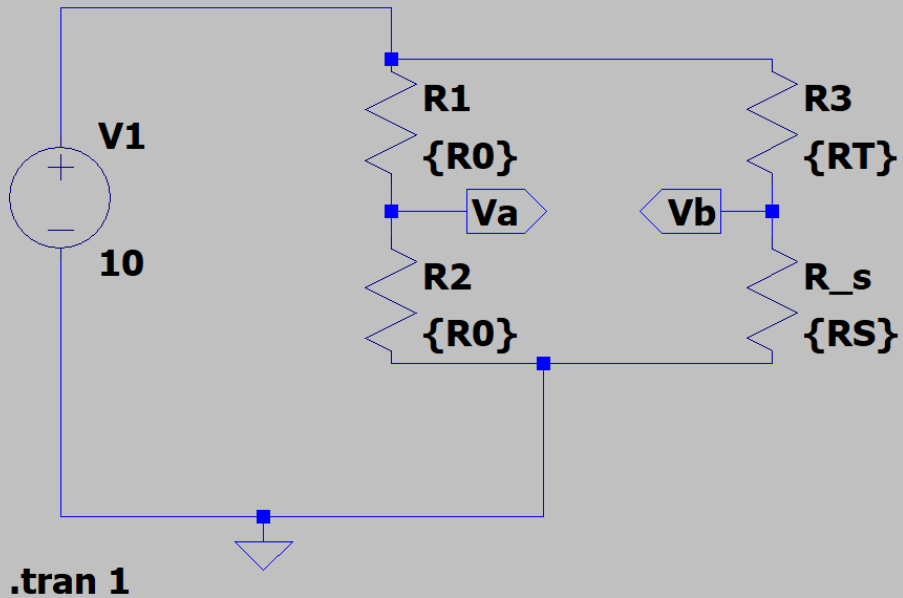
(WITH STRAIN) (ADJUSTED VALUES) $\frac{415}{278} = 1.39$ $\frac{1650}{1405} = 1.19 \Rightarrow x = \frac{120}{1.39} \Rightarrow x = 86.33 \Omega$

$^{\circ}C$ V_{out} (WITH STRAIN)		$^{\circ}C$ V_{out}	
20	2.75	20	2.75
-50	2.75	-50	2.75
800	401 mV	800	401 mV

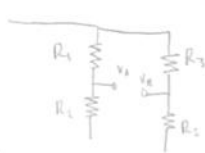
WHEN THERE IS NO STRAIN, WE CAN SEE THAT $V_{out} = 0$. THIS DEMONSTRATES HOW THE INACTIVE STRAIN GAUGE CAUSES THE CHANGE IN TEMPERATURE TO HAVE NO EFFECT ON THE BALANCING OF THE BRIDGE.

FROM THE PROVIDED VALUES FOR THIS STRAIN GAUGE, IT APPEARS THAT THE RESISTANCE DOES NOT HAVE A LINEAR, OR CLOSE TO LINEAR, RELATION TO CHANGES IN TEMPERATURE. THIS CAN BE OBSERVED BY LOOKING AT THE TABLE ABOVE.

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.param R0=120  
.param RS=255  
.param RT=255  
.param strain=0
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4.



$$R_1 = R_2 = R_3 = 100\Omega$$

$$R_4 = 100\Omega$$

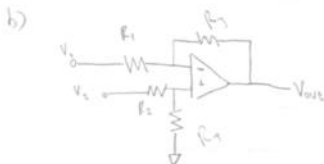
R_s	$V_{out} (V_1 - V_2)$
100	0
(+10%) 110	238 mV
(-10%) 90	263 mV

THE WORST CASE SCENARIO IS WHEN R_s HAS -10% VARIATION.

THE TABLE ABOVE SHOWS HOW V_{out} CHANGES WITH A $\pm 10\%$ VARIATION TO R_s . FOR EASE OF USE, I SELECTED THE RESISTOR VALUES TO BE 100 Ω .

5.

a) WITH 10K Ω RESISTORS, WE HAVE A FAIRLY HIGH INPUT IMPEDANCE. IT LOOKS LIKE THE LT1793 WOULD BE THE BETTER OPTION BECAUSE IT APPEARS TO BE DESIGNED FOR HIGH IMPEDANCE. WHEREAS THE LT1363 IS DESIGNED FOR LOW IMPEDANCE.



CASE 1: $R_1 = R_2$, $R_3 = R_4$, $V_{out} = \frac{R_3}{R_1} (V_2 - V_1)$

CASE 2: $R_4 = R_3$, $V_{out} = (V_2 - V_1) \left[1 + \frac{2R_2}{R_1} \right]$

$$10V_{PK-2PK} \Rightarrow V_{OUT} = 5V$$

$$R_{AILS} \text{ ARE } \frac{1}{2} \cdot 12V$$

$$V_{in} = 0.5V$$

CASE 1: $V_{in} = V_1 - V_2 = 0.5V$

$$GAIN = \frac{10V}{0.5} = 20 \text{ GAIN} \quad R_1 = R_2 = 1k\Omega$$

$$R_3 = R_4 = 20k\Omega$$

$$GAIN = 20 = \frac{R_3}{R_1} = \frac{R_4}{1k\Omega} \Rightarrow R_3 = R_4 = 20k\Omega$$

CASE 2:

$$R_4 = R_3 = 20k\Omega$$

$$R_1 = 1k\Omega$$

$$R_2 = 9.5k\Omega$$

$$V_{out} = (V_2 - V_1) \left[1 + \frac{2R_2}{R_1} \right]$$

$$GAIN = 20 = \left[1 + \frac{2R_2}{R_1} \right] = \left[1 + \frac{2R_2}{1k\Omega} \right] \Rightarrow R_2 = 9.5k\Omega$$

USING 20% SENSITIVITY, $R_s = 877\Omega$

5.

CASE 1: $V_{out} = \frac{R_3}{R_1} (V_2 - V_1)$ $R_1 = R_2 = 10k$ $V_{in} = 0.215V$
 $R_3 = R_1$

$10k = \frac{R_3}{10k} (0.215) \Rightarrow R_3 = 465.116k\Omega$ GAIN = 46.5

CASE 2: $V_{out} = (V_2 - V_1) \left[1 + \frac{2R_3}{R_1} \right] \Rightarrow 10k \cdot (0.215V) \left[1 + \frac{2R_3}{10k} \right] \Rightarrow R_3 = 227550k\Omega$

$R_4 = R_3 = 10k$ $R_1 = 10k$ GAIN = 46.5

VOLTAGE ACROSS R_{load} CASE 1: $\sim 2.752V$ $\sim 2.755V$
CASE 2: $\sim 0.675V$ $\sim 0.676V$

$V_{in} = 0.215$

$V_{in} = 0.190$

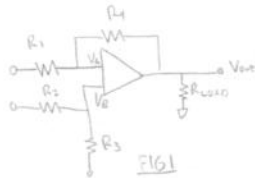


FIG 1

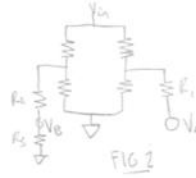


FIG 2

WHEN MEASURING THE VOLTAGE ACROSS R_{load} , IN CASE 1 $V_{in} \approx 0.215V$ AND IN CASE 2 $V_{in} \approx 0.190V$. WHILE THE GAIN IN BOTH CASES IS VERY SIMILAR, THE V_{out} IS QUITE DIFFERENT. AS SHOWN IN FIG 2 ABOVE, WE HAVE AN EQUIVALENT CIRCUIT FOR WHEN THE OP-AMP IS CONNECTED TO THE BRIDGE.

THE VALUE OF V_B IS INFLUENCED BY A VOLTAGE DIVIDER USING R_2 AND R_3 . WHEN WE CHANGE THIS RATIO FROM CASE 1 TO CASE 2, WE HAVE EFFECTIVELY CHANGED THE VALUE OF V_B , THUS CHANGING THE VOLTAGE COMING OUT OF THE OP-AMP, V_{out} , THAT IS NEEDED TO DRIVE $V_A = V_B$. THIS IS WHY WE SEE DIFFERENT VALUES OF VOLTAGE OVER THE LOAD RESISTOR IN CASES 1 AND 2.

