

## Completion Report

# Module 2A: Single-Cycle Processor Microarchitecture

Review

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Verification Code: 004CWW0V

This code can be used to verify the authenticity of this report

Score

**9.7 / 10.0**

Percentage

**97%**

Completed

**10 / 10**

Score

97%

## Detailed Results

#	Question	Pts	Score	Time	Checked?	Check #	Ex-plained?	Comple-ted?	Correct?
1	In a single-cycle processor...	1	1	2s	Yes	1		Yes	Yes
2	Which of the following data...	1	0.7	5s		0	Yes	Yes	No
3	Match each **instruction ty...	1	1	58s	Yes	1	Yes	Yes	Yes
4	For an **I-type ALU instruc...	1	1	10s	Yes	1		Yes	Yes
5	In a single-cycle design, b...	1	1	15s	Yes	2	Yes	Yes	Yes
6	Which instruction determine...	1	1	2s	Yes	1		Yes	Yes
7	Given the following latenci...	1	1	8s	Yes	1		Yes	Yes
8	Rank the following instruct...	1	1	36s	Yes	2		Yes	Yes
9	What is the name of the **I...	1	1	11s	Yes	1		Yes	Yes
10	Why is a single-cycle micro...	1	1	9s	Yes	1		Yes	Yes