		Id	Ab
ADD (v_I ,	$v_2)$	0	\perp
SUB (v_I ,	$v_2)$	0	\perp
$XOR(v_I,$	$v_2)$	0	\perp
SHL (v_{l} ,	$v_2)$	0	\perp
SHR (v_{l} ,	$v_2)$	0	\perp