

CSEG

LUT

.db \$3F, \$06, ..., \$71

; assume R16 = 'F'

R1=0 LDI ZH, HIGH(LUT<<1) ; x2

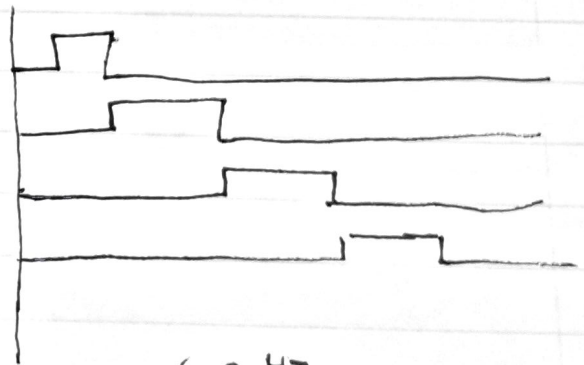
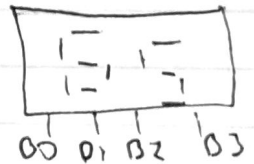
LDI ZL, LOW(LUT<<1)

ADD ZL, R16

EOX R1, R1

ADC ZH, R1

LPM R17, Z



60 MHz