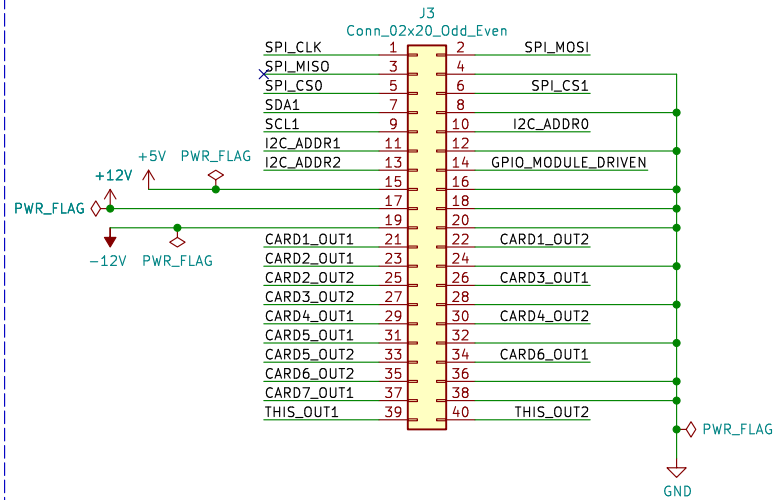
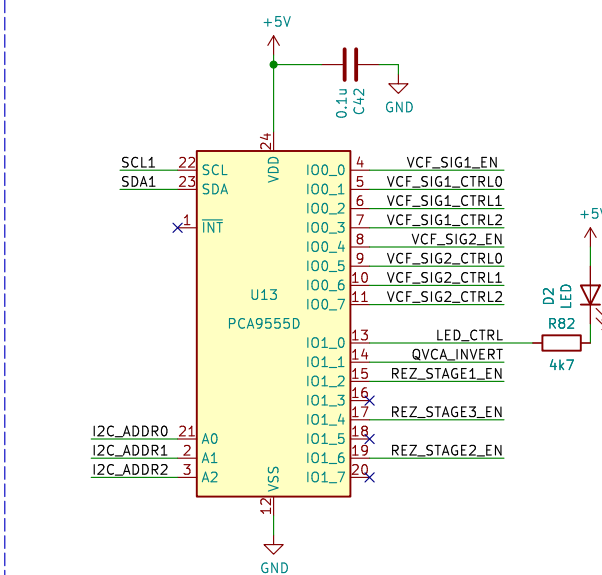


2x20 connection interface

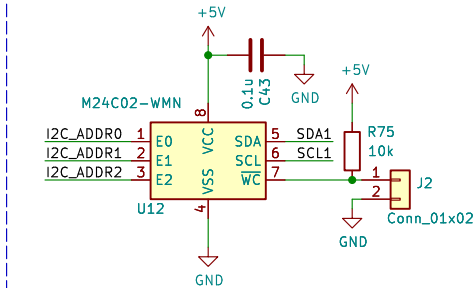


Interface: Zoxnoxious bus interface.  
Digital on one side, power in the middle,  
analog on the other.  
This card will drive THIS\_OUT1 and THIS\_OUT2 and GPIO\_MODULE\_DRIVEN.  
The other lines are tapped into for various functionality.

GPIO for switch control



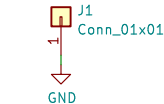
Board ID in EEPROM  
I2C address 1010{addr1, addr0}  
Write enable via jumper



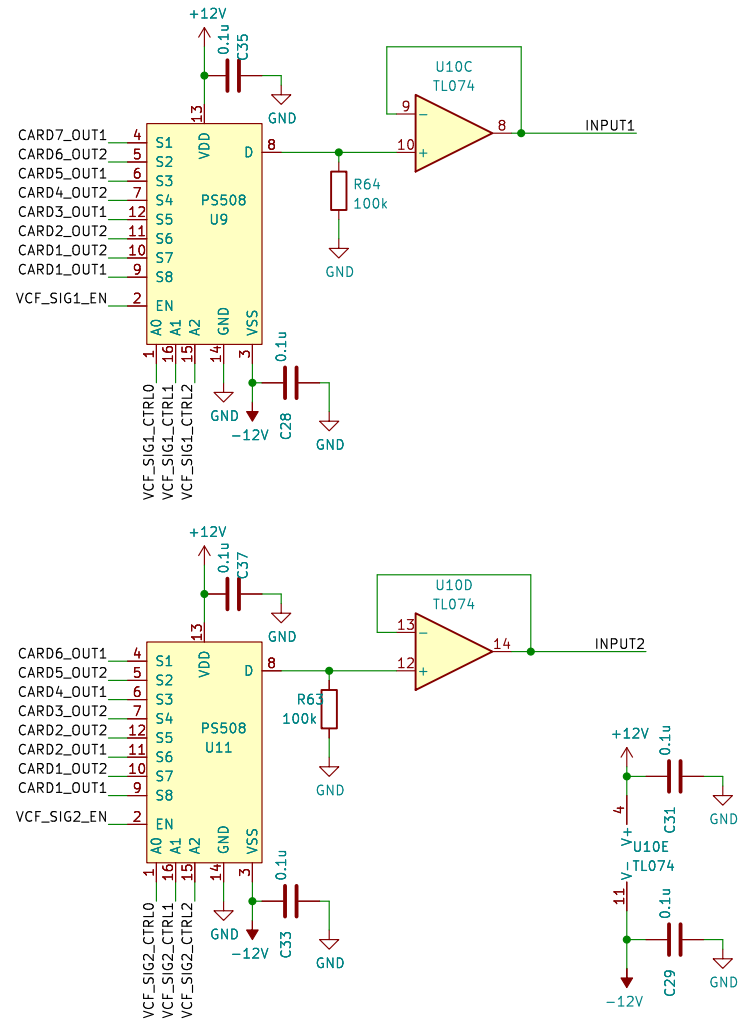
Mounting Holes



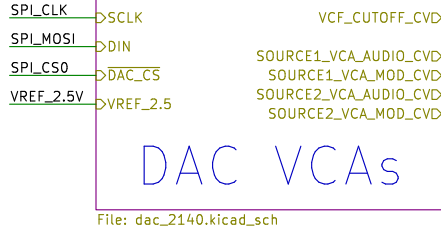
GND Test Point



Signal select --- output selected signal in-phase



dac\_2140



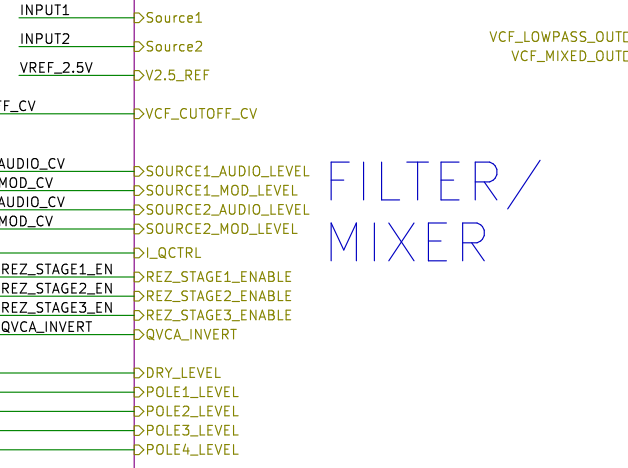
File: dac\_2140.kicad\_sch

dac\_2190



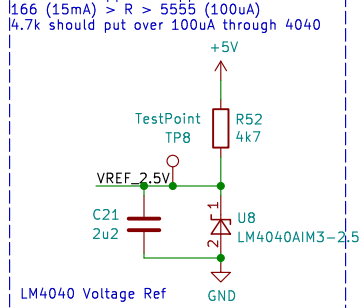
File: dac\_2190.kicad\_sch

filter\_ssi2140

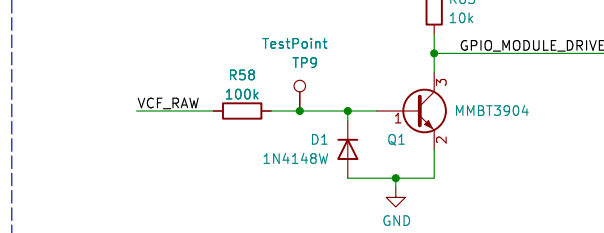


File: filter\_ssi2140.kicad\_sch

LM4040 Voltage Ref



GPIO MODULE DRIVEN:  
Use switching transistor to provide a  
0/+5V output for ucontroller. Diode to protect  
base for case if input goes significantly negative.



Zoxnoxious Engineering

Sheet: /  
File: poledancer.kicad\_sch

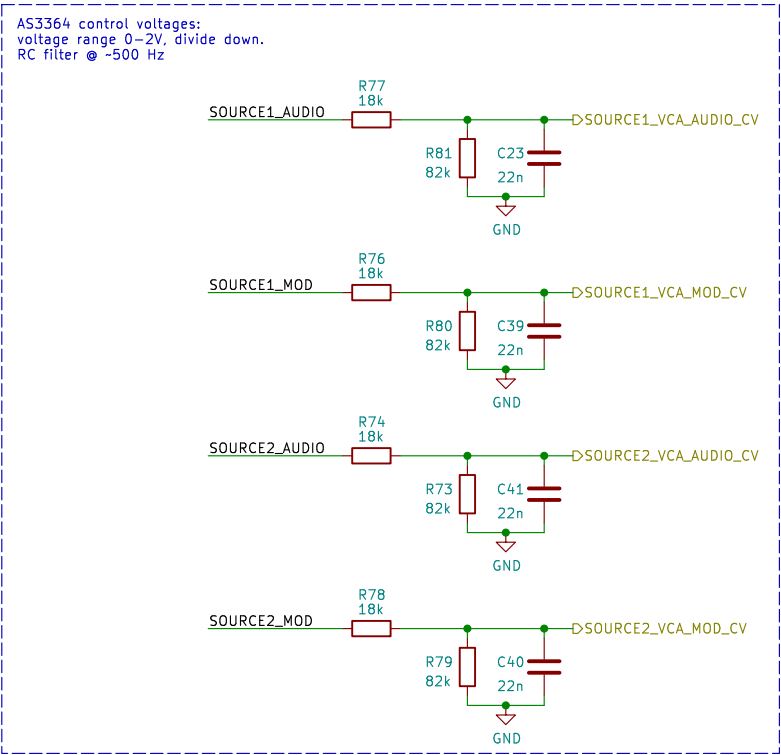
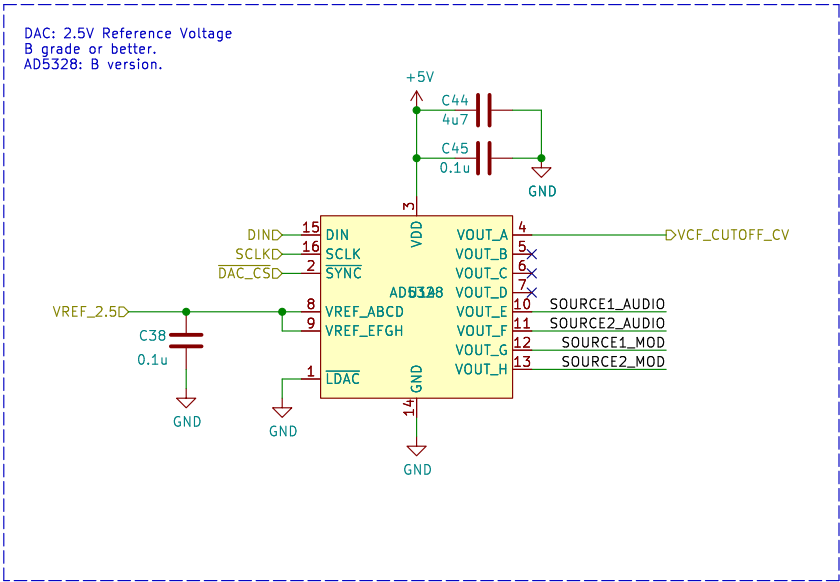
Title: Zoxnoxious Pole Dancer Mixing Filter

Size: B Date: 2024-11-17

KiCad E.D.A. kicad 7.0.11

Rev: 0.1

Id: 1/4



Sheet Vref load: 45k || 45k ==> 22.5k

Zoxnoxious Engineering

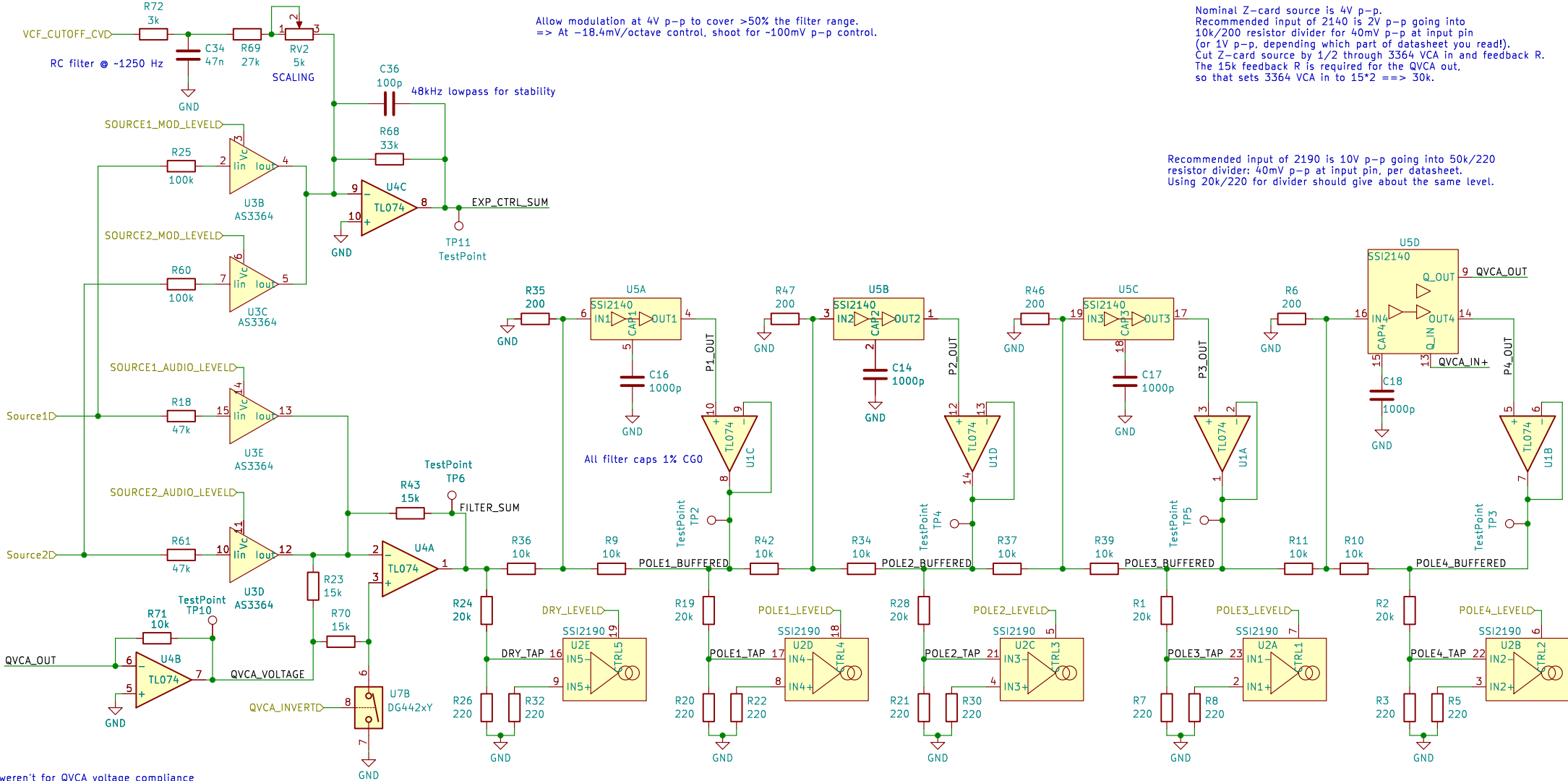
Sheet: /dac\_2140/  
File: dac\_2140.kicad\_sch

**Title: Zoxnoxious Pole Dancer Mixing Filter**

Size: B	Date: 2024-11-17	Rev: 0.1
KiCad E.D.A. kicad 7.0.11		Id: 2/4

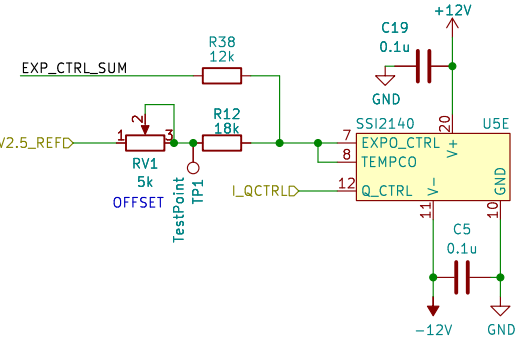


# Filter and Mixer Core

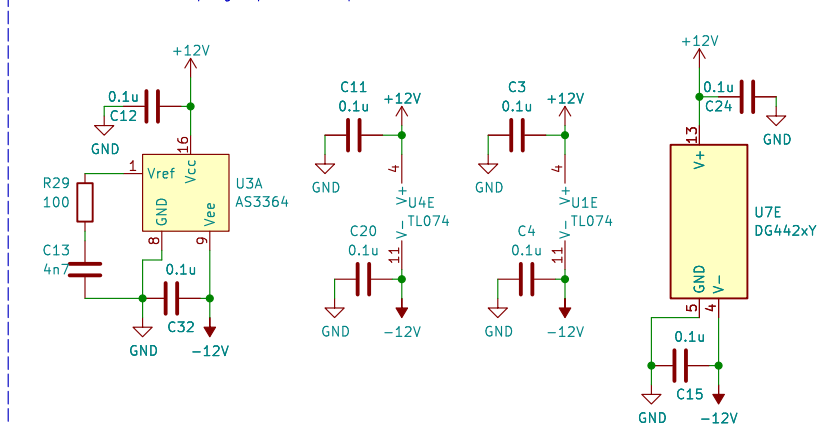


## VCF Control Inputs

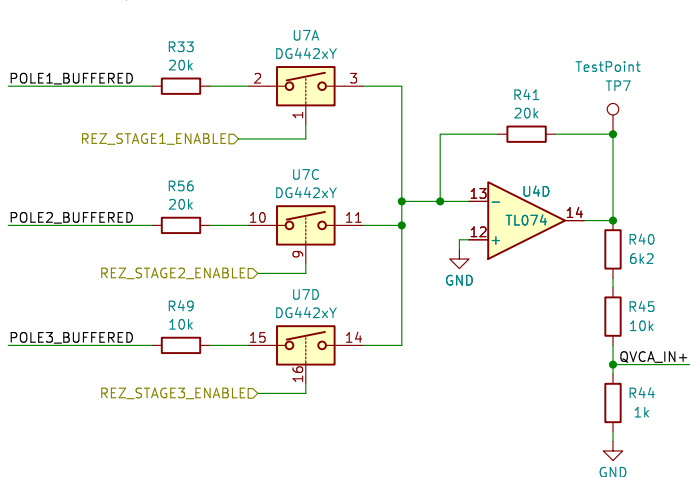
Control Range:  
Datasheet: -18.4 mV/oct; 0V => 1 kHz  
Design for range of 13.75 Hz to 14080 Hz (ten octaves)  
 $\log_2(f_1/f_2) \times (-18.4\text{mV})$  for range:  
113.8mV : 13.75Hz; -70.2mV : 14080 Hz  
Shoot for range a bit greater than this by allowing scaling & offset



## Decoupling caps close to pins

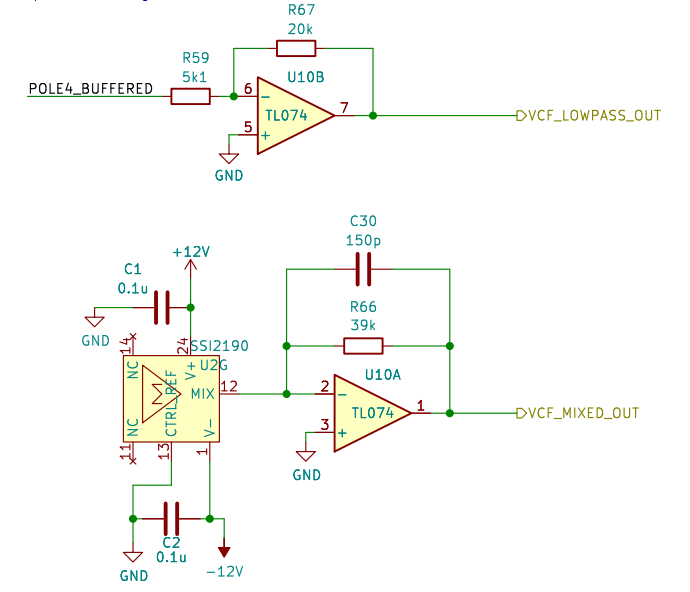


## Resonance Compensation



A questionably weighted selection is used for resonance compensation. SSI app note AN701 details using SSI2164. This attempts the concept.  
Pole 1 switchable with a weight of 1.  
Pole 2 switchable with a weight of 1.  
Pole 3 switchable with a weight of 2.  
Pole 4 hardwired in the 2140 with a weight of 1.  
Used in conjunction with QVCA-INVERT to phase correct the output.  
Useful combinations in the form (weight pole 1, 2, 3, 4):  
1LP, 1HP: 1, 0, 0, 1 (REZ\_STAGE1 set) (not quite the right BP, but close)  
2LP, 2HP: 0, 1, 2, 1 (REZ\_STAGE2, REZ\_STAGE3 set, QVCA-INVERT set)  
uncompensated: 0, 0, 0, 1 (hardwired, all off)  
These are approximate filter equivalents as the weights aren't quite right.

## Output and Scaling



## Unused / Spares

