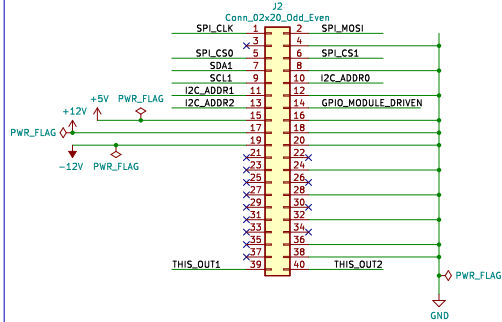


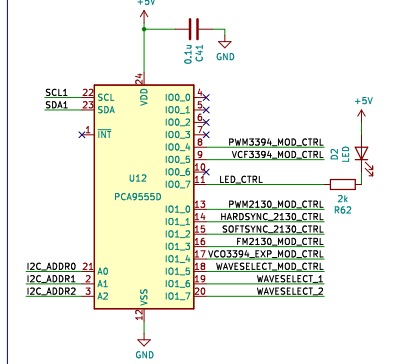
2x20 connection interface



Interface: Zonnoxious bus Interface.
Digital on one side, power in the middle,
analog on the other.
This card will drive THIS_OUT1 and THIS_OUT2 and GPIO_MODULE_DRIVEN.
The other lines are tapped into for various functionality.

GPIO for switch control

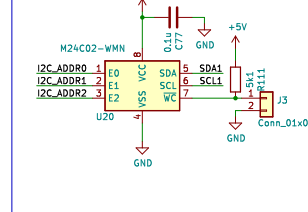
I2C address 0100[addr2,addr1,addr0]



Board ID in EEPROM

I2C address 1010[addr2,addr1,addr0]

Write enable via jumper



GND Test Point

J1 Conn_01x01

GND

DAC 2130

SPL_CLK

SPL_MOSI

SPL_CS1

VREF_2.5

VREF_2.5

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VREF_2.5

DAC 3394

SPL_CLK

SPL_MOSI

SPL_CS0

VREF_4.096

VREF_4.096

VREF_4.096

VREF_4.096

VREF_4.096

VREF_4.096

VREF_4.096

VREF_4.096

VREF_4.096

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VREF_4.096

DAC 2130

SPL_CLK

SPL_MOSI

SPL_CS1

VREF_2.5

VREF_2.5

VREF_2.5

VREF_2.5

VREF_2.5

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DAC 3394

SPL_CLK

SPL_MOSI

SPL_CS0

VREF_4.096

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VREF_4.096

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VREF_4.096

DAC 2130

SPL_CLK

SPL_MOSI

SPL_CS1

VREF_2.5

VREF_2.5

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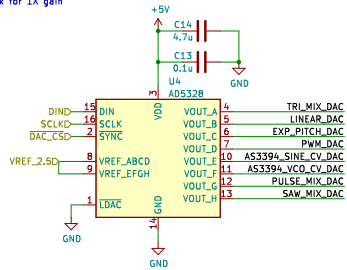
VREF_2.5

VREF_2.5

VREF_2.5

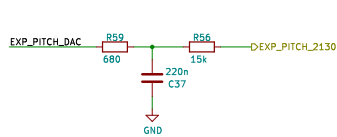
VREF_2.5

DAC: 2.5V Reference Voltage
Outputs at 1X gain from AD5328.
Vref impedance 45k for 1X gain

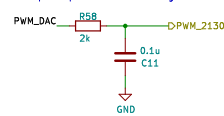


Sheet load:
Vref_2.5: 45k || 30k ==> 18k

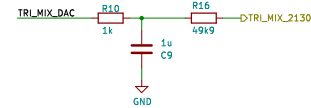
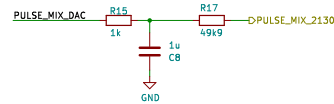
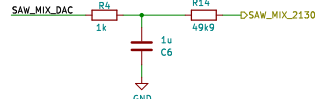
VCO Pitch:
From 2.5V source, a 20 uA/octave
source for 8 octaves gives 15625 for R.
Divide this to a low impedance LPF section
with a ~1063 Hz filter.
No R value needed on SSI2130 sheet.



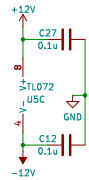
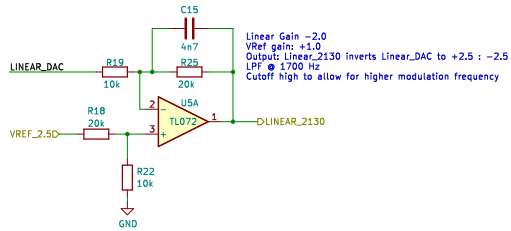
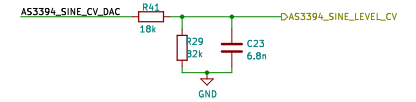
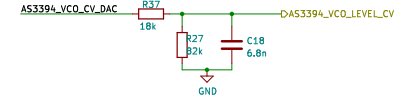
PWM: 795 Hz cutoff freq
note output impedance for next stage



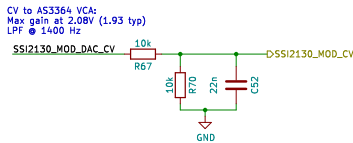
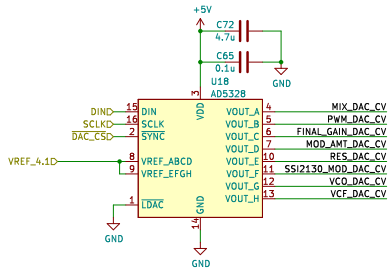
Mix DAC lines: ~159 Hz cutoff frequency.
Follow with current input control resistors



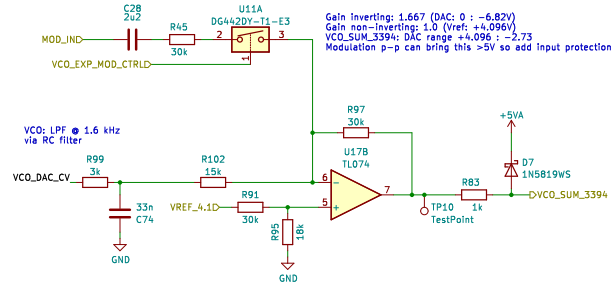
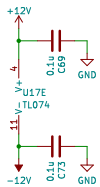
CV for AS3394 VCOs with max level 2.08V (or 1.93V typ)
Lowpass @ 1585 Hz



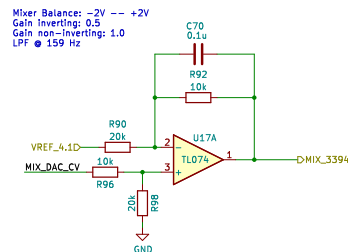
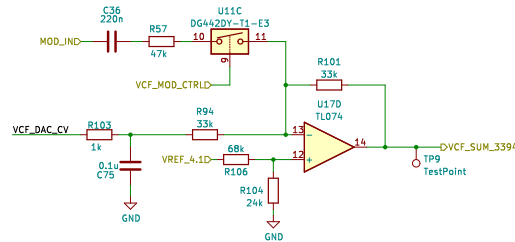
DAC: 4.096V Reference Voltage
B grade spec'd, C, D grades of LM4040 are good 'nuff
Programming: set to 1X gain, so DAC lines are 0 - 4.096V
VREF load: 45k



Decoupling caps close to pins



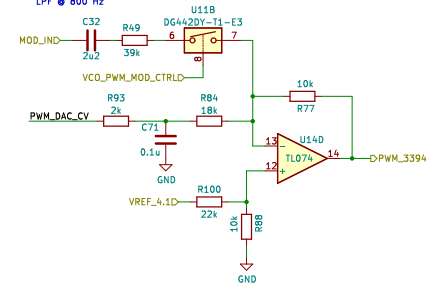
VCF: +2.1V : -1.9V
Gain Inverting: 0.97
Gain non-Inverting: 0.51
This should give -10 octaves from +20kHz to 23kHz
LPF @ 159 Hz
Modulation resistor value: value should give a wide mod range.
The modulator is AC coupled, the R value gives a very wide mod range



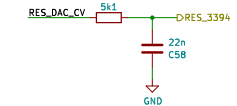
Sheet Load:
Vref 4.096: 45k || 20k || 92k || 48k || 32k ==> 7.4k

Filtering notes:
AD5328 datasheet DC output impedance: 0.5 ohm
AS3394 input impedances: control inputs "high impedance", < 0.5 nA input current

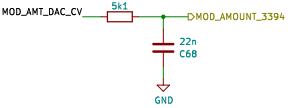
PwM Amount: datasheet is 0 : +2V for range
For DAC values this circuit gives 1.92V : -0.13 V
and yes, that's reverse where high DAC values give low width.
Reverse it in software.
The 22k value is used instead of a 20k to ensure a negative value for zero pulse.
LPF @ 800 Hz



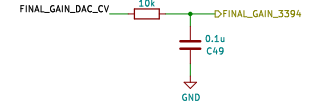
Resonance Amount: 0 -- 4.1V
LPF @ 1.4 kHz



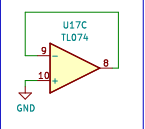
Modulation Amount: 0 -- 4.1V
LPF @ 1.4 kHz



Final Gain: 0 -- 4.1V
Given it's an audio VCA this
uses a lower cutoff: LPF @ 159 Hz



Unused



Zoxnoxious Engineering

Sheet: /DAC_3394/

File: dac_3394.kicad_sch

Title: Zoxnoxious Z5524 (SS12130/AS3394)

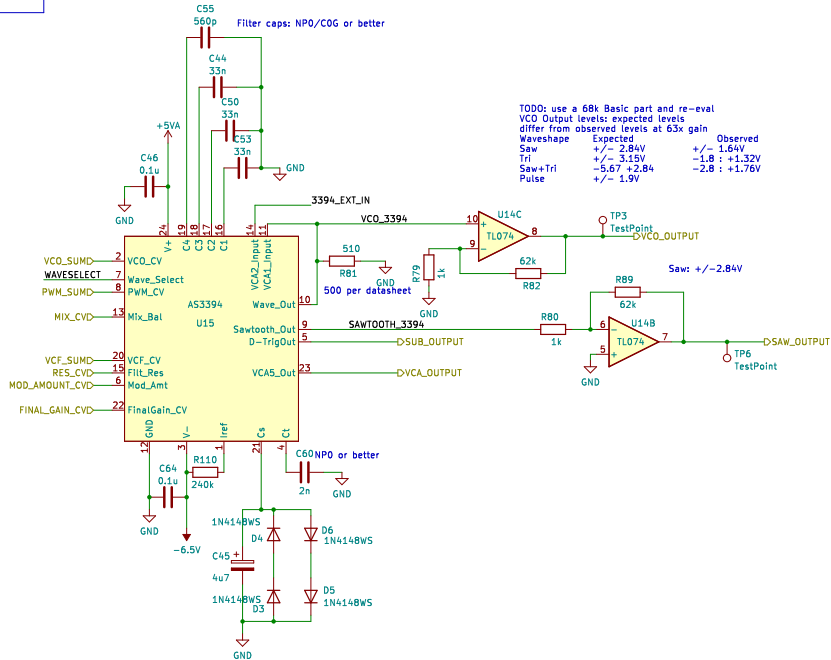
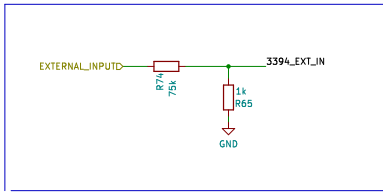
Size: B

Date: 2023-12-12

Rev: 0.3

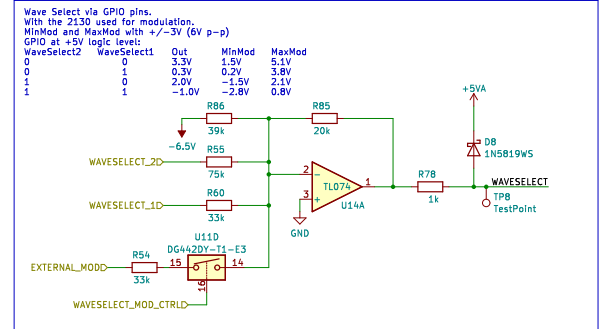
KiCad E.D.A. kicad 7.0.9

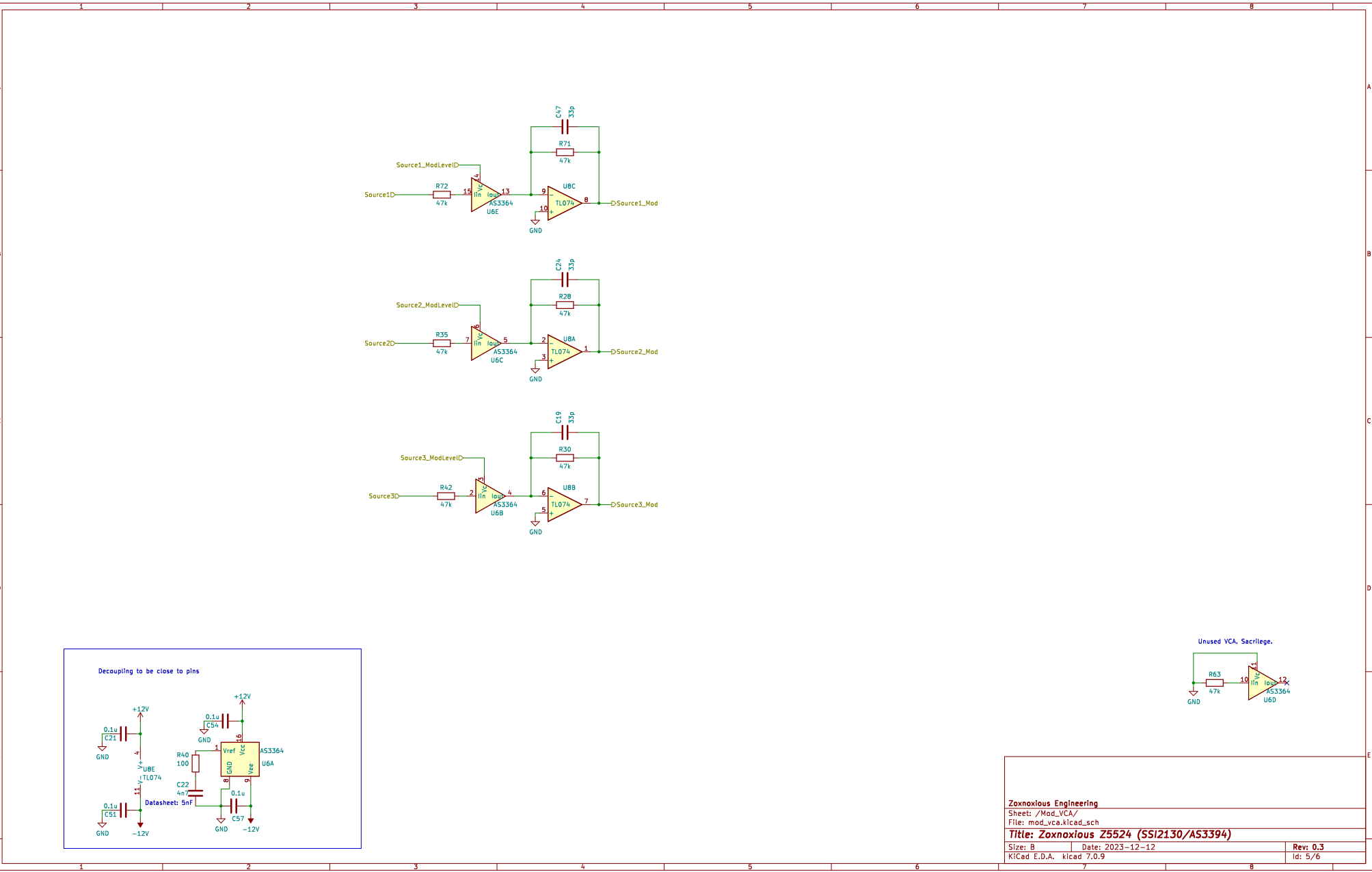
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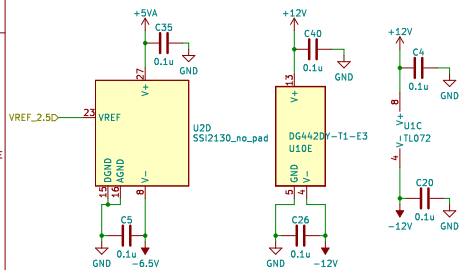
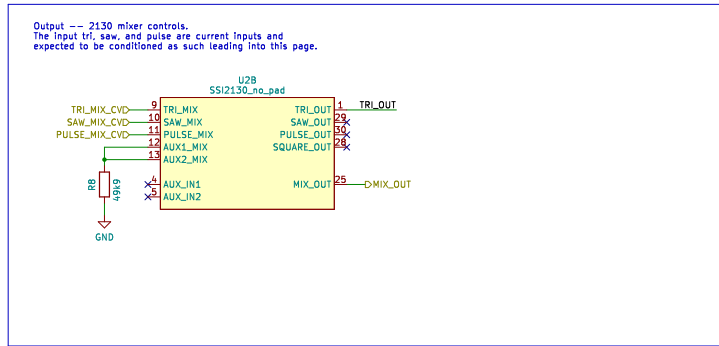
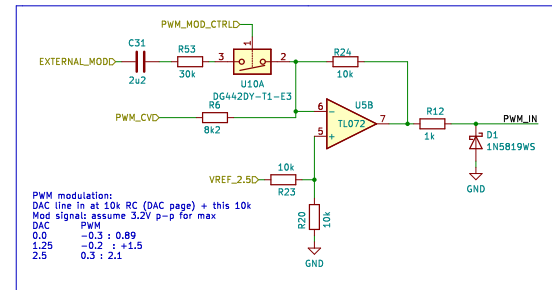
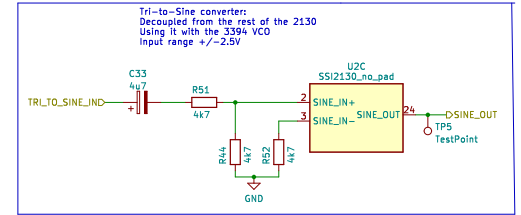
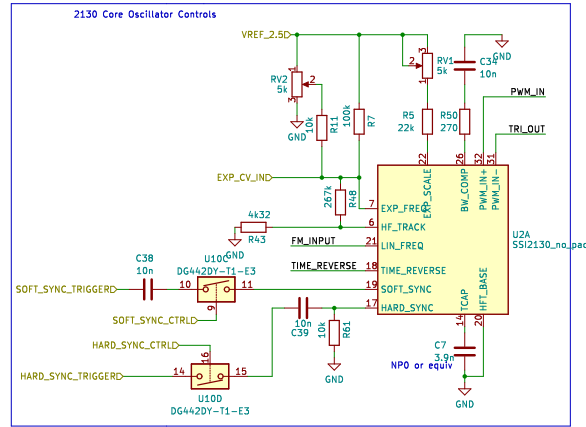
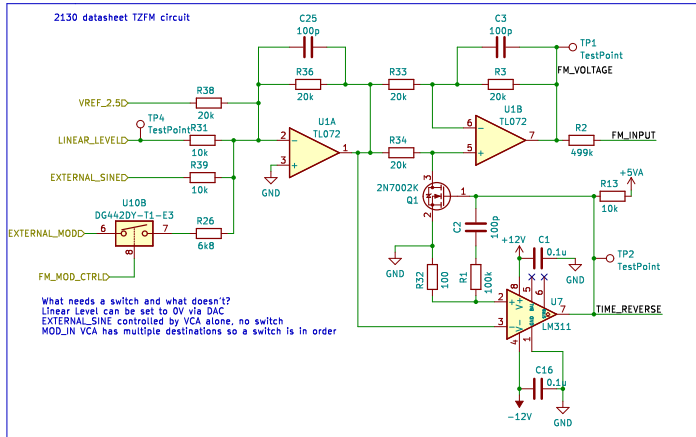


TODO: use a 68k Basic part and re-eval
VCO Output levels: expected levels
differ from observed levels at 63x gain

Waveshape	Expected	Observed
Saw	+/- 2.84V	+/- 1.64V
Tri	+/- 3.15V	+/- 1.8 : +1.32V
Saw+Tri	+5.67 : +2.84	-2.8 : +1.76V
Pulse	+/- 1.9V	







Sheet Load:
 Vref 2.5: 25k (100uA per SSI2130 datasheet) || 20k || 20k || 5k || 100k ==> 2x8
 LM7805: 3.75mA + 0.5mA
 LM337: -2.90mA