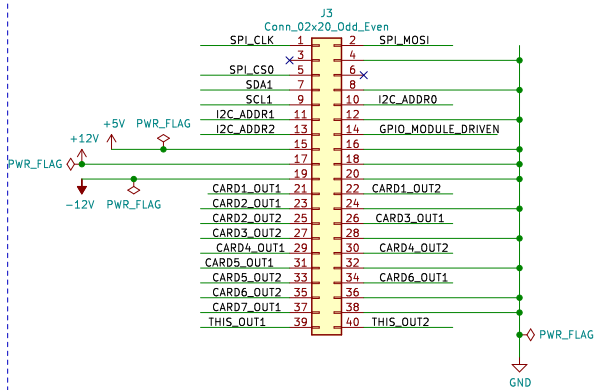
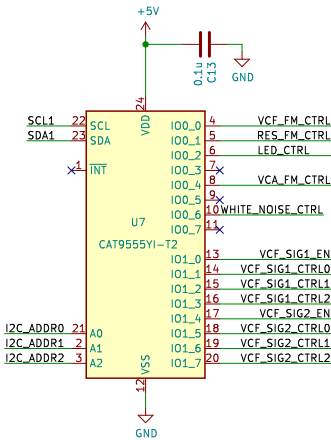


2x20 connection interface

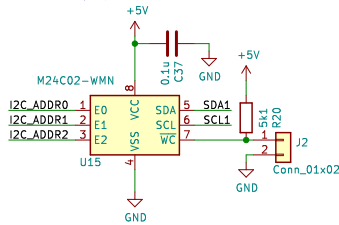


Interface: Zoxnoxious bus interface.
Digital on one side, power in the middle,
analog on the other.
This card will drive THIS_OUT1 and THIS_OUT2 and GPIO_MODULE_DRIVEN.
The other lines are tapped into for various functionality.

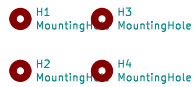
GPIO for switch control
I2C address 0100[addr2,addr1,addr0]



Board ID in EEPROM
I2C address 1010[addr2,addr1,addr0]
Write enable via jumper



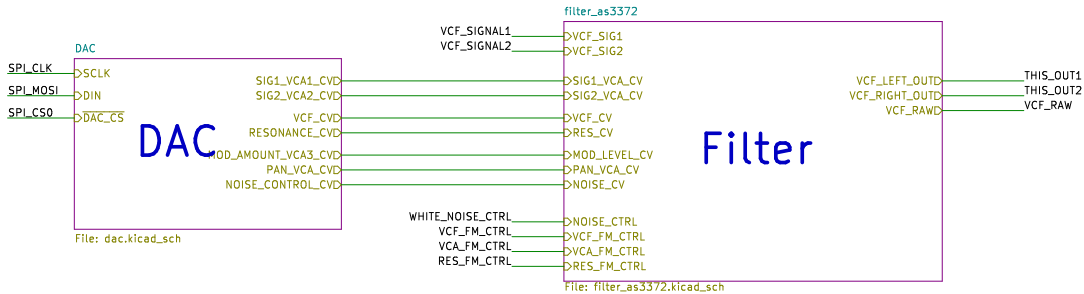
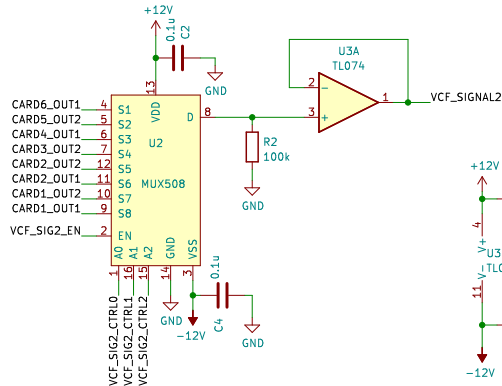
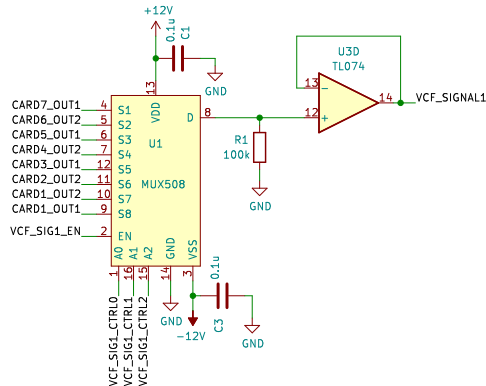
Mounting Holes



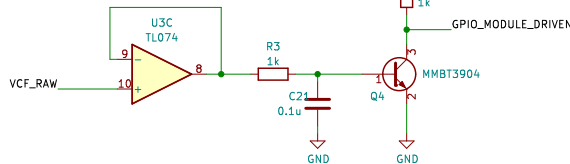
GND Test Point



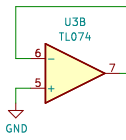
Signal select -- output selected signal in-phase



GPIO Module Driven:
Take VCF output to drive a switch.
Used to calibrate the noise source or resonance level.
And yes, it's inverted. You can do it. Cap gives a decent
capability to detect via interrupt whether a signal is being
generated or not.



Spares



Zoxnoxious Engineering

Sheet: /

File: as3372.kicad_sch

Title: Z3372 Signal Processing

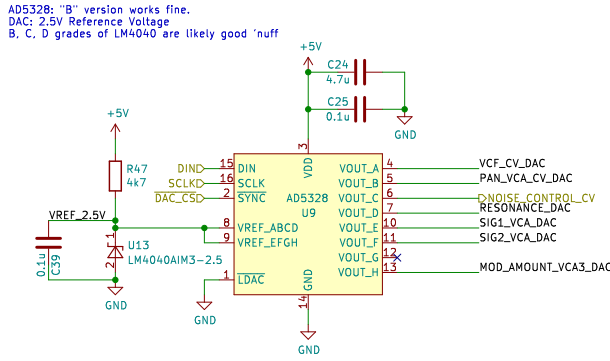
Size: B

Date: 2023-02-16

Rev: 0.2

KiCad E.D.A. kicad (6.0.7-1)-1

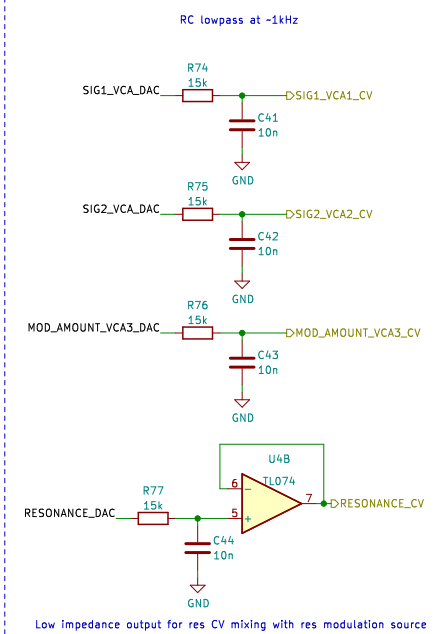
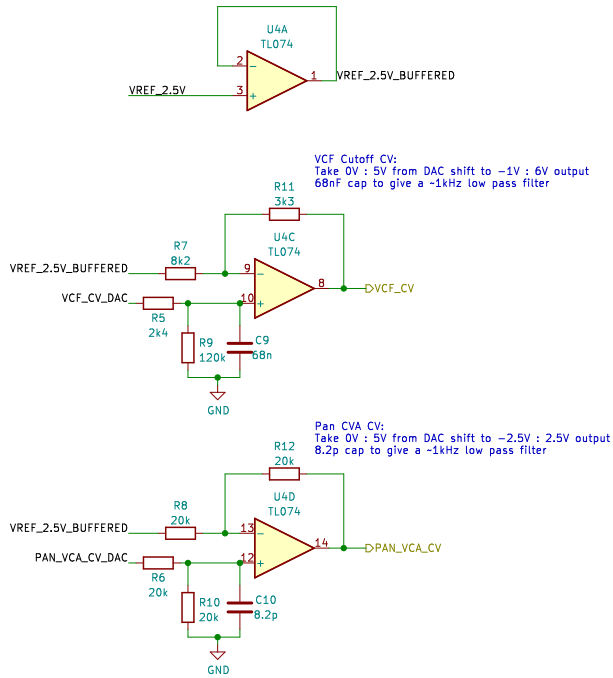
Id: 1/3



Control Voltage levels:
Sig1 VCA CV: 0–5V
Sig2 VCA CV: 0–5V
VCA Mod Level CV: 0–5V
Res CV: 0–5V
VCF CV: –1 – +6V
Pan VCA CV: –2.5 – +2.5V

The latter two use opamps to scale; everything else can be straight from DAC. These are slightly outside datasheet specs of 0:5 and –2:+2 to allow for some offset from modulation.

Decoupling



Zoxxious Engineering

Sheet: /DAC/

File: dac.kicad_sch

Title: Z3372 Signal Processing

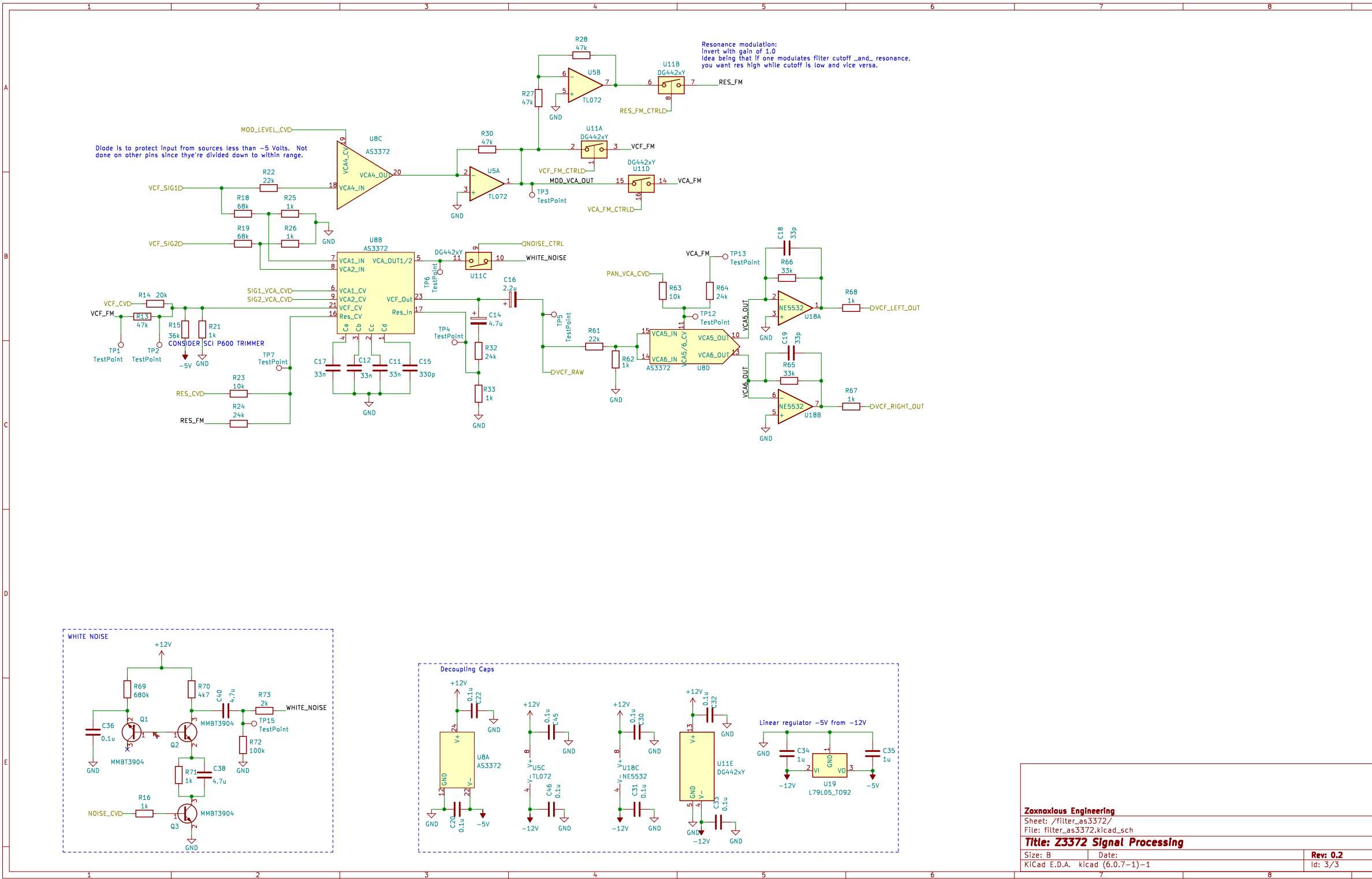
Size: B

Date:

KiCad E.D.A. kicad (6.0.7–1)–1

Rev: 0.2

Id: 2/3



Zoxnoxious Engineering

Sheet: /filter_as3372/
File: filter_as3372.kicad_sch

Title: Z3372 Signal Processing

Size: B
KiCad E.D.A. kicad (6.0.7-1)-1

Date:

Rev: 0.2

Id: 3/3