Brett Gurman

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EDUCATION:

- **Tufts University**, Medford, MA GPA: 3.43
  - • Bachelors of Science in Computer Science and Cognitive and Brain Sciences, expected May 2017

WORK AND RESEARCH EXPERIENCE:

- **Tufts Computer Science Department** | Medford, MA
  Teaching Assistant | September 2014 - Present
  - • Grade assignments for Data Structures and Introduction to Computer Science
  - • Teach computer laboratories for intermediate programming students
  - • Guide students in completion of major course assignments during office hours

- **JoMI (Boston Startup)** | Boston, MA
  IT Engineer | June 2015 - August 2015
  - • Created web content for a video-based medical journal startup company

- **Tufts Human-Robot Interaction Laboratory** | Medford, MA
  Research Assistant | November 2014 - August 2015
  - • Contribute to work searching for novel methods in machine learning including one-shot learning techniques, power of analogy, and moral systems

- **Tufts Electrical Engineering Department** | Medford, MA
  Research Assistant | June 2014 - September 2014
  - • Presented graduate-level papers to research group
  - • Collaborated on research project to find novel methods for tensor decomposition

EXTRACURRICULAR ACTIVITIES:

- **Tufts Varsity Swimming and Diving Team** | September 2013 - Present
  - • Attend daily 4-hour practices and weekly meets

SKILLS:

- **Programming Languages**: C, Python, C++, Assembly Language, Javascript, HTML, CSS, Scheme, Standard ML, Java, Bash, SQL, R,
- **Software**: Microsoft Office, GNU, Git, Linux, Heroku
- **Foreign Languages**: Chinese
- **Relevant Coursework**: Machine Structure and Assembly Language, Programming Languages, Computer Graphics, Data Structures, Web Programming, Computation Theory

EXAMPLE WORK:

- **Lost and Found App** | Fall 2015 (Tufts Fall 2015 Polyhack Hackathon)
  - • A flask- and postgresql-based web app that allows finders of lost items to advertise their finds and helps owners of lost items to recover their belongings

- **Universal Machine** | Fall 2014
  - • Designed and implemented a 32-bit segmented memory Universal Machine