Brian C. Schwedock

Soc Architect

Education

Carnegie Mellon University Pittsburgh, PA

Ph.D in Electrical and Computer Engineering 2017 - 2023

Thesis: Optimizing Data Movement Through Software Control of General-Purpose Hardware Caches

Advisor: NATHAN BECKMANN

Carnegie Mellon University Pittsburgh, PA

M.S. IN ELECTRICAL AND COMPUTER ENGINEERING 2017 - 2019

University of Southern California Los Angeles, CA

B.S. IN COMPUTER ENGINEERING AND COMPUTER SCIENCE (SUMMA CUM LAUDE) 2013 - 2017

MINOR IN MATHEMATICS

Awards

Best Paper nominee at ISCA	2022
NSF Graduate Research Fellowship	2019 - 2022
CMU ECE Ann and Martin McGuinn Graduate Fellowship (x2)	2019 - 2021
CMU CIT Bertucci Fellowship	2017 - 2020
USC Computer Engineering and Computer Science Outstanding Student Award	2017
USC Boeing Scholarship (x2)	2015 - 2017
USC Rose Hills Foundation Scholarship (x2)	2015 - 2017
JFS-David Rubenstein Memorial Scholarship (x4)	2013 - 2017
USC Moore Scholarship	2014 - 2015

Professional Experience

Samsung San Jose, CA

SOC ARCHITECT Sep 2023 - Present

- Research and development for the architecture of Exynos mobile SoCs.

Carnegie Mellon University Pittsburgh, PA

Graduate Research Assistant Aug 2017 - July 2023

• Researching in computer architecture and computer systems.

Google Pittsburgh, PA

Student Researcher Sep 2019 - Jan 2020

• Cloud Storage team. Extended internship optimizing in-memory caches.

Google New York, NY

Software Engineering Research Intern May - Aug, 2018 & 2019

 $\bullet \ \ {\it Cloud Storage team. Built simulator for in-memory database cache. Optimized cache performance.}$

General Atomics Aeronautical Systems Inc.
San Diego, CA

June - Aug 2017

Be'er Sheva, Israel

Software Engineering Intern
Software Flight Controls group. Developed test scripts for UAV flight controls testing.

USC Teamcore Research Group

Los Angeles, CA

Undergraduate Research Assistant Sep 2015 - May 2017

- Developed a linear program for PAWS, an app which solves a Stackelberg Security Game to combat poaching.
- Performed statistical analysis on crime data in Los Angeles.

Sami Shamoon College of Engineering

SOFTWARE ENGINEERING RESEARCH INTERN

June - Aug 2016

• Developed image processing enhancements in support of a Civil Engineering research project.

- Researched improvements for methodologies of unit testing.

ViaSat Carlsbad, CA

SOFTWARE ENGINEERING INTERN

• Built a testing infrastructure deployable in the cloud to test software systems through inconvenient testing.

Refereed Conference Publications _____

Tyr: Taming Dataflow Parallelism for Better Locality

MICRO 2024

May - Aug 2015

Nikhil Agarwal, Mitchell Fream, Souradip Ghosh, Brian C. Schwedock, Nathan Beckmann

Acceptance rate: 23%

Leviathan: A Unified System for General-Purpose Near-Data Computing

MICRO 2024

Brian C. Schwedock, Nathan Beckmann

Acceptance rate: 23%

täkō: A Polymorphic Cache Hierarchy for General-Purpose Optimization of Data Movement ISCA 2022 (Best Paper nominee)

Brian C. Schwedock, Piratach Yoovidhya, Jennifer Seibert, Nathan Beckmann

Acceptance rate: 17%

Jumanji: The Case for Dynamic NUCA in the Datacenter

MICRO 2020

Brian C. Schwedock, Nathan Beckmann

Acceptance rate: 19%

Refereed Journal Publications

UDIR: Towards a Unified Compiler Framework for Reconfigurable Dataflow Architectures

IEEE CAL 2024

Nikhil Agarwal, Mitchell Fream, Souradip Ghosh, *Brian C. Schwedock*, Nathan Beckmann

Kobold: Simplified Cache Coherence for Cache-Attached AcceleratorsJennifer Brana, *Brian C. Schwedock*, Yatin A. Manerkar, Nathan Beckmann

IEEE CAL 2023

PAWS - A Deployed Game-Theoretic Application to Combat Poaching

Al Magazine 2017

Fei Fang, Thanh H. Nguyen, Rob Pickles, Wai Y. Lam, Gopalasamy R. Clements, Bo An, Amandeep Singh, *Brian C. Schwedock*, Milind Tambe, Andrew Lemieux

Refereed Workshop Publications _____

UDIR: Towards a Unified Compiler Framework for Reconfigurable Dataflow Architectures

WDDSA @ MICRO 2023

Nikhil Agarwal, Mitchell Fream, Souradip Ghosh, *Brian C. Schwedock*, Nathan Beckmann

Kobold: Simplified Cache Coherence for Cache-Attached Accelerators

WDDSA @ MICRO 2022

Jennifer Brana, Brian C. Schwedock, Yatin A. Manerkar, Nathan Beckmann

Talks_

Optimizing Data Movement through Software Control of General-Purpose CPU Caches täkō: A Polymorphic Cache Hierarchy for General-Purpose Optimization of Data Movement

Qualcomm, 3 Jan 2023 PDL Retreat, Pittsburgh,

8 Nov 2022

täkō: A Polymorphic Cache Hierarchy for General-Purpose Optimization of Data Movement Jumanji: The Case for Dynamic NUCA in the Datacenter ISCA, 20 June 2022 MICRO, 20 Oct 2020

Teaching

18-746 Storage Systems

CMU

TEACHING ASSISTANT

Fall 2021 CMU

18-746 Storage Systems TEACHING ASSISTANT

Fall 2020

ITP-435 Professional C++

USC

TEACHING ASSISTANT

Spring 2017

EE-355 Software Design for Electrical Engineers

TEACHING ASSISTANT

Spring 2016

USC

EE-355 Software Design for Electrical Engineers

USC Spring 2015

TEACHING ASSISTANT

Mentoring

Jennifer Brana (B.S.) Piratach Yoovidhya (B.S.) Jennifer Seibert (B.S.) Hanchen Yang (M.S.) Amolak Nagi (B.S.) Summer 2022 - Summer 2023 Fall 2020 - Spring 2022 Summer 2021 Fall 2019 - Spring 2020 Fall 2017 - Spring 2018