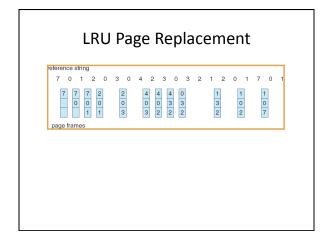
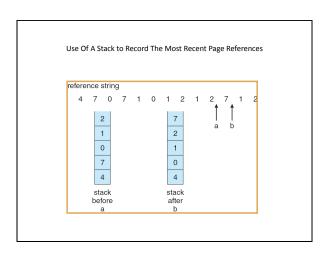
Least Recently Used (LRU) Algorithm

- Reference string: 1, 2, 3, 4 1 255, 1, 2, 3, 4, 5 2 3 5 4 4 3
- Counter implementation
 - Every page entry has a counter; every time page is referenced through this entry, copy the clock into the counter
 - When a page needs to be changed, look at the counters to determine which are to change



LRU Algorithm (Cont.)

- Stack implementation keep a stack of page numbers in a double link form:
 - Page referenced:
 - move it to the top
 - requires 6 pointers to be changed
 - No search for replacement



LRU Approximation Algorithms

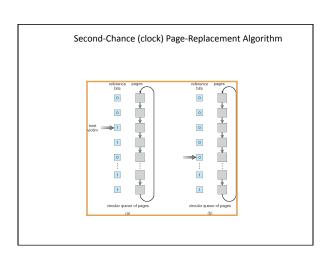
- Reference bit
 - With each page associate a bit, initially = 0 When page is referenced bit set to 1

 - Replace the one which is 0 (if one exists). We do not know the order, however.
- · Second chance
 - Need reference bit

 - Clock replacement

 If page to be replaced (in clock order) has reference bit = 1 then:

 - leave page in memory
 replace next page (in clock order), subject to same rules



Counting Algorithms

- Keep a counter of the number of references that have been made to each page
- LFU Algorithm: replaces page with smallest count
- MFU Algorithm: based on the argument that the page with the smallest count was probably just brought in and has yet to be used

Allocation of Frames

- Each process needs minimum number of pages
- Example: IBM 370 6 pages to handle SS MOVE instruction:
 - instruction is 6 bytes, might span 2 pages
 - 2 pages to handle from
 - 2 pages to handle to
- Two major allocation schemes
 - fixed allocation
 - priority allocation

Fixed Allocation

- Equal allocation For example, if there are 100 frames and 5 processes, give each process 20 frames.
- Proportional allocation Allocate according to the size of process
- $-S = \sum S_i$
- -m = total number of frames
- $-a_i$ = allocation for $p_i = \frac{s_i}{s} \times m$
 - m = 64
 - $s_i = 10$
 - $s_2 = 127$
 - $a_1 = \frac{10}{137} \times 64 \approx 5$
 - $a_2 = \frac{127}{137} \times 64 \approx 59$

Priority Allocation

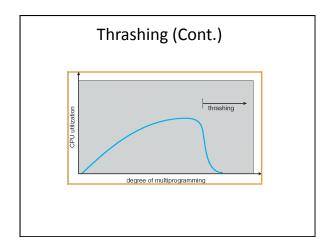
- Use a proportional allocation scheme using priorities rather than size
- If process P_i generates a page fault,
 - select for replacement one of its frames
 - select for replacement a frame from a process with lower priority number

Global vs. Local Allocation

- Global replacement process selects a replacement frame from the set of all frames; one process can take a frame from another
- Local replacement each process selects from only its own set of allocated frames

Thrashing

- If a process does not have "enough" pages, the page-fault rate is very high. This leads to:
 - low CPU utilization
 - operating system thinks that it needs to increase the degree of multiprogramming
 - another process added to the system
- Thrashing = a process is busy swapping pages in and out



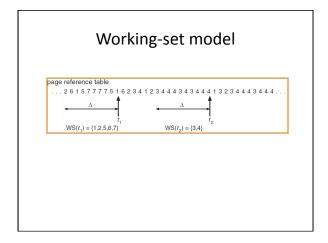
Demand Paging and Thrashing

- Why does demand paging work? Locality model
 - Process migrates from one locality to another
 - Localities may overlap
- Why does thrashing occur?
 Σ size of locality > total memory size

Locality In A Memory-Reference Pattern

Working-Set Model

- Δ = working-set window = a fixed number of page references
 - Example: 10,000 instruction
- WSS_i (working set of Process P_i) = total number of pages referenced in the most recent Δ (varies in time)
 - if Δ too small will not encompass entire locality
 - if Δ too large will encompass several localities
 - if Δ = ∞ \Rightarrow will encompass entire program
- $D = \Sigma WSS_i \equiv \text{total demand frames}$
- if $D > m \Rightarrow$ Thrashing
- Policy if D > m, then suspend one of the processes

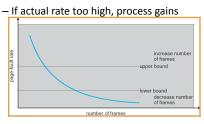


Keeping Track of the Working Set

- Approximate with interval timer + a reference bit
- Example: Δ = 10,000
 - Timer interrupts after every 5000 time units
 - Keep in memory 2 bits for each page
 - Whenever a timer interrupts copy and sets the values of all reference bits to 0
 - − If one of the bits in memory = $1 \Rightarrow$ page in working set
- Why is this not completely accurate?
- Improvement = 10 bits and interrupt every 1000 time units

Page-Fault Frequency Scheme

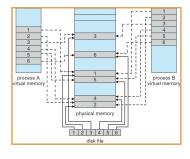
- Establish "acceptable" page-fault rate
 - If actual rate too low, process loses frame



Memory-Mapped Files

- Memory-mapped file I/O allows file I/O to be treated as routine memory access by mapping a disk block to a page in memory
- A file is initially read using demand paging. A page-sized portion of the file is read from the file system into a physical page. Subsequent reads/writes to/from the file are treated as ordinary memory accesses.
- Simplifies file access by treating file I/O through memory rather than read() write() system calls
- Also allows several processes to map the same file allowing the pages in memory to be shared

Memory Mapped Files



Memory-Mapped Files in Java

```
import java.io.*;
import java.nio.*;
import java.nio.*;
public class MemoryMapReadOnly
{

// Assume the page size is 4 KB
public static final int PAGE SIZE = 4096;
public static void main(String args[]) throws IOException {
RandomAccessFile inFile = new RandomAccessFile(args[0],*r*);
FileChannel in = inFile_getChannel();
MappedByteBuffer mappedBuffer =
in.map(FileChannel.MapMode.READ ONLY, 0, in.size());
long numPages = in.size() / (long)PAGE SIZE;
if (in.size() % PAGE SIZE > 0)
++numPages;
```

Memory-Mapped Files in Java (cont)

Other Issues -- Prepaging

- Prepaging
 - To reduce the large number of page faults that occurs at process startup
 - Prepage all or some of the pages a process will need, before they are referenced
 - But if prepaged pages are unused, I/O and memory was wasted
 - Assume s pages are prepaged and lpha of the pages is used
 - Is cost of s * α save pages faults > or < than the cost of prepaging
 - s * (1- α) unnecessary pages?
 - α near zero ⇒ prepaging loses

Other Issues – Page Size

- Page size selection must take into consideration:
 - fragmentation
 - table size
 - I/O overhead
 - locality

Other Issues - TLB Reach

- TLB Reach The amount of memory accessible from the TLB
- TLB Reach = (TLB Size) X (Page Size)
- Ideally, the working set of each process is stored in the TLB.

 Otherwise there is a high degree of page faults.
- Increase the Page Size. This may lead to an increase in fragmentation as not all applications require a large page size
- Provide Multiple Page Sizes. This allows applications that require larger page sizes the opportunity to use them without an increase in fragmentation.

Other Issues – Program Structure

- · Program structure
 - Int[128,128] data;
 - Each row is stored in one page
 - Program 1

```
for (j = 0; j <128; j++)
for (i = 0; i < 128; i++)
data[i,j] = 0;
```

128 x 128 = 16,384 page faults

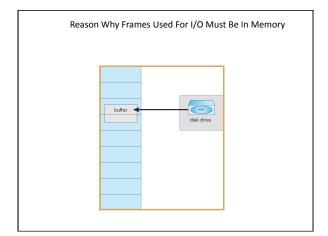
- Program 2

for (i = 0; i < 128; i++) for (j = 0; j < 128; j++) data[i,j] = 0;

128 page faults

Other Issues – I/O interlock

- I/O Interlock Pages must sometimes be locked into memory
- Consider I/O. Pages that are used for copying a file from a device must be locked from being selected for eviction by a page replacement algorithm.



Operating System Examples

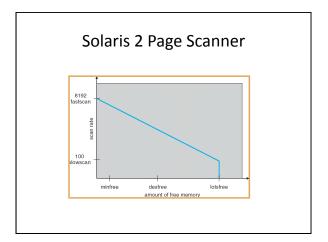
- Windows XP
- Solaris

Windows XP

- Uses demand paging with clustering. Clustering brings in pages surrounding the faulting page.
- Processes are assigned working set minimum and working set maximum
- Working set minimum is the minimum number of pages the process is guaranteed to have in memory
- A process may be assigned as many pages up to its working set maximum
- When the amount of free memory in the system falls below a threshold, automatic working set trimming is performed to restore the amount of free memory.
- Working set trimming removes pages from processes that have pages in excess of their working set minimum

Solaris

- Maintains a list of free pages to assign faulting processes
- Lotsfree threshold parameter (amount of free memory) to begin paging
- Desfree threshold parameter to increasing paging
- Minfree threshold parameter to being swapping
- Paging is performed by pageout process
- Pageout scans pages using modified clock algorithm
- Scanrate is the rate at which pages are scanned. This ranges from slowscan to fastscan
- Pageout is called more frequently depending upon the amount of free memory available



End of Chapter 9