**CTD41**

**EMULATOR**

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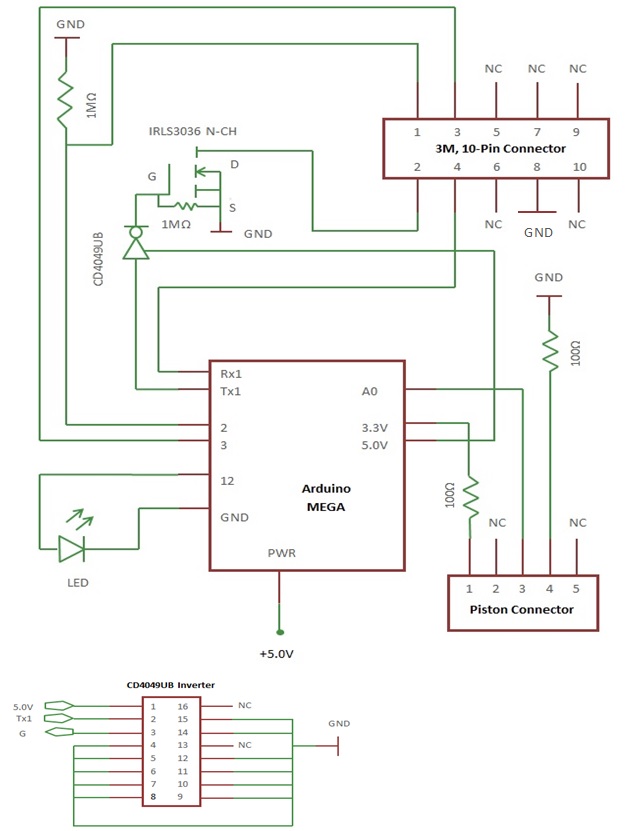
# 1 Description

This document describes the hardware and software functionality of the simulator. The purpose of this document is to provide a detailed description of the hardware, demonstrate how the simulator will be used for testing, and list the expectations and goals of the project. The document will also show how these goals and expectations were met.

1.1

# 2 Block Diagram

Below is the block diagram of the simulator



# 3 Functional Description

The simulator will be the means of performing mission tests for the Teledyne Webb Research Float product that runs on the APF-9 controller board. The simulator will replace the CTD for testing purposes.

The major functional blocks of the simulator are as follows:

* Microprocessor
  + Flash = 256KB
  + SRAM = 8KB
  + EEPROM = 4KB
* Power Management
  + 5V
* Communications
  + Serial (x4)
    - Serial0 for USB-to-TTL Serial chip
    - Serial1 TTL serial data with APF-9